

SAW Components

Data Sheet B3750





SAW Components

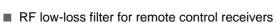
Low-loss Filter

B3750 433,92 MHz

Data Sheet

Features

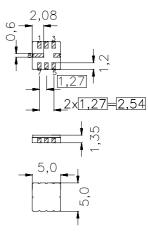
Ceramic package QCC8C



- Package for Surface Mounted Technology (SMT)
- Balanced and unbalanced operation possible
- Passivation layer: Protec



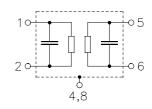
Ni, gold plated



typ. dimensions in mm, approx. weight 0,1 g

Pin configuration

1	Input Ground
2	Input
5	Output
6	Output Ground
4,8	Case - Ground
3,7	to be grounded



Туре	Ordering code	Marking and package according to	Packing according to
B3750	B39431-B3750-U310	C61157-A7-A56	F61074-V8070-Z000

Electrostactic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	T _A	-45/+120	°C	
Storage temperature range	$T_{\rm stg}$	-45/+120	°C	
DC voltage	V _{DC}	6	V	
Source power	P_S	10	dBm	source impedance 50 Ω

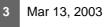
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SAW Components				400	B3750
Low-loss Filter				433,92 MHz	
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Characteristics					
Reference temperature: T_A	= -40	+95°C			
			hing network		
Terminating load impedance: Z _L	= 50 Ω	and matc	hing network	(
		min.	typ.	max.	
Center frequency	f _C	_	433,92	_	MHz
(center frequency between 3 dB points)	-				
Minimum insertion attenuation	$lpha_{min}$				
(including losses in matching network)					
433,76 434,08 MHz		_	2,0	3,0	dB
Pass band (relative to α_{\min})					
433,76 434,08 MHz		_	0,5	2,0	dB
433,74 434,10 MHz		_	0,7	3,0	dB
433,70 434,14 MHz		_	1,0	6,0	dB
Pass bandwidth					
α _{rel} ≤ 3 dB		0,64	0,70	0,76	MHz
Relative attenuation (relative to α_{min})	α_{rel}				
10,00 414,00 MHz		55	62	_	dB
414,00 424,50 MHz		48	55	_	dB
424,50 431,72 MHz		31	35	_	dB
431,72 432,12 MHz		39	44	_	dB
432,12 433,10 MHz		8	11	—	dB
434,92 442,00 MHz		10	14	—	dB
442,00 550,00 MHz		36	44	_	dB
550,00 700,00 MHz 700,00 805,00 MHz		55	60 45	_	dB dB
805,00 805,00 MHz		40 60	45 70		dВ
805,00 1000,00 MHz		00	10	_	UD
Impedance for pass band matching ²⁾					
Input: $Z_{\rm IN} = R_{\rm IN} \parallel C_{\rm IN}$		_	310 2,2	_	Ω pl
Output: $Z_{OUT} = R_{OUT} C_{OUT}$			310 2,2		Ω pf

²⁾ Impedance for passband matching bases on an ideal, perfect matching of the SAW filter to source- and to load impedance (here 50 Ohm). After the SAW filter is removed and input impedance into the input matching / output matching network is calculated.

The conjugate complex value of these characteristic impedances are the input and output impedances for flat passband. For more details, we refer to EPCOS application note #4.

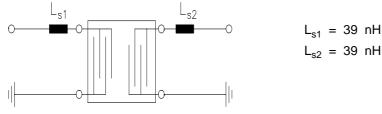




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Matching network to 50 Ω (element values depend on pcb layout and equivalent circuit)



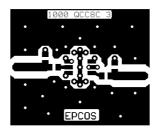
Minimising the crosstalk

For a good ultimate rejection a low crosstalk is necessary. Low crosstalk can be realised with a good RF layout. The major crosstalk mechanism is caused by the "ground-loop" problem.

Grounding loops are created if input-and output transducer GND are connected on the top-side of the PCB and fed to the system grounding plane by a common via hole. To avoid the common ground path, the ground pin of the input- and output transducer are fed to the system ground plane (bottom PCB plane) by their own via hole. The transducers' grounding pins should be isolated from the upper grounding plane.

A common GND inductivity of 0.5nH degrades the ultimate rejection (crosstalk) by 20dB.

The optimised PCB layout, including matching network for transformation to 50 Ohm, is shown here. In this PCB layout the grounding loops are minimised to realise good ultimate rejection.



Optimised PCB layout for SAW filters in QCC8C package, pinning 2,5 (top side, scale 1:1)

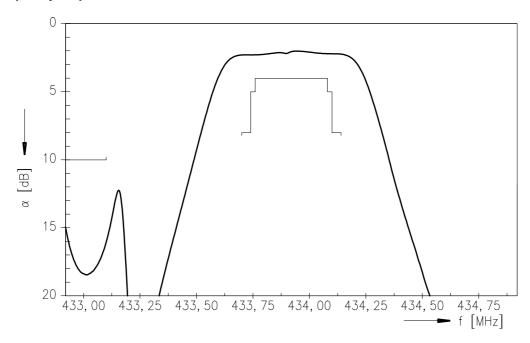
The bottom side is a copper plane (system ground area). The input and output grounding pins are isolated and connected to the common ground by separated via holes.

For good contact of the upper grounding area with the lower side it is necessary to place enough via holes.

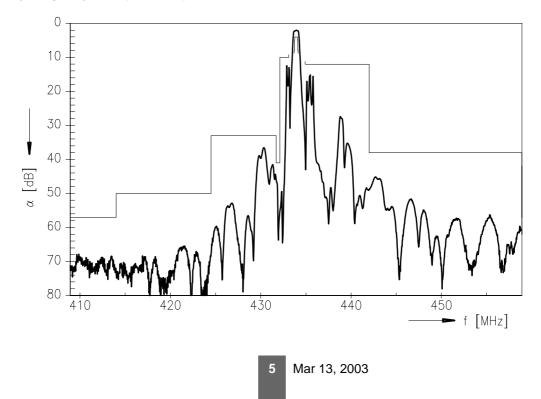


Data Sheet

Frequency response



Frequency response (wideband)

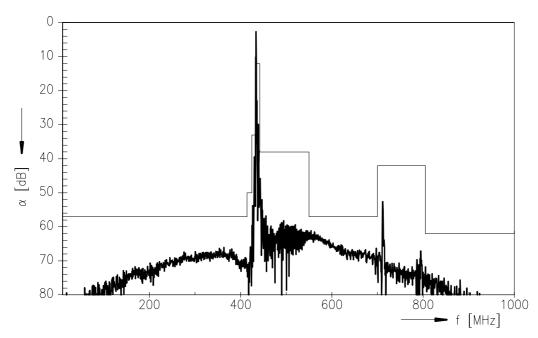




Low-loss Filter

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Frequency response (ultimate rejection)



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