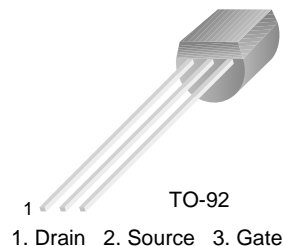


# U1897

## N-Channel JFET Switch

### Features

- This device is designed for low level analog switching, sample and hold circuits and chopper stabilized amplifiers.
- Sourced from Process 51.
- See J111 for characteristics.



### Absolute Maximum Ratings \* $T_a=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
$V_{DG}$	Drain-Gate Voltage	40	V
$V_{GS}$	Gate-Source Voltage	-40	V
$I_{GF}$	Forward Gate Current	50	mA
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 ~ 150	$^{\circ}\text{C}$

\* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES:

- 1) These ratings are based on a maximum junction temperature of  $150^{\circ}\text{C}$ .
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

### Thermal Characteristics\* $T_a=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
$P_D$	Total Device Dissipation Derate above $25^{\circ}\text{C}$	625 5.0	mW $\text{mW}/^{\circ}\text{C}$
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	$^{\circ}\text{C}/\text{W}$

\* Device mounted on FR-4 PCB  $1.6'' \times 1.6'' \times 0.06''$

**Electrical Characteristics \***  $T_C = 25^\circ\text{C}$  unless otherwise noted




Symbol	Parameter	Conditions	Min.	Max	Units
<b>Off Characteristics</b>					
$V_{(BR)GS}$	Gate-Source Breakdown Voltage	$I_G = -1.0\ \mu\text{A}$ , $V_{DS} = 0$	-40		V
$V_{GS(off)}$	Gate-Source Cutoff Voltage	$V_{DS} = 20\ \text{V}$ , $I_D = 1.0\ \text{nA}$	-5.0	-10	V
$I_{DGO}$	Drain-Gate Leakage Current	$V_{DG} = 20\ \text{V}$ , $I_S = 0$		-200	pA
<b>On Characteristics</b>					
$I_{DSS}$	Zero-Gate Voltage Drain Current *	$V_{DS} = 20\ \text{V}$ , $V_{GS} = 0$	30		mA
$r_{DS(on)}$	Static Drain-Source On Resistance	$I_D = 1.0\ \text{mA}$ , $V_{GS} = 0$		30	$\Omega$
<b>Small Signal Characteristics</b>					
$r_{ds(on)}$	Drain-Source On Resistance	$V_{DS} = V_{GS} = 0$ , $f = 1.0\ \text{kHz}$		30	$\Omega$
$C_{iss}$	Input Capacitance	$V_{DS} = 20$ , $V_{GS} = 0$ , $f = 1.0\ \text{MHz}$		16	pF
$C_{rss}$	Reverse Transfer Capacitance	$V_{GS} = -20\ \text{V}$ , $f = 1.0\ \text{MHz}$		3.5	pF
<b>Switching Characteristics</b>					
$t_{on}$	Turn-On Time	$I_{D(on)} = 6.6\ \text{mA}$		25	ns
$t_{off}$	Turn-Off Time	$V_{GS(off)} = 12.0\ \text{V}$		40	ns

\* Pulse Test: Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ **NOTES:**

1. These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.
2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
3. These ratings are based on a maximum junction temperature of 150degrees C.

## TRADEMARKS

The following are registered and unregistered trademarks and service marks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx <sup>®</sup>	Green FPS <sup>™</sup>	Power247 <sup>®</sup>	SuperSOT <sup>™</sup> -8
Build it Now <sup>™</sup>	Green FPS <sup>™</sup> e-Series <sup>™</sup>	POWEREDGE <sup>®</sup>	SyncFET <sup>™</sup>
CorePLUS <sup>™</sup>	GTO <sup>™</sup>	Power-SPM <sup>™</sup>	The Power Franchise <sup>®</sup>
CROSSVOLT <sup>™</sup>	i-Lo <sup>™</sup>	PowerTrench <sup>®</sup>	
CTL <sup>™</sup>	IntelliMAX <sup>™</sup>	Programmable Active Droop <sup>™</sup>	TinyBoost <sup>™</sup>
Current Transfer Logic <sup>™</sup>	ISOPLANAR <sup>™</sup>	QFET <sup>®</sup>	TinyBuck <sup>™</sup>
EcoSPARK <sup>®</sup>	MegaBuck <sup>™</sup>	QS <sup>™</sup>	TinyLogic <sup>®</sup>
	MICROCOUPLER <sup>™</sup>	QT Optoelectronics <sup>™</sup>	TINYOPTO <sup>™</sup>
Fairchild <sup>®</sup>	MicroFET <sup>™</sup>	Quiet Series <sup>™</sup>	TinyPower <sup>™</sup>
Fairchild Semiconductor <sup>®</sup>	MicroPak <sup>™</sup>	RapidConfigure <sup>™</sup>	TinyPWM <sup>™</sup>
FACT Quiet Series <sup>™</sup>	MillerDrive <sup>™</sup>	SMART START <sup>™</sup>	TinyWire <sup>™</sup>
FACT <sup>®</sup>	Motion-SPM <sup>™</sup>	SPM <sup>®</sup>	μSerDes <sup>™</sup>
FAST <sup>®</sup>	OPTOLOGIC <sup>®</sup>	STEALTH <sup>™</sup>	UHC <sup>®</sup>
FastvCore <sup>™</sup>	OPTOPLANAR <sup>®</sup>	SuperFET <sup>™</sup>	UniFET <sup>™</sup>
FPST <sup>™</sup>		SuperSOT <sup>™</sup> -3	VCX <sup>™</sup>
FRFET <sup>®</sup>	PDP-SPM <sup>™</sup>	SuperSOT <sup>™</sup> -6	
Global Power Resource <sup>SM</sup>	Power220 <sup>®</sup>		

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.