

# **TN6727A**



# **PNP General Purpose Amplifier**

This device is designed for general purpose medium power amplifiers and switches requiring collector currents to 1A. Sourced from Process 77. See TN6726A for characteristics.

## Absolute Maximum Ratings\*

 $T_{A\,=\,25^{\circ}\text{C unless otherwise noted}}$ 

| Symbol               | Parameter  | Value       | Units |
|----------------------|--|-------------|-------|
| V <sub>CES</sub>     | Collector-Emitter Voltage                        | 40          | V     |
| V <sub>CBO</sub>     | Collector-Base Voltage                           | 50          | V     |
| V <sub>EBO</sub>     | Emitter-Base Voltage                             | 5           | V     |
| Ic                   | Collector Current - Continuous                   | 1.5         | Α     |
| T <sub>J, Tstg</sub> | Operating and Storage Junction Temperature Range | -55 to +150 | °C    |

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES:

### Thermal Characteristics \_\_\_\_ T\_A = 25°C unless otherwise noted

| Symbol            | Characteristic                             | Max     | Units      |
|-------------------|--|---------|------------|
|                   |  | TN6727A |            |
| P <sub>D</sub>    | Total Device Dissipation Derate above 25°C | 1<br>8  | W<br>mW/°C |
| R <sub>θ</sub> JC | Thermal Resistance, Junction to Case       | 50      | °C/W       |
| R <sub>θJA</sub>  | Thermal Resistance, Junction to Ambient    | 125     | °C/W       |

<sup>1)</sup> These ratings are based on a maximum junction temperature of 150°C.

<sup>2)</sup> These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

## **PNP General Purpose Amplifier**

(continued)

## **Electrical Characteristics**

 $T_{\text{A}\,=\,25^{\circ}\text{C}\,\text{unless otherwise noted}}$ 

| Symbol               | Parameter                            | Test Conditions   | Min            | Max | Units |
|----------------------|--------------------------------------|---|----------------|-----|-------|
| OFF CHA              | RACTERISTICS                         |   |                |     |       |
| BV <sub>CEO</sub>    | Collector-Emitter Breakdown Voltage  | I <sub>C</sub> = 10 mA  | 40             |     | V     |
| BV <sub>CBO</sub>    | Collector-Base Breakdown Voltage     | I <sub>C</sub> = 1 mA   | 50             |     | V     |
| BV <sub>EBO</sub>    | Emitter-Base Breakdown Voltage       | I <sub>E</sub> = 1 mA   | 5              |     | V     |
| Ісво                 | Collector Cutoff Current             | V <sub>CB</sub> = 50 V  |                | 100 | nA    |
| I <sub>EBO</sub>     | Emitter Cutoff Current               | V <sub>EB</sub> = 5 V   |                | 100 | nA    |
| ON CHAF              | RACTERISTICS*                        |   |                |     |       |
| h <sub>FE</sub>      | DC Current Gain                      | $I_{C} = 10 \text{ mA}, V_{CE} = 1 \text{ V}$ $I_{C} = 100 \text{ mA}, V_{CE} = 1 \text{ V}$ $I_{C} = 1A, V_{CE} = 1 \text{ V}$ | 55<br>60<br>50 | 250 | -     |
| V <sub>CE(sat)</sub> | Collector-Emitter Saturation Voltage | I <sub>C</sub> = 1 A, I <sub>B</sub> = 100 mA   |                | 0.5 | V     |
| V <sub>BE(on)</sub>  | Base-Emitter On Voltage              | I <sub>C</sub> = 1 A, V <sub>CE</sub> = 1 V   |                | 1.2 | V     |
| SMALL SI             | GNAL CHARACTERISTICS                 |   |                |     |       |
| C <sub>cb</sub>      | Output Capacitance                   | V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1MHz  |                | 30  | pF    |
| h <sub>fe</sub>      | Small Signal Current Gain            | I <sub>C</sub> = 50 mA,V <sub>CE</sub> = 10 V, f=20MHz  | 2.5            | 25  | -     |
|                      | 1                                    | I .   |                |     |       |

<sup>\*</sup>Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  1.0%

### **TRADEMARKS**

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

FACT $^{\text{TM}}$  QFET $^{\text{TM}}$  FACT Quiet Series $^{\text{TM}}$  QS $^{\text{TM}}$ 

 $\begin{array}{lll} \mathsf{FAST}^{\circledast} & \mathsf{Quiet}\,\mathsf{Series^{\mathsf{TM}}} \\ \mathsf{FASTr^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}\text{-}3} \\ \mathsf{GTO^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}\text{-}6} \\ \mathsf{HiSeC^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}\text{-}8} \\ \end{array}$ 

#### **DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

### PRODUCT STATUS DEFINITIONS

### **Definition of Terms**

| Datasheet Identification | Product Status            | Definition  |
|--------------------------|---------------------------|---|
| Advance Information      | Formative or<br>In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.  |
| Preliminary              | First Production          | This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| No Identification Needed | Full Production           | This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.   |
| Obsolete                 | Not In Production         | This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.   |