

TN4033A

FAIRCHILD SEMICONDUCTOR TM



PNP General Purpose Amplifier

This device is designed for general purpose amplifier and switching applications at currents to 500 mA and collector voltages up to 70V. Sourced from Process 67.

Absolute Maximum Ratings* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CEO}	Collector-Emitter Voltage	80	V
V _{CBO}	Collector-Base Voltage	80	V
V _{EBO}	Emitter-Base Voltage	5.0	V
I _C	Collector Current - Continuous	1.0	А
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES: 1) These ratings are based on a maximum junction temperature of 150 degrees C. 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations. 3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		TN4033A	
P _D	Total Device Dissipation	1.0	W
	Derate above 25°C	8.0	mW/∘C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	50	°C/W

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PNP

PNP General Purpose Amplifie (continue			
unless otherwise noted Test Conditions	Min	Max	Units
- 10 mA L - 0	80		V
	80 80		V V
$_{c} = 10 \text{ mA}, I_{B} = 0$ $_{c} = 10 \mu\text{A}, I_{E} = 0$ $_{E} = 10 \mu\text{A}, I_{C} = 0$,

ON CHARACTERISTICS

OFF CHARACTERISTICS

Symbol

V_{(BR)CEO} V_{(BR)CBO}

V_{(BR)EBO}

 I_{CBO}

I_{EBO}

h _{FE}	DC Current Gain	$I_{C} = 100 \ \mu A, V_{CE} = 5.0 \ V$	75		
		I _C =100mA, V _{CE} =5.0V,T _A = -55°C	40		
		$I_{C} = 100 \text{ mA}, V_{CE} = 5.0 \text{ V}$	100	300	
		$I_{C} = 500 \text{ mA}, V_{CE} = 5.0 \text{ V}$	70		
		$I_{C} = 1.0 \text{ A}, V_{CE} = 5.0 \text{ V}$	25		
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_{\rm C} = 150 \text{ mA}, I_{\rm B} = 15 \text{ mA}$		0.15	V
- ()		$I_{\rm C} = 500 \text{ mA}, I_{\rm B} = 50 \text{ mA}$		0.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	$I_{\rm C} = 150$ mA, $I_{\rm B} = 15$ mA		0.9	V
V _{BE(on)}	Base-Emitter On Voltage	$I_{\rm C}$ = 500 mA, $V_{\rm CE}$ = 0.5 V		1.1	V

TA = 25°C unless otherwise noted

SMALL SIGNAL CHARACTERISTICS

Electrical Characteristics

Parameter

Collector-Emitter Sustaining Voltage*

Collector-Base Breakdown Voltage

Emitter-Base Breakdown Voltage

Collector-Cutoff Current

Emitter-Cutoff Current

C _{obo}	Output Capacitance	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1.0 \text{ MHz}$		20	pF
Cibo	Input Capacitance	$V_{EB} = 0.5 \text{ V}, I_{C} = 0, f = 1.0 \text{ MHz}$		110	pF
h _{fe}	Small-Signal Current Gain	$I_{C} = 50 \text{ mA}, V_{CE} = 10 \text{ V},$ f = 100 MHz	1.0	4.0	

SWITCHING CHARACTERISTICS

ts	Storage Time	$I_{\rm C} = 500 \text{ mA}, I_{\rm B1} = I_{\rm B2} = 50 \text{ mA}$	350	ns
t _{on}	Turn-On Time	I _C = 500 mA, I _{B1} = 50 mA	100	ns
t _f	Fall Time	$I_{C} = 500 \text{ mA}, I_{B1} = I_{B2} = 50 \text{ mA}$	50	ns

*Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 1.0%

NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

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(continued)

PNP General Purpose Amplifier

-10

- 50

- 1

REVERSE BIAS VOLTAGE (V)

Typical Characteristics Typical Pulsed Current Gain Collector-Emitter Saturation V_{CESAT} - COLLE CTOR-EMITTER VOLTAGE (V) 70 70 70 70 70 70 70 70 70 70 70 70 vs Collector Current Voltage vs Collector Current $\beta = 10$ = 5V VCE °C ++++П 125 ||| - 40 °C °Ċ 300 0.3 30 100 100 I c - COLLECTOR CURRENT (mA) 1 3 10 1000 10 1000 I_c-COLLECTOR CURRENT (mA) **Base-Emitter Saturation** Base-Emitter ON Voltage vs Voltage vs Collector Current **Collector Current** V_{CE} = 5V $\beta = 10$ 40 40 c 25 °C 125 °C 125 с 10 50 100 1000 I c - COLLECTOR CURRENT (mA) I c - COLLECTOR CURRENT (mA) **Collector-Cutoff Current Collector-Base and Emitter-Base** vs Ambient Temperature Capacitance vs Reverse Bias Voltage 500 f = 1.0 MHz св= 50 V 200 CAPACITANCE (pF) 100 50 Cibo 20 C opo 10 6 ⊑ −0.1

GAIN 300

10

100

10

1

0.1

25

50

75

T_A - AMBIENT TEMPERATURE (°C)

100

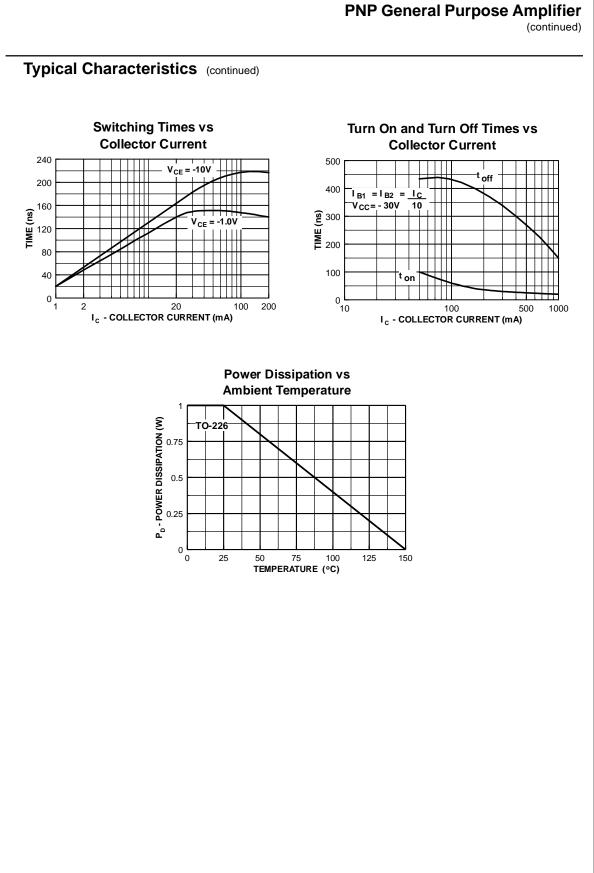
125

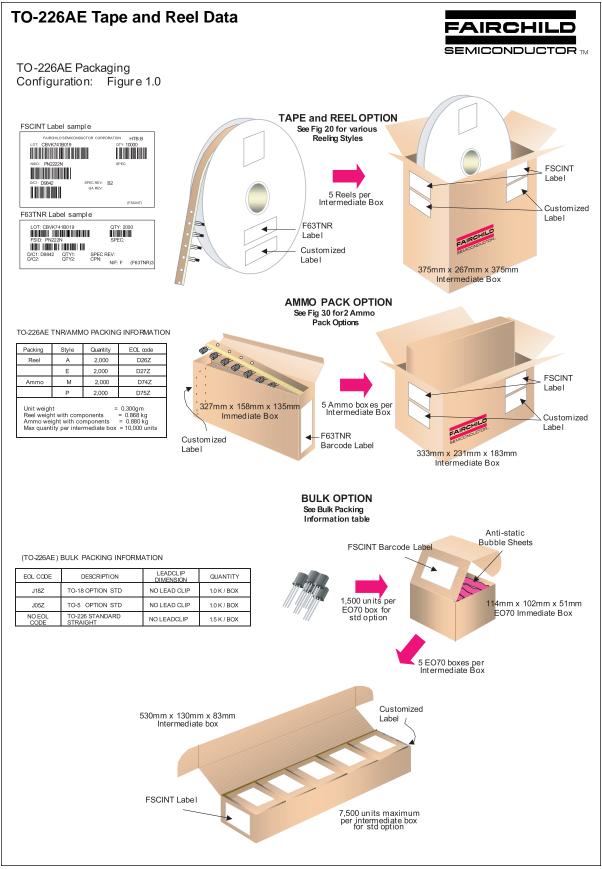
150

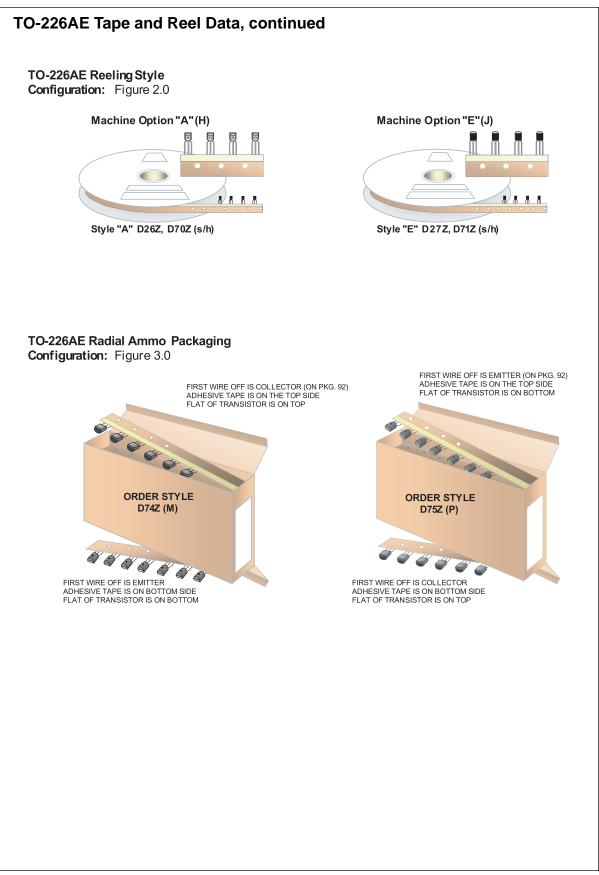
I_{CBO}- COLLEC TOR CURRENT (nA)

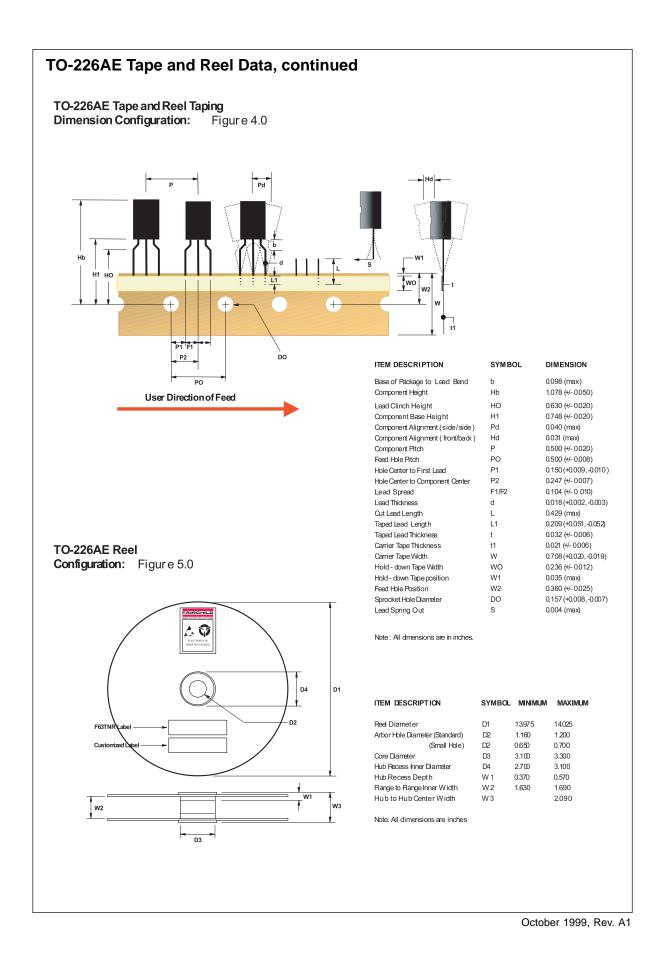
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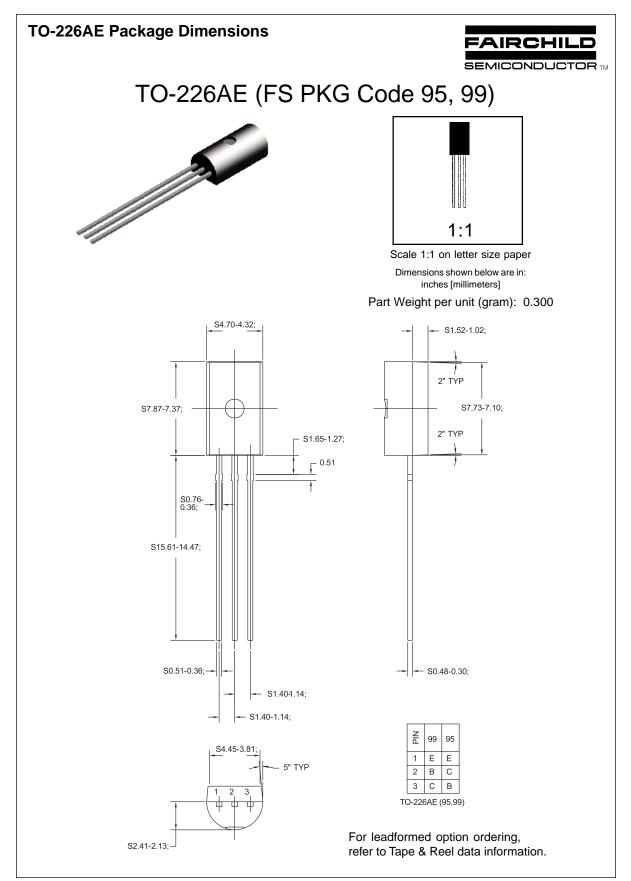
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