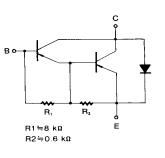


# **PNP Silicon Darlington Transistor**

Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V <sub>CBO</sub>	Collector-Base Voltage	- 100	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	- 100	V	
V <sub>EBO</sub>	Emitter-Base Voltage	- 8	V	
Ι <sub>C</sub>	Collector Current (DC)	- 3	А	
I <sub>CP</sub>	*Collector Current (Pulse)	- 5	Α	
P <sub>C</sub>	Collector Dissipation (Ta=25°C)	1.3	W	
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	15	W	
TJ	Junction Temperature	150	°C	
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C	
* PW≤10ms, Duty Cycle≤50%				



1. Emitter 2.Collector 3.Base

uty Cyc

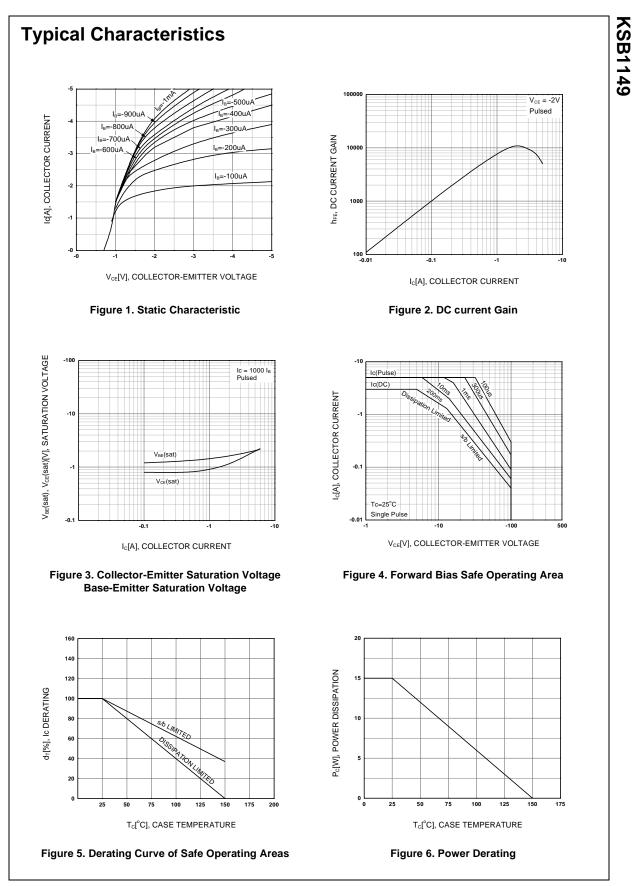
### Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> = - 100V, I <sub>E</sub> = 0			- 10	μA
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$			- 2	mA
h <sub>FE1</sub> h <sub>FE2</sub>	* DC Current Gain	$V_{CE} = -2V, I_{C} = -1.5A$ $V_{CE} = -2V, I_{C} = -3A$	2000 1000		20000	
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	I <sub>C</sub> = - 1.5A, I <sub>B</sub> = - 1.5mA		- 0.9	- 1.2	V
V <sub>BE</sub> (sat)	* Base-Emitter Saturation Voltage	I <sub>C</sub> = - 1.5A, I <sub>B</sub> = - 1.5mA		- 1.5	- 2	V
t <sub>ON</sub>	Turn ON Time	V <sub>CC</sub> = - 40V, I <sub>C</sub> = - 1.5A		0.5		μs
t <sub>STG</sub>	Storage Time	I <sub>B1</sub> = - I <sub>B2</sub> = - 1.5mA		2		μs
t <sub>F</sub>	Fall Time	$R_L = 27\Omega$		1		μs

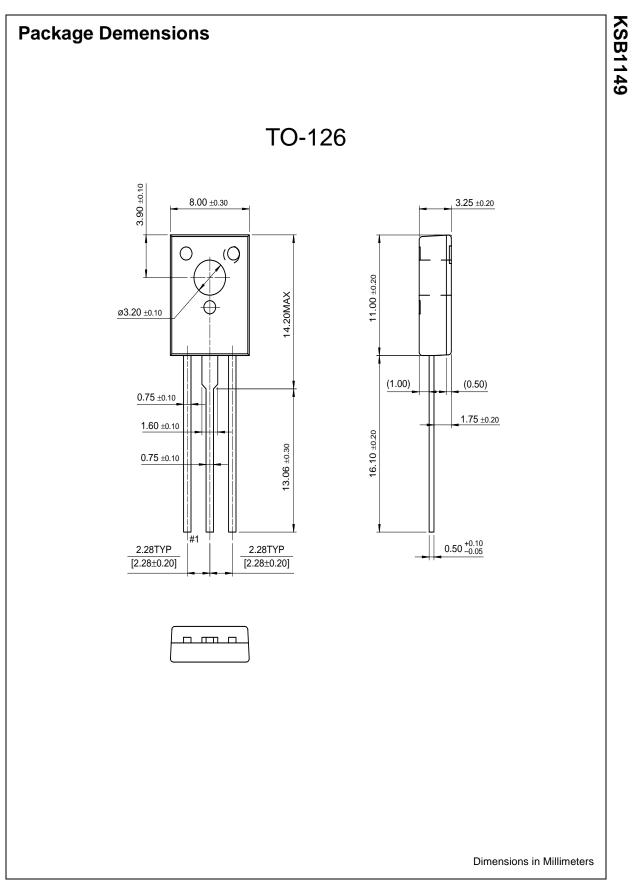
\* Pulse test: PW≤350µs, duty Cycle≤2% Pulsed

## h<sub>FE</sub> Classification

Classification	0	Y	G
h <sub>FE1</sub>	2000 ~ 5000	4000 ~ 12000	6000 ~ 20000



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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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