FAIRCHILD SEMICONDUCTOR® KSA1242 Medium Power Amplifier Camera Flash Applications • hFE = 100~320 (VCE = -2V, IC = -0.5V) • hFE = 70 (Min.) (VCE = -2V, IC = -4A) • Low Saturation Voltage: VCE(sat) = -1V (Max.)

1 I-PAK 1. Base 2. Collector 3. Emitter KSA1242

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings ${\rm T_{C}=25^{\circ}C}$ unless otherwise noted

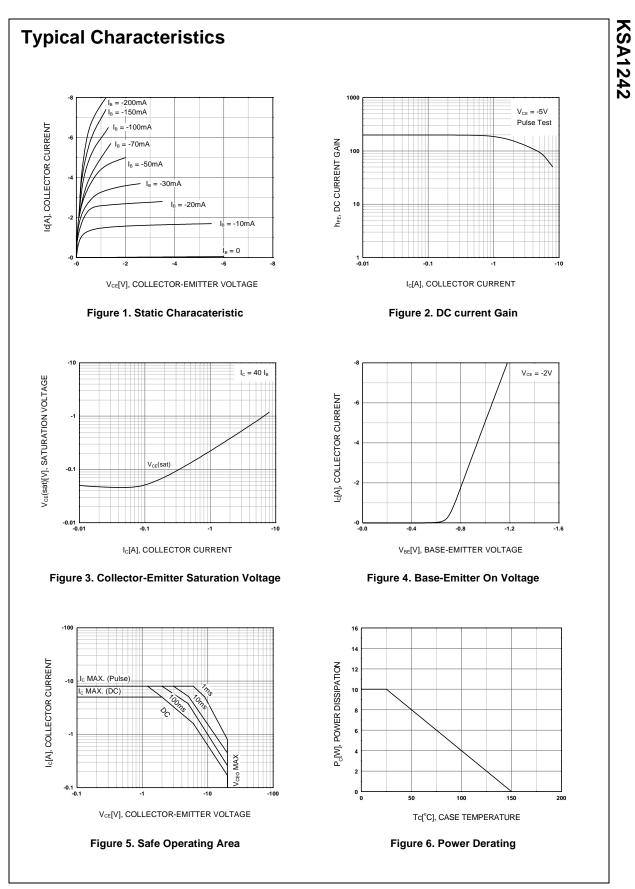
Symbol	Parameter	Ratings	Units
V _{CBO}	Collector-Base Voltage	- 35	V
V _{CEO}	Collector-Emitter Voltage	- 20	V
V _{EBO}	Emitter-Base Voltage	- 8	V
I _C	Collector Current (DC)	- 5	A
I _{CP}	Collector Current (Pulse)	- 8	A
P _C	Collector Dissipation (T _C =25°C)	10	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = - 10mA, I _B = 0	- 20			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_{E} = -1 mA, I_{C} = 0$	- 8			V
I _{CBO}	Collector Cut-off Current	$V_{CB} = -35V, I_E = 0$			- 100	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -8V, I_{C} = 0$			- 100	μΑ
h _{FE1}	DC Current Gain	$V_{CE} = -2V, I_{C} = -0.5A$	100		320	
h _{FE2}		$V_{CE} = -2V, I_{C} = -4A$	70			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = - 4A, I _B = - 0.1A			- 1	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = -2V, I_{C} = -4A$			- 1.5	V
f _T	Current Gain Bandwidth Product	V _{CE} = - 2V, I _C = - 0.5A		180		MHz
C _{ob}	Collector Output Capacitance	V _{CB} = - 10V, f = 1MHz		50		pF

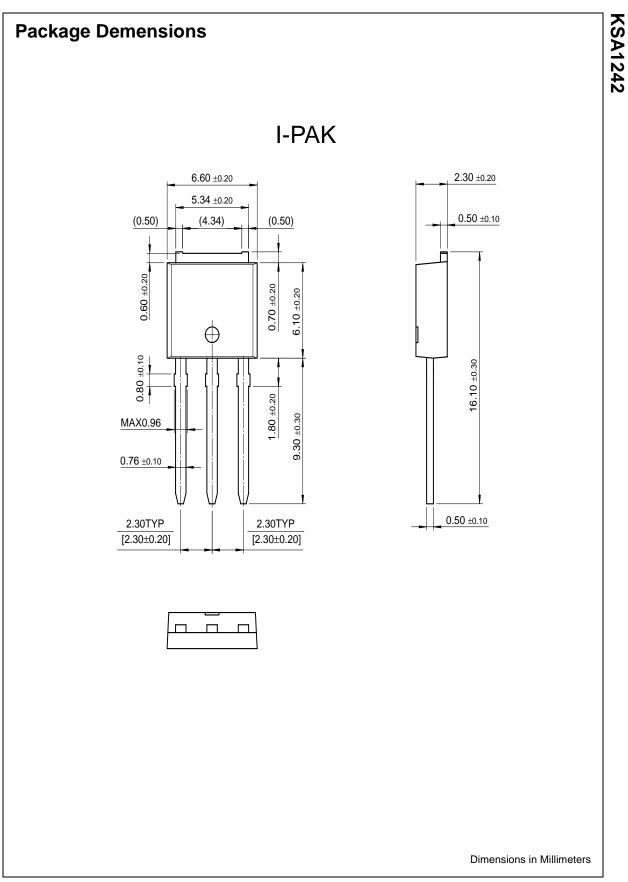
h_{FE} Classification

Classification	0	Y	
h _{FE1}	100 ~ 200	160 ~ 320	



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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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