

July 2003

ISL9R460P2, ISL9R460S2, ISL9R460S3S

4A, 600V Stealth™ Diode

General Description

The ISL9R460P2, ISL9R460S2 and ISL9R460S3S are Stealth™ diodes optimized for low loss performance in high frequency hard switched applications. The Stealth™ family exhibits low reverse recovery current (I_{RRM}) and exceptionally soft recovery under typical operating conditions.

This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low I_{RRM} and short t_a phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the Stealth $^{\text{TM}}$ diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

Formerly developmental type TA49408.

Features

•	Soft Recovery $t_b / t_a > 3$
•	Fast Recovery t_{rr} < 20ns
•	Operating Temperature
•	Reverse Voltage 600V

· Avalanche Energy Rated

Applications

- · Switch Mode Power Supplies
- · Hard Switched PFC Boost Diode
- UPS Free Wheeling Diode
- Motor Drive FWD

N/C

ANODE

- SMPS FWD
- · Snubber Diode

Package JEDEC TO-220AC JEDEC STYLE TO-262 JEDEC TO-263AB K CATHODE (FLANGE) ANODE CATHODE (FLANGE) CATHODE (FLANGE)

Device Maximum Ratings T_C= 25°C unless otherwise noted

CATHODE

(FLANGE)

Symbol	Parameter	Ratings	Units
V_{RRM}	Peak Repetitive Reverse Voltage	600	V
V _{RWM}	Working Peak Reverse Voltage	600	V
V _R	DC Blocking Voltage	600	V
I _{F(AV)}	Average Rectified Forward Current (T _C = 155°C)	4	А
I _{FRM}	Repetitive Peak Surge Current (20kHz Square Wave)	8	Α
I _{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	50	Α
P _D	Power Dissipation	58	W
E _{AVL}	Avalanche Energy (0.5A, 80mH)	10	mJ
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to 175	°C
TL	T _L Maximum Temperature for Soldering		
T_{PKG}	Leads at 0.063in (1.6mm) from Case for 10s	300	°C
	Package Body for 10s, See Techbrief TB334	260	°C

CAUTION: Stresses above those listed in "Device Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Package Marking and Ordering Information

Device Marking	Device	Package	Tape Width	Quantity
R460P2	ISL9R460P2	TO-220AC	N/A	50
R460S2	ISL9R460S2	TO-262	N/A	50
R460S3S	ISL9R460S3S	TO-263AB	N/A	50
R460S3S	ISL9R460S3ST	TO-263AB	24mm	800

Symbol	Parameter	Test Conditions		Min	Тур	Max	Units
Off State	Characteristics						
I _R	Instantaneous Reverse Current	V _R = 600V	T _C = 25°C	-	-	100	μΑ
			T _C = 125°C	-	-	1.0	mA
On State	Characteristics						
V_{F}	Instantaneous Forward Voltage	I _F = 4A	T _C = 25°C	-	2.0	2.4	V
			$T_{\rm C} = 125^{\circ}{\rm C}$	-	1.6	2.0	V
СЈ	'	$V_R = 10V, I_F = 0$					
						•	
Switchin	ng Characteristics						
Switchin	g Characteristics Reverse Recovery Time	I _F = 1A, d _{IF} /dt =	100A/μs, V _R = 30V	-	17	20	ns
	-		100A/μs, $V_R = 30V$ 100A/μs, $V_R = 30V$	-	17 19	20 22	ns ns
	-						
t _{rr}	Reverse Recovery Time	$I_F = 4A, d_{IF}/dt =$ $I_F = 4A, d_{IF}/dt = 4A, d_{IF}/dt = 200A/\mu s$	$100A/\mu s$, $V_R = 30V$	-	19	22	ns
t _{rr}	Reverse Recovery Time Reverse Recovery Time	$I_F = 4A, d_{IF}/dt =$ $I_F = 4A,$	$100A/\mu s$, $V_R = 30V$	-	19 17	22	ns ns
t _{rr}	Reverse Recovery Time Reverse Recovery Time Maximum Reverse Recovery Current	$\begin{split} I_F &= 4A, \ d_{IF}/dt = \\ I_F &= 4A, \\ d_{IF}/dt &= 200A/\mu s \\ V_R &= 390V, T_C = \\ I_F &= 4A, \end{split}$	100A/μs, V _R = 30V s, = 25°C	-	19 17 2.6	22	ns ns A
t _{rr} t _{rr} I _{RRM} Q _{RR}	Reverse Recovery Time Reverse Recovery Time Maximum Reverse Recovery Current Reverse Recovery Charge	$I_{F} = 4A, d_{IF}/dt = I_{F} = 4A,$ $d_{IF}/dt = 200A/\mu s$ $V_{R} = 390V, T_{C} = I_{F} = 4A,$ $d_{IF}/dt = 200A/\mu s$	100A/μs, V _R = 30V s, = 25°C	- - -	19 17 2.6 22	22 - - -	ns ns A nC
t _{rr} t _{rr} I _{RRM} Q _{RR} t _{rr}	Reverse Recovery Time Reverse Recovery Time Maximum Reverse Recovery Current Reverse Recovery Charge Reverse Recovery Time	$\begin{split} I_F &= 4A, \ d_{IF}/dt = \\ I_F &= 4A, \\ d_{IF}/dt &= 200A/\mu s \\ V_R &= 390V, T_C = \\ I_F &= 4A, \end{split}$	100A/μs, V _R = 30V s, = 25°C		19 17 2.6 22 77	22	ns ns A nC

Thermal Characteristics

S

 Q_{RR}

dl_M/dt

Reverse Recovery Time

Reverse Recovery Charge

Maximum di/dt during tb

Maximum Reverse Recovery Current

Softness Factor (t_b/t_a)

$R_{\theta JC}$	Thermal Resistance Junction to Case		-	-	2.6	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-220	-	-	62	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-262	-	-	62	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-263			62	°C/W

 $I_F = 4A$,

 $V_R = 390V,$ $T_C = 125^{\circ}C$

 $d_{IF}/dt = 400A/\mu s$,

54

3.5

4.3

110

500

ns

Α

nC

A/µs

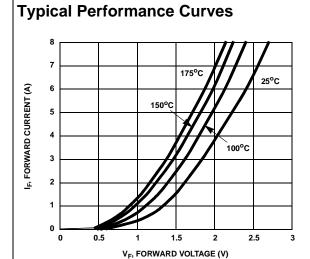
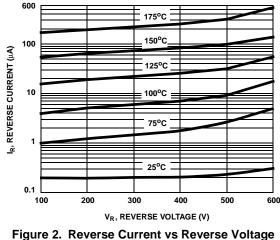


Figure 1. Forward Current vs Forward Voltage



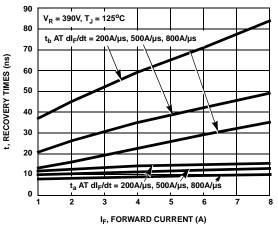


Figure 3. t_a and t_b Curves vs Forward Current

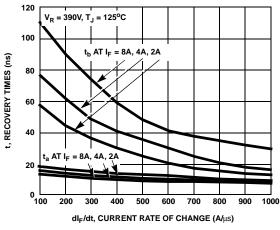


Figure 4. t_a and t_b Curves vs dl_F/dt

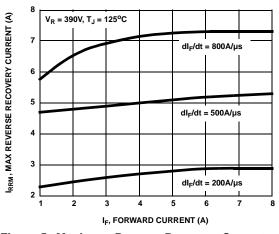


Figure 5. Maximum Reverse Recovery Current vs **Forward Current**

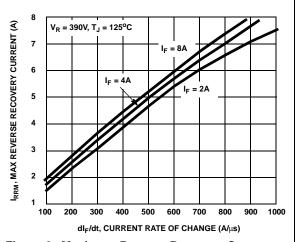
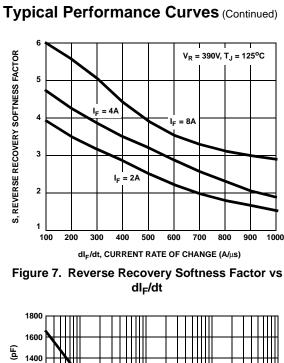


Figure 6. Maximum Reverse Recovery Current vs dl_F/dt



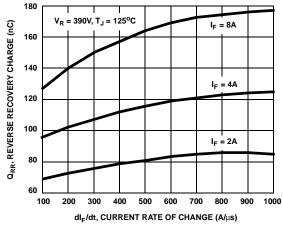
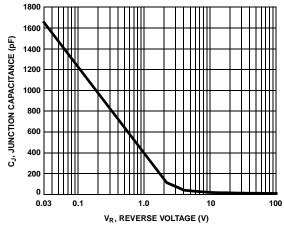


Figure 8. Reverse Recovery Charge vs dl_F/dt



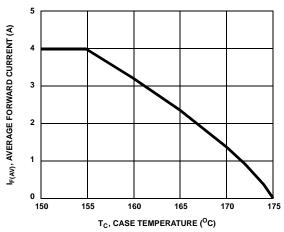


Figure 9. Junction Capacitance vs Reverse Voltage

Figure 10. DC Current Derating Curve

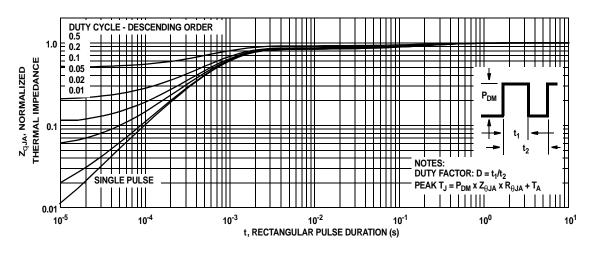
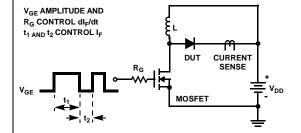


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit and Waveforms



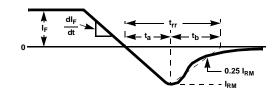


Figure 12. It_{rr} Test Circuit

Figure 13. t_{rr} Waveforms and Definitions

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I = 0.5A
L = 80mH
R < 0.1\Omega
V_{DD} = 200V
E_{AVL} = 1/2LI^2 \left[V_{R(AVL)}/(V_{R(AVL)} - V_{DD})\right]
Q_1 = IGBT \left(BV_{CES} > DUT \ V_{R(AVL)}\right)
CURRENT + 0
SENSE V_{DD}
V_{DD}
DUT - 0
```

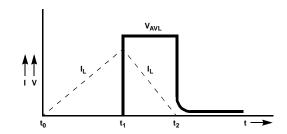


Figure 14. Avalanche Energy Test Circuit

Figure 15. Avalanche Current and Voltage Waveforms

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Across the board	. Around the world.™	OCXPro™	RapidConnect™	UltraFET [®]
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