

PNP Low Saturation Transistor

These devices are designed for high current gain and low saturation voltage with collector currents up to 3.0 A continuous. Sourced from Process PC.

Absolute Maximum Ratings* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CEO}	Collector-Emitter Voltage	30	V
V _{CBO}	Collector-Base Voltage	35	V
V _{EBO}	Emitter-Base Voltage	5.0	V
I _C	Collector Current - Continuous	3.0	А
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:
1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units	
		FPN630 / FPN630A	-	
PD	Total Device Dissipation	1.0	W	
R _{θJC}	Thermal Resistance, Junction to Case	50	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	125	°C/W	

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PNP Low Saturation Transistor

(continued)

Symbol	Parameter	Test Conditions	Min	Мах	Units
OFF CHAI	RACTERISTICS				
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0$	30		V
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{\rm C} = 100 \ \mu {\rm A}, \ I_{\rm E} = 0$	35		V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 100 \ \mu A, I_C = 0$	5.0		V
I _{CBO}	Collector Cutoff Current	$V_{CB} = 30 \text{ V}, \text{ I}_{E} = 0$		100	nA
		V _{CB} = 30 V, I _E = 0, T _A = 100°C		10	μΑ
I _{EBO}	Emitter Cutoff Current	$V_{EB} = 4.0 \text{ V}, I_{C} = 0$		100	nA

h _{FE}	DC Current Gain	$I_{C} = 100 \text{ mA}, V_{CE} = 2.0 \text{ V}$	630	100		
			630A	250		
		$I_{\rm C} = 1.0 \text{ A}, V_{\rm CE} = 2.0 \text{ V}$		60		
		$I_{\rm C} = 2.0 \text{ A}, V_{\rm CE} = 2.0 \text{ V}$		40		
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_{\rm C} = 1.0 \text{ A}, I_{\rm B} = 100 \text{ mA}$	630		300	mV
- (,			630A		250	mV
		$I_{\rm C} = 2.0 \text{ A}, I_{\rm B} = 200 \text{ mA}$			500	mV
V _{BE(sat)}	Base-Emitter Saturation Voltage	$I_{C} = 1.0 \text{ A}, I_{B} = 100 \text{ mA}$			1.25	V
V _{BE(on)}	Base-Emitter Saturation Voltage	$I_{C} = 1.0 \text{ A}, V_{CE} = 2.0 \text{ V}$			1.0	V

SMALL SIGNAL CHARACTERISTICS

Cobo	Output Capacitance	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1.0 \text{ MHz}$		100	pF
F⊤	Transition Frequency	I_{C} = 100 mA, V_{CE} = 5.0 V, f = 100 MHz	100		MHz

*Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

PNP Low Saturation Transistor (continued) **Typical Characteristics Base-Emitter Saturation Base-Emitter On Voltage vs** Voltage vs Collector Current **Collector Current BASE-EMITTER ON VOLTAGE (V)** 1.6 V_{ce} = 2.0V β=10 1.4 1.2 1 40 °C - 40 °(0.8 ------ТШ 0.6 125 °C 25 5°C 0.4 125 °C ≥ 0.2 0.0001 0.01 0.1 10 0.001 0.01 0.1 10 Ic- COLLECTOR CURRENT (A) I c - COLLECTOR CURRENT (A) **Collector-Emitter Saturation** Input/Output Capacitance vs V_{CESAT}- COLLECTOR-EMITTER VOLTAGE (V) 00 7:0 9:0 8:0 1 1 Voltage vs Collector Current **Reverse Bias Voltage** 500 $\beta = 10$ 450 400 350 200 250 200 150 100 400 Cibo Ш 125°C Ш Cobo Л 40°C 100 П 50 0 L 0 0.01 0.5 10 20 50 100 1 0.1 10 V_{CE}- COLLECTOR VOLTAGE (V) Ic- COLLECTOR CURRENT (A) **Current Gain vs Collector Current** Power Dissipation vs 600 Ambient Temperature Vce 1111 1 500 POWER DIS SIPATION (W) 0.50 - 0.25 - 0.25 TO-226 H^H- CURRENT GAIN 25°C 0.5 25°C 40°C ΠIII 100 0 L 0.0001 0.001 0.01 0.1 10 25 50 75 100 125 150 I_c - COLLECTOR CURRENT (A) TEMPERATURE (°C)

FPN630 / FPN630A

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