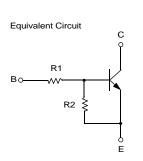


1.Emitter 2. Collector 3. Base

NPN Epitaxial Silicon Transistor

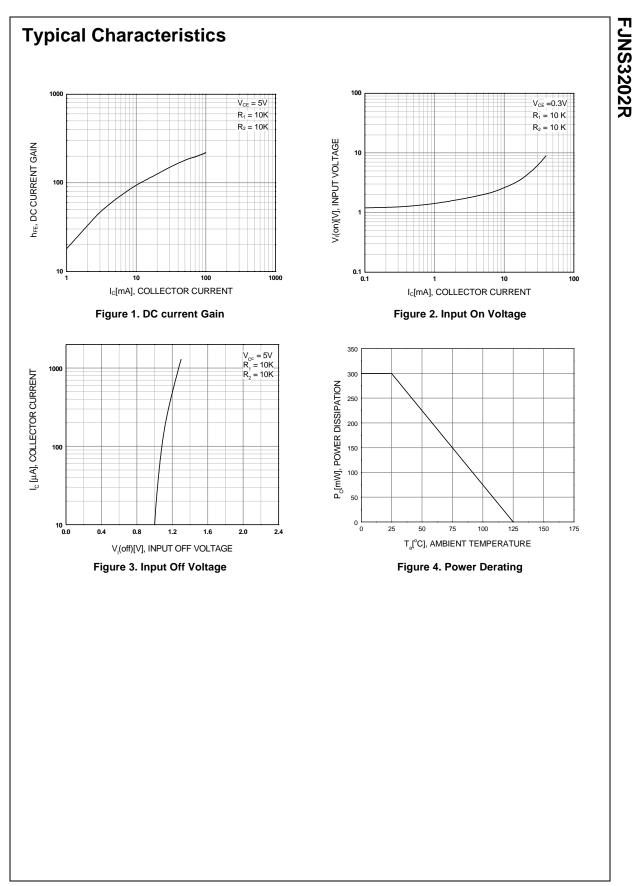
Absolut	e Maximum	Ratings	T _a =2	5°C unless otherwise noted

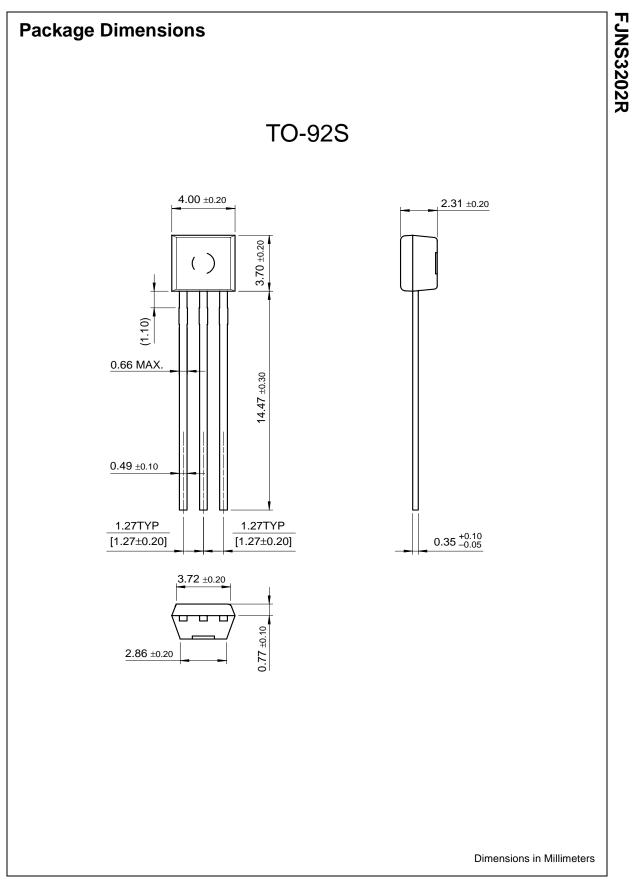
Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	50	V	
V _{CEO}	Collector-Emitter Voltage	50	V	
V _{EBO}	Emitter-Base Voltage	10	V	
Ι _C	Collector Current	100	mA	
P _C	Collector Power Dissipation	300	mW	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	-55 ~ 150	°C	



Electrical Characteristics $T_a=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	I _C =10μΑ, I _E =0	50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C =100μA, I _B =0	50			V
I _{CBO}	Collector Cut-off Current	V _{CB} =40V, I _E =0			0.1	μΑ
h _{FE}	DC Current Gain	V _{CE} =5V, I _C =5mA	30			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =10mA, I _B =0.5mA			0.3	V
f _T	Current Gain Bandwidth Product	V _{CE} =10V, I _C =5mA		250		MHz
C _{ob}	Output Capacitance	V _{CB} =10V, I _E =0 f=1.0MHz		3.7		pF
V _I (off)	Input Off Voltage	V _{CE} =5V, I _C =100μA	0.5			V
V _I (on)	Input On Voltage	V _{CE} =0.3V, I _C =10mA			3	V
R ₁	Input Resistor		7	10	13	KΩ
R_1/R_2	Resistor Ratio		0.9	1	1.1	





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