

October 2006

FDFC2P100 Integrated P-Channel PowerTrench[®] MOSFET and Schottky Diode

-20V, -3A, 150mΩ

Features

- Max $r_{DS(on)}$ = 150m Ω at V_{GS} = -4.5V, I_D = -3.0A
- Max $r_{DS(on)}$ = 200m Ω at V_{GS} = -2.5V, I_D = -2.2A
- Low Gate Charge (3.4nC typ)
- Compact industry standard SuperSOTTM-6 package

Schottky:

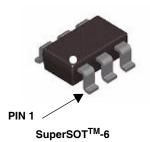
- V_F < 0.45 V at I_F = 1A
- RoHS Compliant

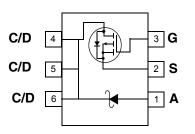


General Description

The FDFC2P100 combine the exceptional performance of Fairchild's PowerTrench MOSFET technology with a very low forward voltage drop Schottky barrier rectifier in an SSOT-6 package.

This device is designed specifically as a single package solution for DC to DC converters. It features a fast switching, low gate charge MOSFET with very low on-state resistance. Significant improvement of Thermal Characteristics and Power Dissipation via replacement of independently connected Schottky with internal connection of Schottky Diode Cathode pn to P-Channel PowerTrench MosFET Drain pin.





MOSFET Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units		
V _{DS}	Drain to Source Voltage		-20	V	
V _{GS}	Gate to Source Voltage		±12	V	
1	Drain Current -Continuous	(Note 1a)	-3		
D	-Pulsed		-6	— A	
D	Power Dissipation (Note 1a)	(Note 1a)	1.5	W	
PD		(Note 1b)	0.8	vv	
V _{RRM}	Schotty Repetitive Peak Reverse Voltage		20	V	
I _O	Schotty Average Forward Current	(Note 1a)	1	Α	
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +150	°C	

Thermal Characteristics

$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	87	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient	(Note 1b)	166	C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
.100	FDFC2P100	SSOT-6	7"	8mm	3000units

Symbol	Parameter	Test C	onditions	Min	Тур	Max	Units
Off Chara	acteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	I _D = -250μA, V _{GS} = 0V		-20			V
ΔBV _{DSS} ΔT ₋₁	Breakdown Voltage Temperature Coefficient	$I_D = -250 \mu A$, referenced to 25°C			-12		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS}$	= -16V			-1	μA
I _{GSS}	Gate to Source Leakage Current	V _{GS} = ±12V, V _E	_{DS} = 0V			±100	μA
On Chara	acteristics						
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D =$	-250uA	-0.6	-0.9	-1.5	V
$\Delta V_{GS(th)}$	Gate to Source Threshold Voltage					-	2400
ΔT_J	Temperature Coefficient	I _D = -250μA, refe			3		mV/°C
		V_{GS} = -4.5V, I_D			95	150	_
r _{DS(on)}	Drain to Source On-Resistance	$V_{GS} = -2.5V, I_D = -2.2A$			150	200	mΩ
		V_{GS} = -4.5V, I_{D} = -3.0A, T_{J} = 125°C			130	252	
9 _{FS}	Forward Transconductance	$V_{DS} = -5V, I_D = -5V$	-3.0A		5.4		S
Dynamic	Characteristics						
C _{iss}	Input Capacitance				335	445	pF
C _{oss}	Output Capacitance	$V_{\rm DS} = -10V, V_{\rm GS} = 0V,$			80	105	pF
C _{rss}	Reverse Transfer Capacitance	f = 1MHz f = 1MHz			40	60	pF
R _g	Gate Resistance				6		Ω
•	a Characteriation						
	g Characteristics				9	10	
t _{d(on)}	Turn-On Delay Time Rise Time	$-V_{DD} = -10V, I_D = -3.0A$ $-V_{GS} = -4.5V, R_{GEN} = 6\Omega$			9 11	16 20	ns
t _r	Turn-Off Delay Time				11	20	ns
t _{d(off)}	Fall Time		_		4	8	ns
t _f		$\gamma = 0/4$	V		-	4.7	ns
Q _{g(TOT)}	Total Gate Charge at -10V	$v_{GS} = 0 v_{10} - 10$	V V _{DD} = -4.5V I _D = -3.0A		3.4	4./	nC
Q _{gs}	Gate to Source Gate Charge	_	$I_{\rm D} = -3.0{\rm A}$		0.9		nC
Q _{gd}	Gate to Drain "Miller" Charge				1.0		nC
Drain-So	urce Diode Characteristics						
I _S	Maximum Continuous Drain tio Source Dic	1				-1.2	Α
V _{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0V, I_{S} = -$	1.2A (Note 2)		-0.8	-1.2	V
t _{rr}	Reverse Recovery Time	I _F = -3.0A, di/dt	= 100A/us		17		ns
Q _{rr}	Reverse Recovery Charge	·F ••••• • •••			5		nC
Schottky	Diode Characteristics						
		V _R = 20V	T _J = 25°C		26	400	μA
L	Reverse Leakage	*R 200	T _J = 100C		2.7	20	mA
I _R	Neverse Leakage	V _R = 10V	T _J = 25°C		23	200	μA
		*K 'OV	T _J = 100°C		2.5	10	mA
		I _F = 500mA	T _J = 25°C		0.31	0.4	
	Forward Voltage	1 _F - 50011A	T _J = 100°C		0.24	0.35	v
V_		I_ = 1A	T _J = 25°C		0.37	0.45	v
V _F	Torward voltage	I _F = 1A	$T_{\rm J} = 100^{\circ}{\rm C}$				

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Notes:

1: $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.

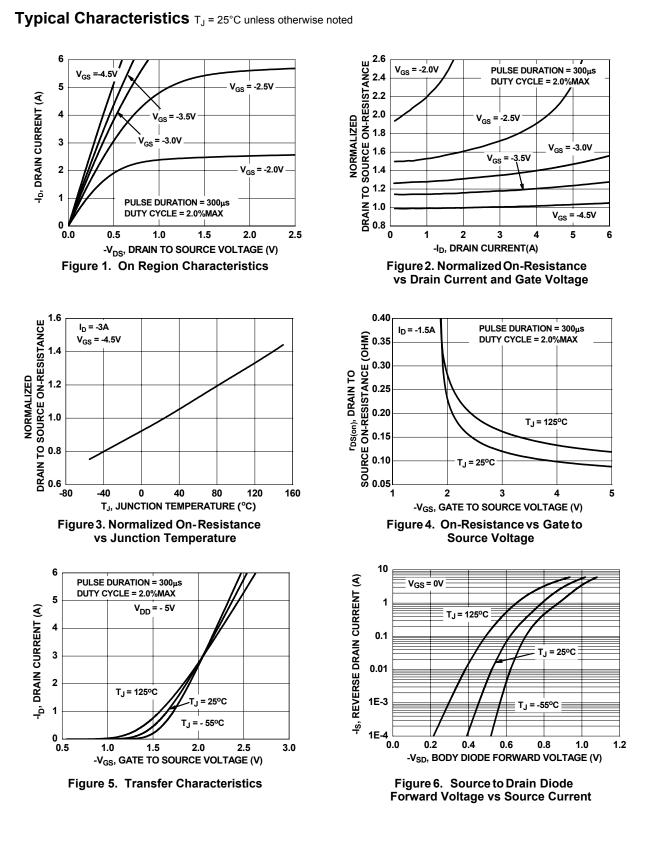


a) 87°C/W when mounted on a 1in² pad of 2 oz copper

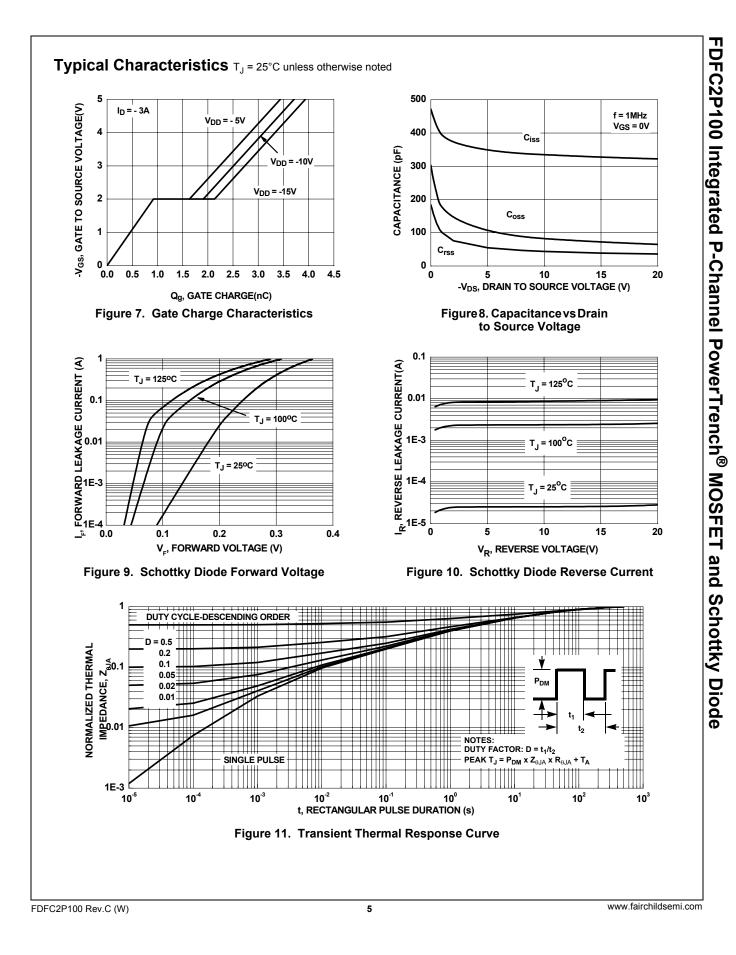
mounted on a minimun pad

b) 166°C/W when

2: Pulse Test: Pulse Width <300 ms, Duty Cycle < 2.0%



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