

October 1986 Revised February 2000

DM74AS1034A Hex Non-Inverting Driver

General Description

These devices contain six independent drivers, each of which performs the logic Y=A function. The DM74AS1034A is a driver version of the DM74AS34. Each driver has increased output drive capability to allow the driving of high capacitive loads.

Features

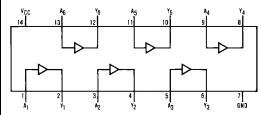
- Switching specifications at 50 pF
- \blacksquare Switching specifications guaranteed over full temperature and V_{CC} range
- Advanced oxide-isolated, ion-implanted Schottky TTL process

Ordering Code:

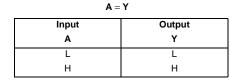
Order Number	Package Number	Package Description
DM74AS1034AM	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74AS1034AN	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table



L = LOW Logic Level H = HIGH Logic Level

Absolute Maximum Ratings(Note 1)

Supply Voltage 7V
Input Voltage 7V

Operating Free Air Temperature Range 0°C to +70°C

Storage Temperature Range -65°C to +150°C

Typical θ_{JA}

N Package 76.0°C/W M Package 106.0°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
Гон	HIGH Level Output Current			-48	mA
OL	LOW Level Output Current			48	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

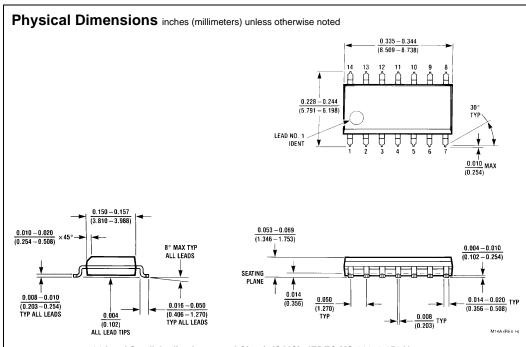
over recommended operating free air temperature range. All typical values are measured at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Symbol	Parameter	Cond	itions	Min	Тур	Max	Units
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5V, I_{I} = -18 \text{ mA}$				-1.2	V
V _{OH}	HIGH Level	$I_{OH} = -2 \text{ mA}, V_{CC} = 4.5 \text{V}$	to 5.5V	V _{CC} - 2			V
	Output Voltage	$I_{OH} = -3 \text{ mA}, V_{CC} = 4.5 \text{V}$		2.4	3.2		V
		$I_{OH} = Max, V_{CC} = 4.5V$		2			
V _{OL}	LOW Level	V _{CC} = 4.5V			0.35	0.5	V
	Output Voltage	I _{OL} = Max			0.35	0.5	V
I	Input Current @	\/ - F F\/ \/ - 7\/	V _{CC} = 5.5V, V _{IH} = 7V			0.1	mA
Max Inp	Max Input Voltage	v _{CC} = 5.5 v, v _{IH} = 7 v					
I _{IH}	HIGH Level Input Current	$V_{CC} = 5.5V, V_{IH} = 2.7V$				20	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = 5.5V, V_{IL} = 0.4V$				-0.5	mA
I _O	Output Drive Current	$V_{CC} = 5.5V, V_{O} = 2.25V$		-50	-135	-200	mA
Icc	Supply Current	V _{CC} = 5.5V	Outputs HIGH		9	15	mA
			Outputs LOW		21	35	mA

Switching Characteristics

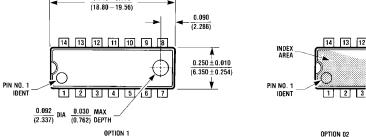
over recommended operating free air temperature range

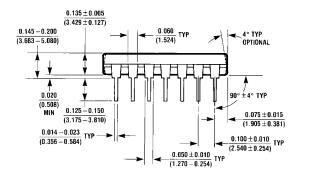
Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time	V _{CC} = 4.5V to 5.5V	1	6	ns
	LOW-to-HIGH Level Output	$R_L = 500\Omega$			
t _{PHL}	Propagation Delay Time	$C_L = 50 \text{ pF}$	1	6	ns
	HIGH-to-LOW Level Output				

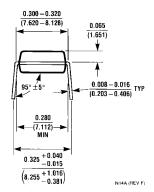


14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow Package Number M14A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued) 0.740 - 0.770 (18.80 - 19.56)







14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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