

September 1986 Revised February 2000

# **DM74ALS1032A Quadruple 2-Input OR Buffer**

# **General Description**

These devices contain four independent buffers, each of which performs the logic OR function. The DM74ALS1032A is a buffer version of the DM74ALS32.

#### **Features**

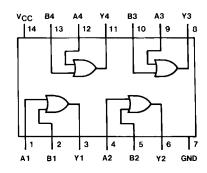
- Switching specifications at 50 pF
- Switching specifications guaranteed over full temperature and  $V_{\mbox{\footnotesize CC}}$  range
- Advanced oxide-isolated, ion-implanted Schottky TTL
- Improved line receiving characteristics

# **Ordering Code:**

Order Number	Package Number	Package Description
DM74ALS1032AM	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74ALS1032AN	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

# **Connection Diagram**



# **Function Table**

Y =	: A	+	В
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Inp	Output	
Α	В	Y
L	L	L
Н	X	Н
X	Н	Н

L = LOW Logic Level

H = HIGH Logic Level X = Either LOW or HIGH Logic Level

# Absolute Maximum Ratings(Note 1)

Supply Voltage 7V
Input Voltage 7V

Operating Free Air Temperature Range  $0^{\circ}\text{C to } +70^{\circ}\text{C}$ Storage Temperature Range  $-65^{\circ}\text{C to } +150^{\circ}\text{C}$ 

Tunical O

Typical  $\theta_{JA}$ 

 N Package
 83.0°C/W

 M Package
 114.0°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions

for actual device operation.

# **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	V
I <sub>OH</sub>	HIGH Level Output Current			-2.6	mA
I <sub>OL</sub>	LOW Level Output Current			24	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

# **Electrical Characteristics**

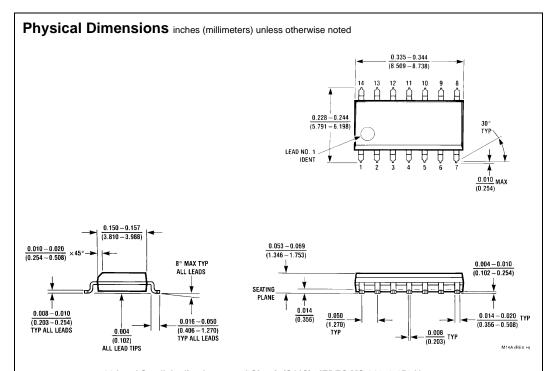
over recommended operating free air temperature range. All typical values are measured at  $V_{CC} = 5V$ ,  $T_A = 25^{\circ}C$ .

Symbol	Parameter	$\label{eq:Conditions} Conditions$ $V_{CC} = 4.5 \text{V, I}_{I} = -18 \text{ mA}$		Min	Тур	Max	Units
V <sub>IK</sub>	Input Clamp Voltage					-1.5	V
V <sub>OH</sub>	HIGH Level Output Voltage	$V_{CC} = 4.5V$ $V_{IH} = 2V$	I <sub>OH</sub> = Max	2.4	3.2		V
		V <sub>CC</sub> = 4.5V to 5.5V	$I_{OH} = -400 \mu A$	V <sub>CC</sub> - 2			V
V <sub>OL</sub>	LOW Level	V <sub>CC</sub> = 4.5V	I <sub>OL</sub> = 12 mA		0.25	0.4	V
	Output Voltage	$V_{IH} = 0.8V$	$I_{OL} = 24 \text{ mA}$		0.35	0.5	V
I <sub>I</sub>	Input Current at Maximum Input Voltage	V <sub>CC</sub> = 5.5V, V <sub>IH</sub> = 7V	V <sub>CC</sub> = 5.5V, V <sub>IH</sub> = 7V			0.1	mA
I <sub>IH</sub>	HIGH Level Input Current	$V_{CC} = 5.5V, V_{IH} = 2.7V$	$V_{CC} = 5.5V, V_{IH} = 2.7V$			20	μА
I <sub>IL</sub>	LOW Level Input Current	$V_{CC} = 5.5V, V_{IL} = 0.4V$	$V_{CC} = 5.5V, V_{IL} = 0.4V$			-0.1	mA
Io	Output Drive Current	$V_{CC} = 5.5V, V_{O} = 2.25V$		-30		-112	mA
I <sub>CCH</sub>	Supply Current with Outputs HIGH	V <sub>CC</sub> = 5.5V, V <sub>I</sub> = 4.5V			2.5	5	mA
I <sub>CCL</sub>	Supply Current with Outputs LOW	$V_{CC} = 5.5V, V_{I} = 0V$			6.6	10.6	mA

# **Switching Characteristics**

over recommended operating free air temperature range

Symbol	Parameter	Conditions	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time	V <sub>CC</sub> = 4.5V to 5.5V	2	9	ns
	LOW-to-HIGH Level Output	$R_L = 500\Omega$	2	9	115
t <sub>PHL</sub>	Propagation Delay Time	$C_L = 50 \text{ pF}$	3	12	ns
	HIGH-to-LOW Level Output		3	12	115



14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow Package Number M14A

#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued) 0.740 - 0.770 (18.80 - 19.56)0.090 (2.286) 14 13 12 11 10 9 8 14 13 12 INDEX AREA 0.250 ± 0.010 (6.350 ± 0.254) PIN NO. 1 PIN NO. 1 IDENT 1 2 3 4 5 6 7 1 2 3 $\frac{0.092}{(2.337)}$ DIA 0.030 MAX (0.762) DEPTH OPTION 1 OPTION 02 $\frac{0.135 \pm 0.005}{(3.429 \pm 0.127)}$ 0.300 - 0.320 $\frac{0.620 - 8.128}{(7.620 - 8.128)}$ 0.060 0.145 - 0.2004° TYP Optional (1.651) (3.683 - 5.080) $\frac{0.008 - 0.016}{(0.203 - 0.406)}$ TYP 0.020 (0.508)0.125 - 0.150 $0.075 \pm 0.015$ $\overline{(3.175 - 3.810)}$ $(1.905 \pm 0.381)$ (7.112) MIN 0.014 - 0.0230.100 ± 0.010 (2.540 ± 0.254) (0.356 - 0.584) $\frac{0.050 \pm 0.010}{(1.270 - 0.254)}$ TYP 0.325 <sup>+0.040</sup> -0.015

14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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- 2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

8.255 + 1.016

N144 (REV.F)

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