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DM74145 BCD-to-Decimal Decoders/Drivers

General Description

These BCD-to-decimal decoders/drivers consist of eight inverters and ten, four-input NAND gates. The inverters are connected in pairs to make BCD input data available for decoding by the NAND gates. Full decoding of BCD input logic ensures that all outputs remain OFF for all invalid (10–15) binary input conditions. These decoders feature high-performance, NPN output transistors designed for use as indicator/relay drivers, or as open-collector logic-circuit drivers. The high-breakdown output transistors are compatible for interfacing with most MOS integrated circuits.

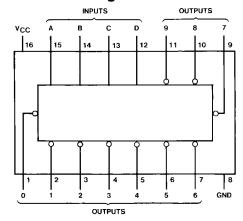
Features

- Full decoding of input logic
- 80 mA sink-current capability
- All outputs are OFF for invalid BCD input conditions

Ordering Code:

Order Number	Package Number	Package Description
DM74145N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide

Connection Diagram

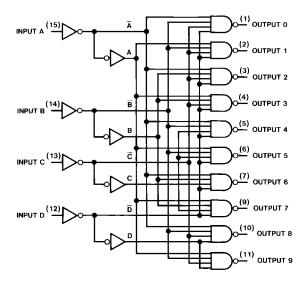


Function Table

	No.	Inputs			Outputs										
		D	С	В	Α	0	1	2	3	4	5	6	7	8	9
	0	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
	1	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н
	2	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н
	3	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н
	4	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Н	Н
	5	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
	6	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н
	7	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н
	8	Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	Н
	9	Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L
	I	Н	L	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
	Ν	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
	V	Н	Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
	Α	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
	L	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
	-1	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
	D														

H = HIGH Level (OFF) L = LOW Level (ON)

Logic Diagram



Absolute Maximum Ratings(Note 1)

Supply Voltage 7V Input Voltage 5.5V Operating Free Air Temperature Range 0° C to $+70^{\circ}$ C Storage Temperature Range -65° C to $+150^{\circ}$ C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings.

-65°C to +150°C

The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
V _{OH}	HIGH Level Output Voltage			15	V
l _{OL}	LOW Level Output Current			20	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -12 \text{ mA}$			-1.5	V
I _{CEX}	HIGH Level Output Current	$V_{CC} = Min, V_{OH} = Max$ $V_{II} = Max, V_{IH} = Min$			250	μА
V _{OL}	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min, V_{IL} = Max$			0.4	V
		$I_{OL} = 80 \text{ mA}$ $V_{CC} = \text{Min}$		0.5	0.9	v
II	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$			1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.4V$			40	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-1.6	mA
Icc	Supply Current	V _{CC} = Max (Note 3)		43	70	mA

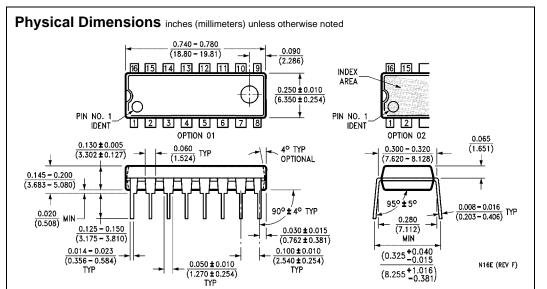
Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Note 3: I_{CC} is measured with all outputs open and all inputs grounded.

Switching Characteristics

at $V_{CC} = 5V$ and $T_{\Delta} = 25^{\circ}C$

Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time	C _L = 15 pF		30	20
	LOW-to-HIGH Level Output	$R_L = 100\Omega$		30	ns
t _{PHL}	Propagation Delay Time			30	ne
	HIGH-to-LOW Level Output			30	ns



16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide Package Number N16E

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