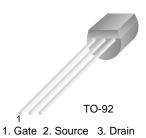
September 2007



# 2N5951 N-Channel RF Amplifier

• This device is designed primarily for electronic switching applications such as low on resistance analog switching.

• Sourced from process 50.



### Absolute Maximum Ratings\* Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>DG</sub>	Drain-Gate Voltage	30	V
V <sub>GS</sub>	Gate-Source Voltage	-30	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 ~ 150	°C

\* This ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES:

1) These rating are based on a maximum junction temperature of 150 degrees C.

2) These are steady limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

## Thermal Characteristics $T_a=25$ °C unless otherwise noted

Symbol	Parameter	Max.	Units
PD	Total Device Dissipation	350	mW
	Derate above 25°C	2.8	mW/°C
R <sub>0JC</sub>	Thermal Resistance, Junction to Case	125	°C/W
R <sub>0JA</sub>	Thermal Resistance, Junction to Ambient	357	°C/W

Symbol	Parameter	Test Condition	Min.	Max.	Units
Off Chara	cteristics				
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_{G} = 1.0 \mu A, V_{DS} = 0$	-30		V
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = 15V, V_{DS} = 0, T = 25^{\circ}C$ T = 100°C		-1.0 -200	nA
V <sub>GS(off)</sub>	Gate-Source Cut-off Voltage	V <sub>DS</sub> = 15V, I <sub>D</sub> = 100nA	-2	-5	V
V <sub>GS</sub>	Gate-Source Forward Voltage	V <sub>DS</sub> = 15V, I <sub>D</sub> = 700μA	-1.3	-4.5	V
Cmall Clar					
•	nal Characteristics Common- Source Output Conductance	$V_{DS} = 15V$ , $V_{CS} = 0V$ , $f = 1.0$ kHz		75	μ/Ω
Small Sigi goss gos	Common- Source Output Conductance           Output Conductance	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0kHz V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 100MHz		75 100	μ/Ω μ/Ω
goss gos	Common- Source Output Conductance			-	•
goss	Common- Source Output Conductance Output Conductance	$V_{DS} = 15V, V_{GS} = 0V, f = 100MHz$		100	μ/Ω
goss gos gis Ciss	Common- Source Output Conductance Output Conductance Input Conductance	$V_{DS} = 15V, V_{GS} = 0V, f = 100MHz$ $V_{DS} = 15V, V_{GS} = 0V, f = 100MHz$		100 250	μ/Ω μ/Ω
goss gos gis	Common- Source Output Conductance Output Conductance Input Conductance Input Capacitance	$V_{DS} = 15V, V_{GS} = 0V, f = 100MHz$ $V_{DS} = 15V, V_{GS} = 0V, f = 100MHz$ $V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz$		100 250 6	μ/Ω μ/Ω pF

\* Pulse Test: Pulse Width  $\leq 300 \mu s, \, Duty \, Cycle = 2\%$ 



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