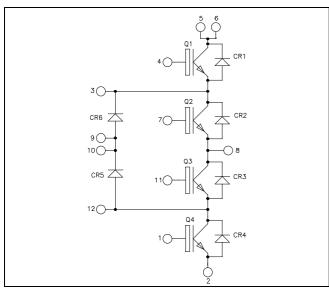
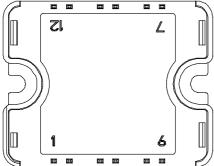


Three level inverter NPT IGBT Power Module







All multiple inputs and outputs must be shorted together 5/6; 9/10

Application

- Solar converter
- Uninterruptible Power Supplies

Features

- Non Punch Through (NPT) Fast IGBT
 - Low voltage drop
 - Low tail current
 - Switching frequency up to 100 kHz
 - Soft recovery parallel diodes
 - Low diode VF
 - Low leakage current
 - RBSOA and SCSOA rated
- Kelvin emitter for easy drive
- Very low stray inductance
- High level of integration

Benefits

- Stable temperature behavior
- Very rugged
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Easy paralleling due to positive TC of VCEsat
- Low profile
- RoHS Compliant

O1 to O4 Absolute maximum ratings

QI to Q	- 11050lute maximum lutings			
Symbol	Parameter		Max ratings	Unit
V_{CES}	Collector - Emitter Breakdown Voltage		600	V
т	Continuous Collector Current	$T_C = 25^{\circ}C$	42	
I_{C}	Continuous Conector Current T _C	$T_C = 80^{\circ}C$	30	A
I_{CM}	Pulsed Collector Current	$T_C = 25^{\circ}C$	100	
V_{GE}	Gate – Emitter Voltage		±20	V
P_{D}	Maximum Power Dissipation	$T_C = 25^{\circ}C$	140	W
RBSOA	Reverse Bias Safe Operating Area	$T_{j} = 125^{\circ}C$	60A@500V	

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



All ratings @ $T_j = 25^{\circ}C$ unless otherwise specified

Q1 to Q4 Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
I_{CES}	Zero Gate Voltage Collector Current	$V_{GE} = 0V$	$T_j = 25$ °C			250	μA
1CES	Zero Gate Voltage Concetor Current	$V_{CE} = 600V$ $T_j = 125^{\circ}$	$T_j = 125$ °C			500	μΑ
V	Collector Emitter on Voltage	$V_{GE} = 15V$	$T_j = 25^{\circ}C$	1.7	2.0	2.45	V
V _{CE(on)}	Collector Emitter on Voltage	$I_C = 30A$	$T_j = 125$ °C		2.2		·
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}$, $I_C = 1 \text{mA}$		4		6	V
I_{GES}	Gate – Emitter Leakage Current	$V_{GE} = 20V, V_{CE} = 0V$				400	nA

Q1 to Q4 Dynamic Characteristics

_	Characteristic	Test Conditions	1	Min	Тур	Max	Unit
Cies	Input Capacitance	$V_{GE} = 0V$			1350		
C_{oes}	Output Capacitance	$V_{CE} = 25V$			193		pF
C_{res}	Reverse Transfer Capacitance	f = 1MHz		120			
Q_{g}	Total gate Charge	$V_{GE} = 15V$			99		
Q_{ge}	Gate – Emitter Charge	$V_{Bus} = 300V$			10		nC
Q_{gc}	Gate – Collector Charge	$I_C=30A$	$I_C = 30A$				
$T_{d(on)}$	Turn-on Delay Time	Inductive Switch	hing (25°C)		30		ns
T_{r}	Rise Time	$V_{GE} = 15V$			12		
$T_{d(off)}$	Turn-off Delay Time	$V_{\text{Bus}} = 400\text{V}$ $I_{\text{C}} = 30\text{A}$			80		
T_{f}	Fall Time	$R_G = 6.8\Omega$		15			
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching (125°C) $V_{GE} = 15V$ $V_{Bus} = 400V$ $I_{C} = 30A$			32		ns
T_{r}	Rise Time				12		
$T_{d(off)}$	Turn-off Delay Time				90		
$T_{\rm f}$	Fall Time	$R_G = 6.8\Omega$			21		
Eon	Turn-on Switching Energy	$V_{GE} = 15V$ $V_{Bus} = 400V$	$T_j = 125$ °C		0.3		I
E_{off}	Turn-off Switching Energy	$I_C = 30A$ $R_G = 6.8\Omega$	$T_j = 125$ °C		0.8		mJ
I_{sc}	Short Circuit data	$V_{GE} \le 15V$; $V_{Bus} = 360V$ $t_p \le 10 \mu s$; $T_i = 125 ^{\circ}C$			135		A
R_{thJC}	Junction to Case Thermal Resistance					0.9	°C/W



CR1 to CR4 diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V
I_{RM}	Maximum Reverse Leakage Current	V _R =600V	$T_i = 25^{\circ}C$ $T_i = 150^{\circ}C$			25 500	μА
I_{F}	DC Forward Current		$Tc = 80^{\circ}C$		15		A
		$I_F = 15A$			2	2.4	
V	Diada Farward Valtaga	$I_F = 30A$			2.5		V
V_{F}	Diode Forward Voltage	$I_F = 15A$	$T_{i} = 125^{\circ}C$		1.6		V
ŧ	Payarsa Pagayary Tima		$T_j = 25$ °C		20		200
t_{rr}	$I_{\rm F} = 13$ A	$T_j = 125$ °C		105		ns	
0	Payarsa Pagayary Charga	$V_{R} = 400V$ $di/dt = 200A/\mu s$	$T_j = 25$ °C		21		nC
Q_{rr}	Reverse Recovery Charge		$T_i = 125^{\circ}C$		250		пС
E _{rr}	Reverse Recovery Energy	$\begin{split} I_F &= 15A \\ V_R &= 400V \\ di/dt &= 1000A/\mu s \end{split}$	$T_j = 125$ °C		0.24		mJ
R_{thJC}	Junction to Case Thermal Resistance					2	°C/W

CR5 & CR6 diode ratings and characteristics

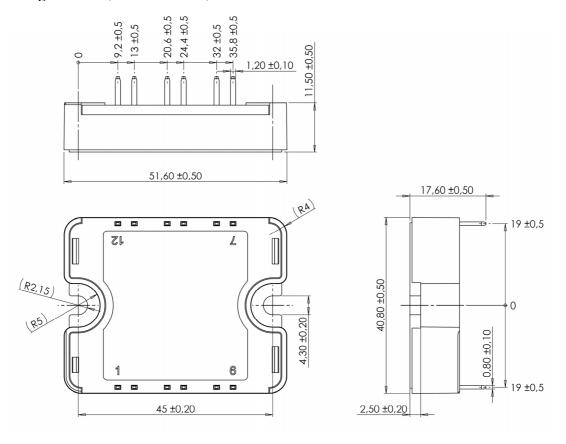
Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit	
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V	
I_{RM}	Maximum Reverse Leakage Current	$V_R=600V$	$T_i = 25^{\circ}C$ $T_i = 150^{\circ}C$			25 500	μA	
I_{F}	DC Forward Current		$Tc = 80^{\circ}C$		30		A	
		$I_{\rm F} = 30A$			1.8	2.2		
**	Di-d-Ed V-lt	$I_F = 60A$			2.2		7.7	
V_{F}	Diode Forward Voltage $I_F = 30A$ $T_i = 12$	$I_F = 30A$	$I_F = 30A$	$T_{i} = 125^{\circ}C$		1.5		V
t _{rr}	Reverse Recovery Time		$T_j = 25$ °C		25		ns	
ι _{rr}	Reverse Recovery Time	$I_F = 30A$	$ T_F - 30A T_C = 125^{\circ}C$	$T_{j} = 125^{\circ}C$		160		115
0	Reverse Recovery Charge	$V_{R} = 400V$ $di/dt = 200A/\mu s$	$T_j = 25$ °C		35		пC	
Q_{rr}	Reverse Recovery Charge		verse recovery charge	SISE INCUSVEIN CHAISE	$T_j = 125$ °C		480	
E _{rr}	Reverse Recovery Energy	$I_F = 30A$ $V_R = 400V$ $di/dt = 1000A/\mu s$	$T_j = 125$ °C		0.6		mJ	
R_{thJC}	Junction to Case Thermal Resistance					1.2	°C/W	

Thermal and package characteristics

Symbol	Characteristic			Min	Тур	Max	Unit
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T_{J}	Operating junction temperature range		-40		150		
T_{STG}	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight					80	g



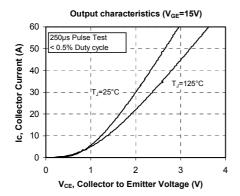
SP1 Package outline (dimensions in mm)

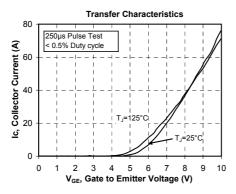


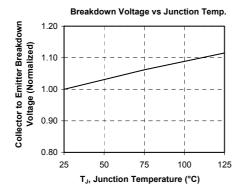
See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com

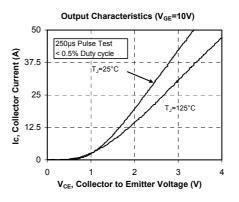


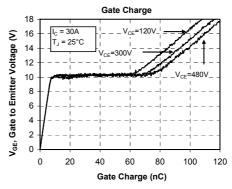
Q1 to Q4 Typical performance curve

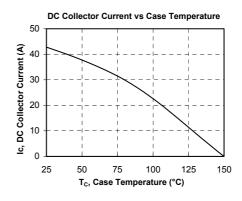




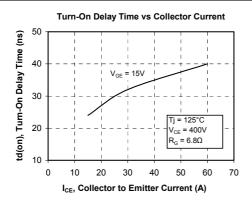


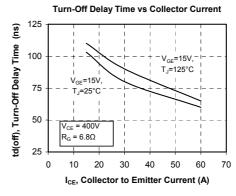


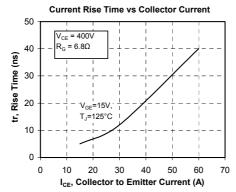


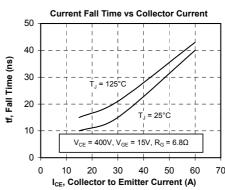


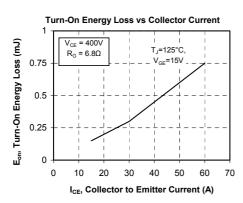


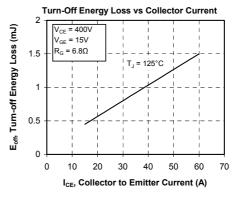


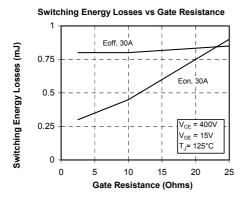


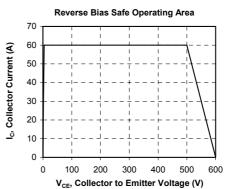




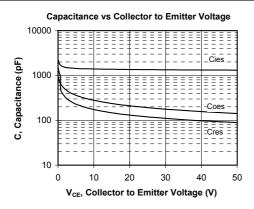


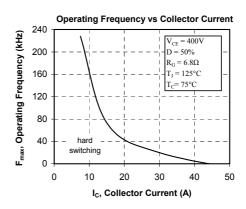


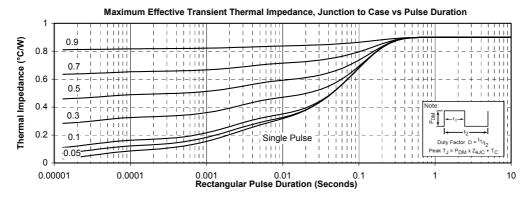






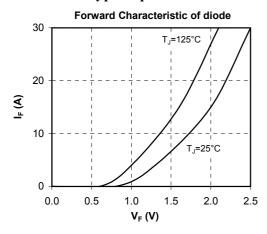


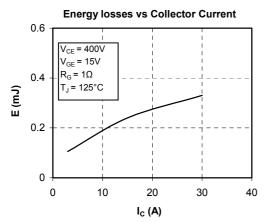


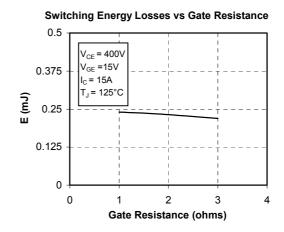


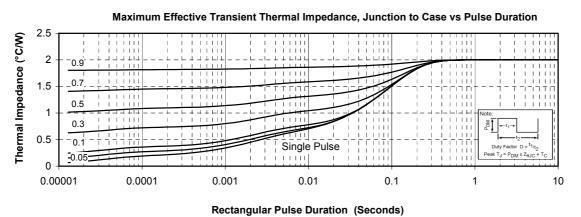


CR1 to CR4 Typical performance curve



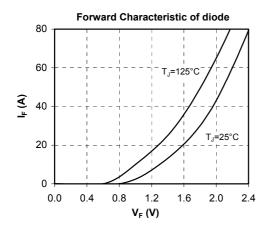


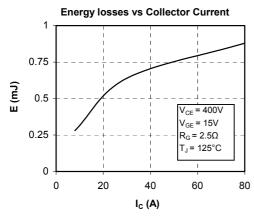


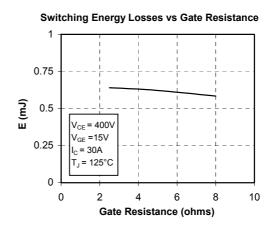


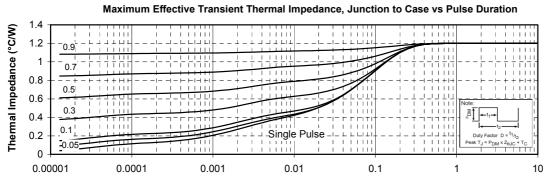


CR5 & CR6 Typical performance curve











DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with life-support or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at http://www.microsemi.com/legal/tnc.asp

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.