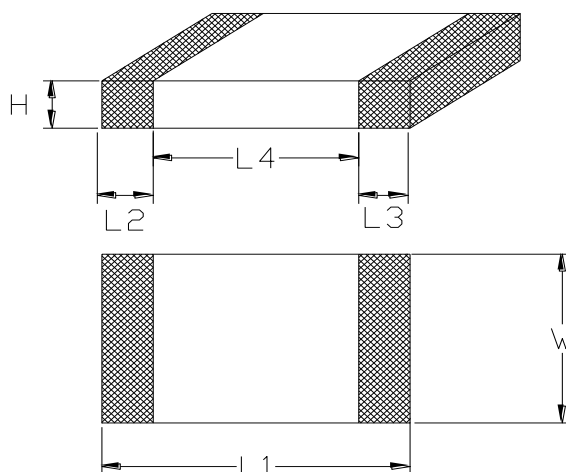


# Multilayer Ceramic Chip Capacitor

**1812J6300223JGT**

Knowles offers Multilayer Ceramic Chip Capacitors of the highest quality and reliability for a range of demanding applications. Manufactured using our "Wet Process", the range encompasses 10V to 12kV capability, with a variety of termination options including FlexiCap™, the worlds first commercially available flexible termination.



## Mechanical Specification

Size Code	1812
Length (L1) mm	4.5 ± 0.3
Width (W) mm	3.2 ± 0.2
Thickness (H) mm	3.2 Max.
Termination Bands (L2,L3) mm	0.25 - 1.143
Minimum Band Gap (L4) mm	2.15
Termination Material	100% Matte Sn over Ni
Solderability	IEC 60068-2-58
RoHS Compliant	2011/65/EU as amended by 2015/863/EU
Packaging	Taped and Reeled, 7 inch Reel

## General Electrical Specification

Rated Voltage	630V
Nominal Capacitance Value	22nF
Capacitance Tolerance	± 5%
Tangent of Loss Angle (Tan δ)	≤0.0015
Capacitance and Tan δ Test Conditions	1Vrms @ 1kHz
Voltage Proof	1.5 x Rated Voltage
(Voltage applied for 5 secs max. @ 50mA max. charge current)	
Min Insulation Resistance (IR)	45.45GΩ
Dielectric Classification	C0G (NP0)
Rated Temperature Range	-55°C to +125°C
Maximum Capacitance Change over Temperature Range	No DC Voltage 0±30ppm/°C
Climatic Category (IEC)	Rated DC Voltage -
Ageing Characteristic	55/125/56
	Zero

This datasheet is for a standard item and is confirmed valid on the date generated, the latest published data for this part may differ and is available at [www.knowlesc capacitors.com/syfer](http://www.knowlesc capacitors.com/syfer) or by contacting us at [syfersales@knowles.com](mailto:syfersales@knowles.com)

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Description:-

**1812 630V 22nF ± 5% C0G (NP0)**

The information contained on this drawing is confidential and may not be copied in whole or part in any form or disclosed to a third party without the consent of Knowles. and any customer mentioned within this specification.

Name: J So

Date: 26 July 2017

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