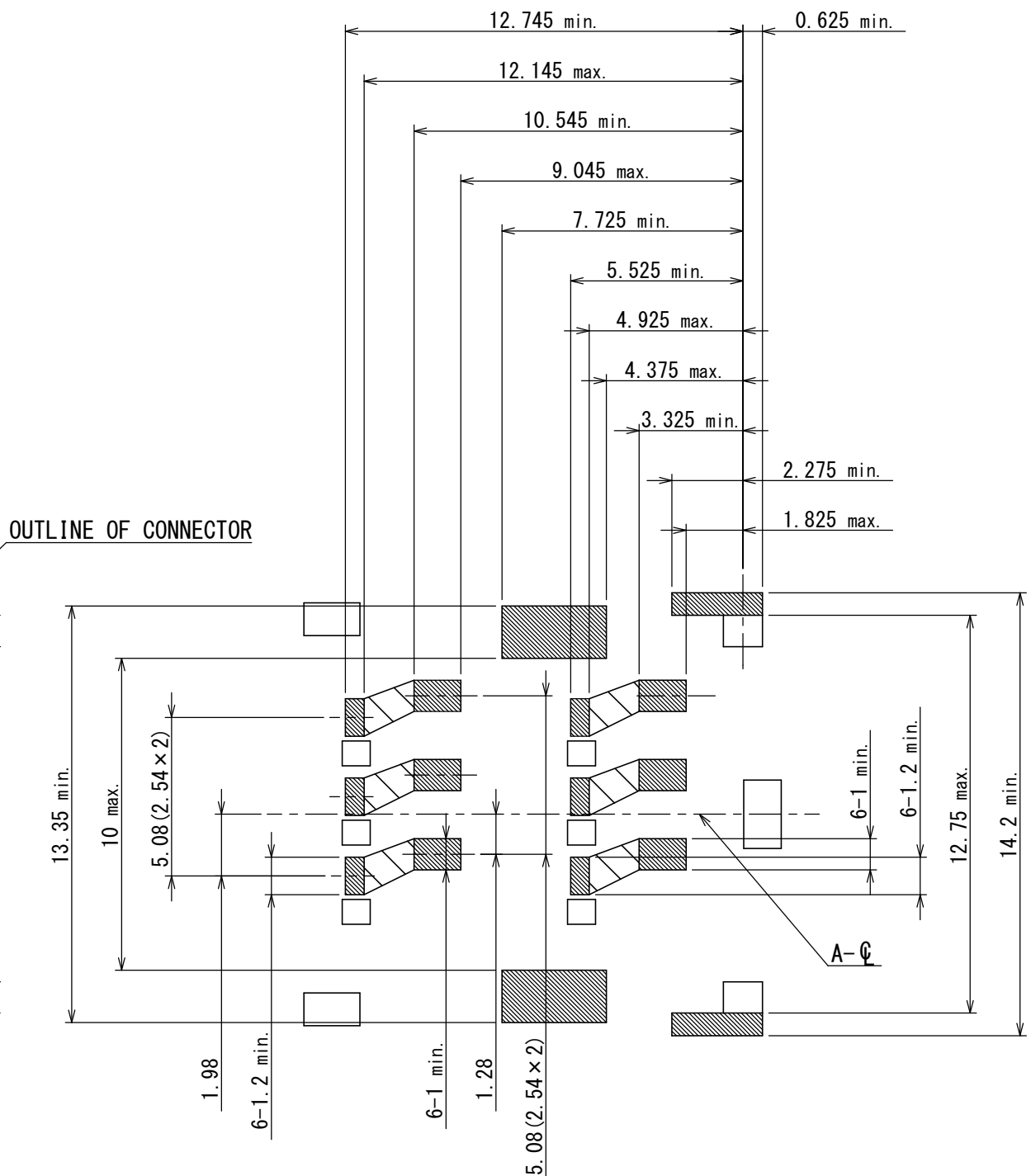
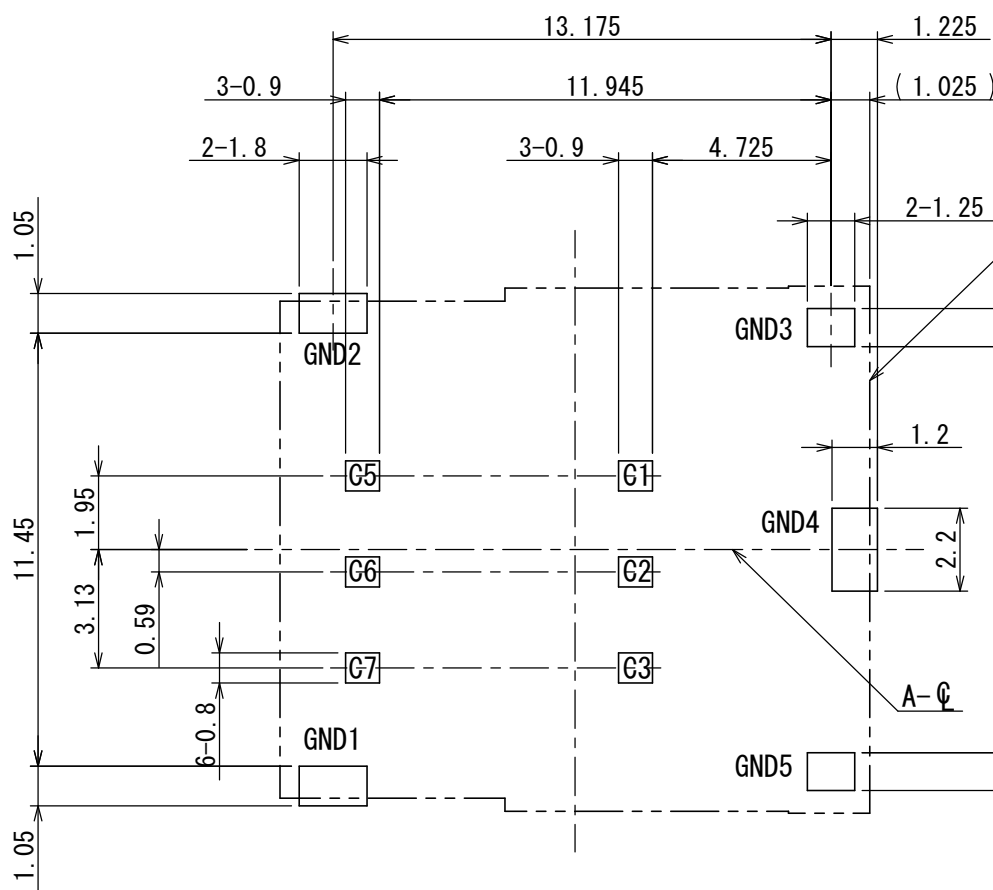
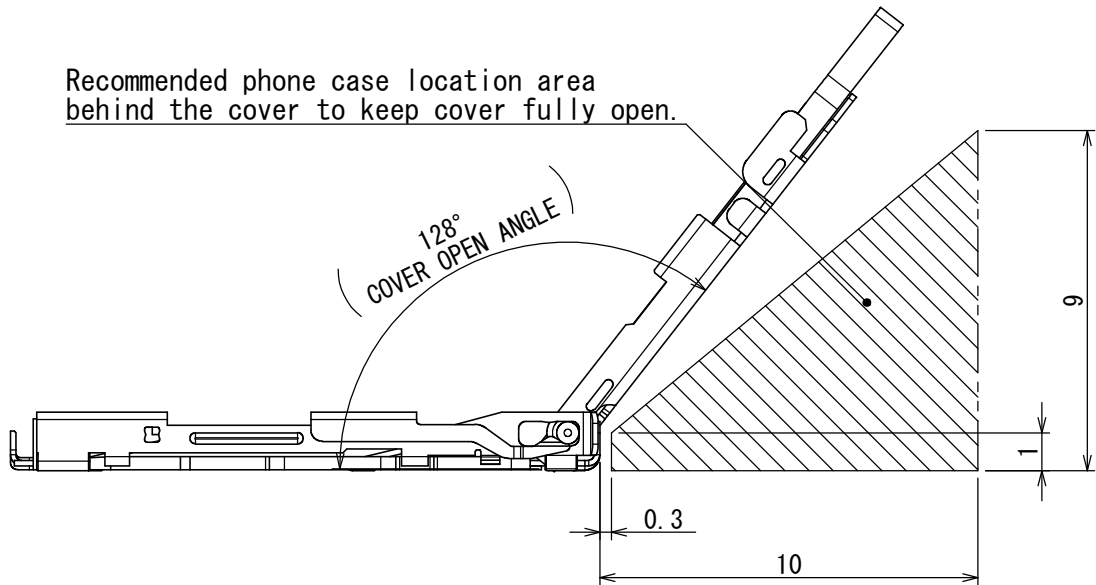
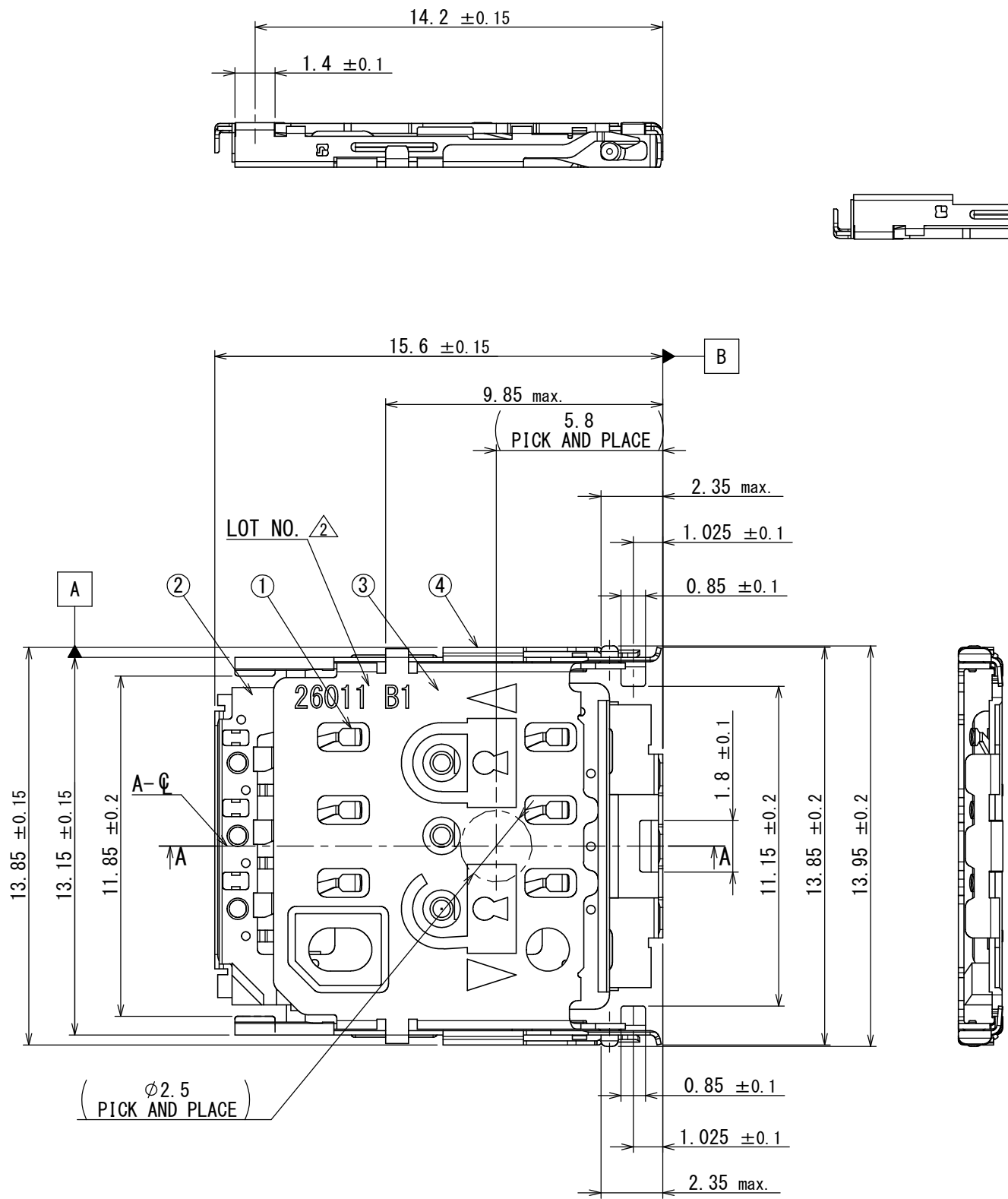


版数 VER.	年月日 DATE	図 DR.	変更内容 DESCRIPTION	製 DR.	担 CHK.	査 APPD.	承認 APPD.
2	15/APR/2014	004608	ADDED DIMENSION		M. SHIMADA	—	A. NATORI



RESTRICTED AREA (No pattern and Via hole)

All the pattern and Via holes under the connector should be coated by resist.

APPLICABLE PWB. DIMENSION FOR RESTRICTED AND INSULATION AREA (REF.)

NOTE1. COPLANARITY 0.08mm max.
NOTE2. LOT INFORMATION IS SHOWN AS BELOW.
e.g. 2 6 0 1 1

PRODUCTION LINE NO.

PRODUCTION DATE (YMDD)

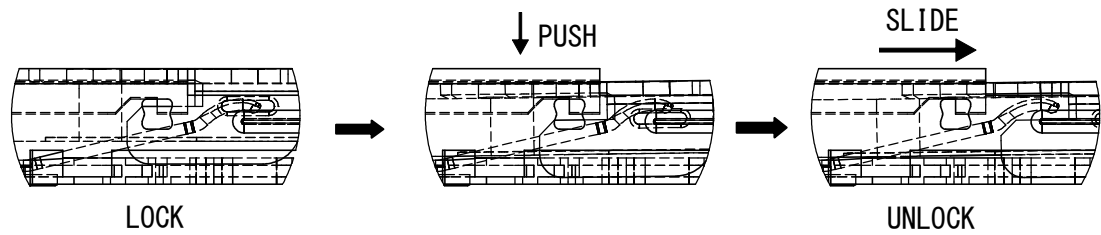


TABLE 1. FINISH			
PART No.	PART NAME	TERMINAL AREA / LOCK HINGE AREA	CONTACT AREA
1	CONTACT	Au FLASH OVER Ni (1 μm MIN.)	Au (0.5 μm MIN.) OVER Ni (2~6 μm)
4	GROUND-FRAME		—

4	GROUND-FRAME	1	COPPER ALLOY	TABLE 1	
3	COVER	1	STAINLESS	—	
2	HOUSING	1	GLASS FILLED LCP	—	
1	CONTACT	6	COPPER ALLOY	TABLE 1	

仕様書 (SPECIFICATION)		第1版 (ORIGINAL DATE)		尺度 (SCALE)		シリーズ (SERIES)		日本航空電子工業株式会社		図面番号 (DRAWING NO.)	版数 (VER.)
JACS-10936		01/JUN/2012		5:1		SF15		 JAPAN AVIATION ELECTRONICS INDUSTRY, LTD.			
一般公差 (GENERAL TOLERANCE)		製図 DR.		名称 (TITLE)							
寸法 (DIMENSION)		担当 CHK.		SF15W006S4B							
±0.8		角度 (ANGLES)		査閲 APPD.							
× ±0.4		×° ±		承認 APPD.		A. NATORI					
×× ±0.1											
××× ±		単位 (UNIT) : mm				質量 (MASS)				SJ113074	