

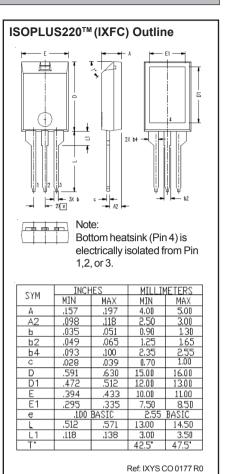
Powe ISOP (Electrica N-Chann	HV [™] HiPerFET IXF er MOSFET LUS220 [™] ally Isolated Back Surface) el Enhancement Mode nsic Diode ne Rated	EC .	12N8	0P		$\begin{array}{llllllllllllllllllllllllllllllllllll$			
Symbol	Test Conditions	Maximum Ratings				ISOPLUS220™ (IXFC) E153432			
V _{dss} V _{dgr}	$T_J = 25^{\circ} C$ to $150^{\circ} C$ $T_J = 25^{\circ} C$ to $150^{\circ} C$; $R_{GS} = 1 M\Omega$		80 80		V V				
V _{GS} V _{GSM}	Continuous Transient		±3 ±4		V V	GDS			
_{D25} _{DM}	$T_c = 25^{\circ}C$ $T_c = 25^{\circ}C$, pulse width limited by T_{JM}		3	7 36	A A	5 Isolated back surface			
I _{AR} E _{AR} E _{AS}	$T_{c} = 25^{\circ}C$ $T_{c} = 25^{\circ}C$ $T_{c} = 25^{\circ}C$ $T_{c} = 25^{\circ}C$			6 30 .0	A mJ J	G = Gate D = Drain S = Source			
dv/dt	I _s ≤I _{DM} , di/dt ≤100 A/μs, V _{DD} ≤V _{DSS} , T _J ≤150°C, R _g = 10 Ω			10	V/ns				
P _D T _J T _{JM} T _{stg}	$T_c = 25^{\circ}C$	12 -55 +18 18 -55 +18		50 °C 50 °C		substrate - High power dissipation - Isolated mounting surface - 2500V electrical isolation • Low drain to tab capacitance(<35pF) • Low R _{DS (on)} HDMOS [™] process • Rugged polysilicon gate cell structure • Unclamped Inductive Switching (UIS)			
T _L T _{SOLD}	1.6 mm (0.062 in.) from case for 10 s Plastic body for 10 s 50/60 Hz, RMS, t = 1, leads-to-tab	s 300 260 2500			°C °C V~				
V _{ISOL}	Mounting Force				N/lb				
WeightSymbol $(T_j = 25^{\circ} C)$			2 g Characteristic Values Min. Typ. Max.			 Applications DC-DC converters Battery chargers Switched-mode and resonant-mode power supplies 			
BV _{DSS}	$V_{_{\rm GS}}$ = 0 V, I _D = 250 µA	800			V	DC choppersAC motor control			
V _{GS(th)}	$V_{_{\rm DS}} = V_{_{\rm GS}}, I_{_{\rm D}} = 2.5 \text{ mA}$	3.0		5.5	V	Advantages			
I _{gss}	$V_{_{\rm GS}}$ = ±30 V, $V_{_{\rm DS}}$ = 0 V			±100	nA	 Easy assembly: no screws, or isolation foils required 			
I _{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0 V$ $T_{J} = 125^{\circ}C$			25 750	μΑ μΑ	Space savingsHigh power density			
R _{DS(on)}	$V_{_{\rm GS}}$ = 10 V, $I_{_{\rm D}}$ = $I_{_{\rm T_{.}}}$ (Note 1) Pulse test, t \leq 300 µs, duty cycle d \leq 2 %				βmΩ	 Low collector capacitance to ground (low EMI) 			
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IXFC 12N80P

Symbol		Test Conditions	Characteristic Values (T _J = 25° C unless otherwise specified)					
			Min.	Тур.	Max.			
g _{fs}		V_{DS} = 20 V; I_{D} = I_{T} , pulse test	12	18	S			
C _{iss})			2800	pF			
C _{oss}	}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MH	Iz	210	pF			
C _{rss}	J			19	pF			
t _{d(on)})			21	ns			
t,		$V_{GS} = 10 \text{ V}, \text{ V}_{DS} = 0.5 \text{ V}_{DSS}, \text{ I}_{D} =$	Ι _τ	22	ns			
t _{d(off)}	(R_{g} = 10 Ω (External)		62	ns			
t _f	J			22	ns			
Q _{g(on)})			51	nC			
Q _{gs}	}	V_{GS} = 10 V, V_{DS} = 0.5 V_{DSS} , I_{D} =	I _T	13	nC			
Q _{gd}	J			19	nC			
R _{thJC}					1.05 °C/W			
$\mathbf{R}_{\mathrm{thCS}}$				0.21	° C/W			



Source-Dra		Characteristic Values					
Symbol	(T _j = 25° C unlessTest ConditionsMin.	s otherw Typ.	ise speci Max.	fied)			
I _s	$V_{GS} = 0 V$		12	Α			
I _{sm}	Repetitive		36	А			
$V_{\rm SD}$	$I_{_{\rm F}}$ = $I_{_{\rm S}}$, $V_{_{\rm GS}}$ = 0 V, Pulse test, t ≤300 µs, duty cycle d≤ 2 %		1.5	V			
t _{rr} }	I _F = 12 A, -di/dt = 100 A/μs V _R = 100 V, V _{GS} = 0 V	7	250	ns A			
Q _{RM})		0.7		μC			

Note 1: Test Current I_{τ} = 6 A

ADVANCE TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated objective result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

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IXYS MOSFETs and IGBTs are covered by 4,835,	92 4,931,844	5,049,961	5,237,481	6,162,665	6,404,065 B1	6,683,344	6,727,585	7,005,734 B2
one or moreof the following U.S. patents: 4,850,	5,017,508	5,063,307	5,381,025	6,259,123 B1	6,534,343	6,710,405 B2	6,759,692	7,063,975 B2
4,881,	06 5,034,796	5,187,117	5,486,715	6,306,728 B1	6,583,505	6,710,463	6,771,478 E	2 7,071,537