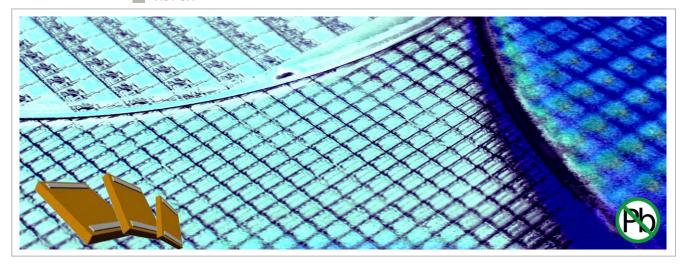


HTSC424.xxx - 0402 High Temperature Silicon Capacitor

Rev 3.1



Key features

- High stability up to 200°C:
 - Temperature <±1% (-55 °C to +200 °C)
 - Voltage <0.1 %/V
 - Negligible capacitance loss through aging
- Unique high capacitance in EIA/0402 package size, up to 47 nF
- High reliability (FIT <0.017 parts / billion hours)</p>
- Low leakage current down to 100 pA
- Low ESL and Low ESR
- Suitable for lead free reflow-soldering *Please refer to our assembly Application Note for further recommendations

Thanks to the unique IPDiA Silicon capacitor technology, most of the problems encountered in demanding applications can be solved.

High Temperature Silicon Capacitors are dedicated to applications where **reliability** up to **200°C** is the main parameter.

This technology features a capacitor integration capability (up to 250nF/mm²) which offers capacitance value similar to X7R dielectric, but with better electrical performances than C0G/NP0 dielectrics, up **to 200°C**.

HTSC provides the highest capacitor **stability** over the full -55°C/+200°C temperature range in the market with a **Temperature coefficient Lower than ±1%.**

Key applications

- All applications up to 200°C, such as military, aerospace and automotive industries
- High reliability applications
- Replacement of X7R and C0G dielectrics
- Decoupling / Filtering / Charge pump (i.e.: motor management, temperature sensors)
- Downsizing

The IPDiA technology offers industry leading performances relative to **Failure rate** with a FIT<0.017.

This technology also offers **high reliability**, up to 10 times better than alternative capacitor technologies, such as Tantalum or MLCC, and eliminates cracking phenomena.

This Silicon based technology is RoHS compliant and compatible with lead free reflow soldering process.



Parameters

Capacitance range

Capacitance tolerances

Storage temperatures

Temperature coefficient

Breakdown voltage (BV)

Insulation resistance

Capacitor height

RVDC

Ageing Reliability

Operating temperature range

Capacitance variation versus

Equivalent Serial Inductor (ESL)

Equivalent Serial Resistor (ESR)

Value

<±1 %, from -55 °C to +200 °C

0.1 % /V (from 0 V to RVDC)

20GΩ min @ 3V,200°C

Negligible, < 0.001 % / 1000 h FIT<0.017 parts / billion hours,

100 pF to 100 nF^{(*}

-55 °C to 200 °C

- 70 °C to 215 °C

11 VDC, 30VDC

Max 100 pH

Max 400mΩ^(***) 50GΩ min @ 3V.25°C

Max 400 µm 🖱

±15 %

Electrical specification

		Capacitance value					
		10	15	22	33	47	68
Unit	1 pF	Contact IPDIA Sales	Contact IPDIA Sales				
	10 pF	100 pF: 935.132.424.310	150 pF: 935.132.424.315	220 pF: 935.132.424.322	330 pF: 935.132.424.333	470 pF: 935.132.424.347	680 pF: 935.132.424.368
	0.1 nF	1 nF: 935.132.424.410	1.5 nF: 935.132.424.415	2.2 nF: 935.132.424.422	3.3 nF: 935.132.424.433	4.7 nF: 935.132.424.447	6.8 nF: 935.132.424.468
	1 nF	10 nF: 935.132.424.510	15 nF: 935.132.424.515	22 nF: 935.132.424.522	33 nF: 935.132.424.533	47 nF: 935.132.424.547 935.132.724.547	Contact IPDIA Sales
	10 nF	100 nF: 935.132.424.610					

(*) Thinner thickness (as low as 100 µm thick) available, see Low Profile Silicon Capacitor product: LPSC

(**) Extended temperature range (up to +250 °C) available, see Xtreme Temperature Silicon Capacitor product: XTSC

(***) Other values on request.

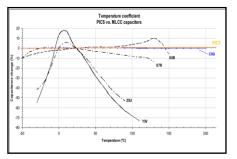
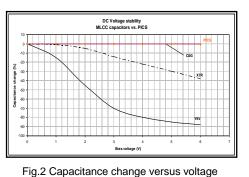


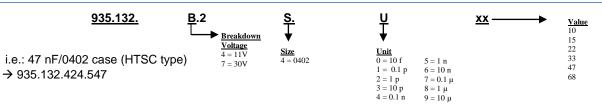
Fig.1 Capacitance change versus temperature variation compared with alternative dielectrics



ESL (nH) @25'C 0402 C0G(NPO) ys. PICS

Fig.3 ESL versus capacitance value compared with alternative dielectrics

Part Number



variation compared with alternative dielectrics

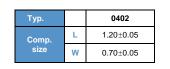
Termination and Outline

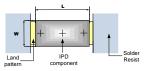
Termination

Lead-free nickel/solder coating compatible with automatic soldering technologies: reflow and manual.

Typical dimensions, all dimensions in mm.

Package outline





(0402 PCB footprint)

Packaging

Tape and reel, tray, waffle pack or wafer delivery.

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