

[Home](#) > [Products](#) > [Clock / Timing Devices](#) > [PC-Notebook-Server Clocks](#) > [Clock Synthesizer by Chipset Vendor](#) > [Desktop Chipsets](#) > [954305](#) > [954305DKLF](#)

[Add to myIDT \[?\]](#)

You may also like...

954305DKLF

Category: Desktop Chipsets

Generic Part: 954305

Market Group: PC CLOCK

Description: PC MAIN CLOCK

The ICS954305 is a CK410M+ Yellow Cover clock synthesizer. The ICS954305 provides a single-chip solution for mobile systems built with Intel P4-M processors and Intel mobile chipsets. The ICS954305 is driven with a 14.318MHz crystal and generates CPU outputs up to 400MHz.

Output Features:

- 2 - 0.7V current-mode differential CPU pairs
- 9 - 0.7V current-mode differential SRC pair for SATA and PCI Express*
- 1 - 0.7V current-mode differential CPU/SRC selectable pair
- 4 - PCI (33MHz)
- 1 - PCICLK_F, (33MHz) free-running
- 1 - USB, 48MHz
- 1 - DOT, 96MHz, 0.7V current differential pair
- 2 - REF, double-strength 14.318MHz
- 1 - 0.7V current-mode differential LCD spread/SRC selectable clock
- 1 - 100MHz/96MHz selectable LCD spread clock



Parameters

Package	VFQFPN 72 (NLG72)
Voltage	3.3 V
Package	VFQFPN 72
Speed	NA
Temperature	C
Status	Active
Sample	Yes
Minimum Order Quantity	120
Factory Order Increment	30

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

Type	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007
Product Change Notice	PCN# : A0701-02 Punch Singulation as Alternate Assembly Process for 10mm x 10mm VFQFP-N-72	514 KB	01/30/2007
	PCN# : A0701-02R1 Punch Singulation as Alternate Assembly Process for 10mm x 10mm VFQFP-N-72 and transfer Test facility from IDT Singapore to IDT Penang	453 KB	04/24/2007

Package

Description	VFQFP-N 10.0 X 10.0 X 0.9 MM - NO LEAD
Class	PLASTIC
Moisture Sensitivity Level (MSL)	3
Category	Green
Moisture Exposure Floor Life	168 hrs. @ <30°C/60%RH
Peak Reflow Temperature	260°C
Rebake Conditions	48 hrs.@125°C
Length	10.0
Mark	K
Width	10.0
Pitch	0.5
Thickness	1.0
Status	Active