



English | 简体中文 | 日本語

Search Entire Site [GO](#)

[Contact IDT](#) | [Investors](#) | [Press](#)

[About IDT](#)

[Products](#)

[Applications](#)

[Support](#)

[myIDT](#)

[Document Search](#) | [Package Search](#) | [Parametric Search](#) | [Cross Reference Search](#) | [Green & RoHS](#) | [Calculators](#) | [Thermal Data](#) | [Reliability & Quality](#) | [Military](#)

[Home](#) > [Products](#) > > [952801](#) > 952801CFLFT

[Add to myIDT \[?\]](#)

You may also like...

952801CFLFT

Category:

Generic Part: [952801](#)

Market Group: PC CLOCK

Description: PC MAIN CLOCK

The ICS952801 is a two chip clock solution for desktop designs using SIS 755/760 style chipsets. When used with a zero delay buffer such as the ICS9179-16 for PC133 or the ICS93735 for DDR applications it provides all the necessary clocks signals for such a system. Output Features: â€¢ 2 - Pairs of differential push-pull K8CPU outputs â€¢ 8 - PCICLK @ 3.3V â€¢ 2 - AGPCLK @ 3.3V â€¢ 3 - REF @ 3.3V â€¢ 2 - ZCLK @ 3.3V â€¢ 1 - 24_48MHz @ 3.3V â€¢ 1 - 48MHz @ 3.3V

Time is money

Parameters

Package	SSOP 48 (PVG48)
Speed	NA
Temperature	C
Voltage	3.3 V
Status	Active
Sample	No
Minimum Order Quantity	1000
Factory Order Increment	1000

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

Type	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007

Package

Description	SSOP 300 MIL, 48LD
Class	PLASTIC
Moisture Sensitivity Level (MSL)	1
Category	Green
Moisture Exposure Floor Life	Unlimited @ <30°C/85% RH
Peak Reflow Temperature	260°C
Rebake Conditions	N/A
Length	15.9
Mark	PVG
Width	7.5
Pitch	0.64
Thickness	2.3
Status	Active

[Home](#) | [Site Map](#) | [Legal](#) | [Careers](#) | [Contact Us](#) | [Quality](#) | [RSS](#) What is this?

Use of this website signifies your agreement to the [acceptable use](#) and [privacy policy](#). Copyright © 1997-2008 Integrated Device Technology, Inc. All Rights Reserved.