

125.00 MHz LVPECL Oscillator High Performance Differential MEMS Oscillator

4MA125000Z3

DATASHEET

Features

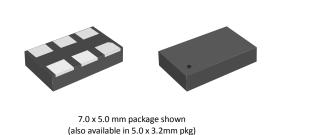
- Frequency:
- Output Type:
- Frequency Stability:
- Supply Voltage:
- - 5.0 x 3.2 mm; 7.0 x 5.0 mm

125.00 MHz

LVPECL

± 50ppm

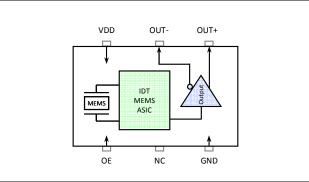
- Standard Packages: RMS phase jitter:
- Operating Temperature:
- 2.5V & 3.3V 0.6ps typical (12k to 20MHz) - 40 to 85 °C



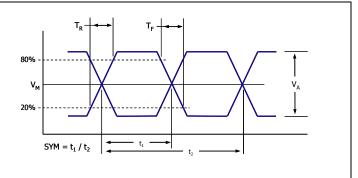
Specification

Parameter	2.5 V Specifications			3.3 V Specifications			Units	Conditions
	Min	Тур	Max	Min	Тур	Max		
Supply Voltage (V _{DD})	2.375	2.50	2.625	2.97	3.30	3.63	V	
Output Frequency		125.00			125.00		MHz	
Frequency Stability	- 50		+ 50	- 50		+ 50	ppm	Includes supply voltage and temperature variation (-40 to 85°C), reflow drift, and aging.
Supply Current		95			100		mA	No load
Enable/Disable Time			1			1	us	Guaranteed by design
Input LOW level			$0.3V_{\text{DD}}$			$0.3V_{\text{DD}}$	V	At OE pin
Input HIGH level	$0.7V_{DD}$			0. $7V_{DD}$			V	At OE pin
Output LOW level		0.8	Vdd -1.8		1.5	Vdd -1.8	V	
Output HIGH level	Vpp-1.0	1.6		VDD -1.1	2.3		V	
Amplitude (V _A)		0.75			0.75		V	Single Ended output swing (Pk-Pk)
Mid Level (V _M)		Vdd -1.3			Vod - 1.3		V	
Rise Time (T _R)		200	240		200	240	ps	Maximum; 20/80% of V_A ; Output load (CL) = 2pF; Guaranteed by Char.
Fall Time (T _F)		200	240		200	240	ps	Maximum; 20/80% of V_A ; Output load (CL) = 2pF; Guaranteed by Char.
Symmetry (SYM)	48	50	52	48	50	52	%	Worst case; measured at 50% of waveform
Phase Jitter		0.9			0.6		ps	12k to 20MHz, RMS; Measured Differentially
Period Jitter		2.4			2.2		ps	RMS
Cycle-to-Cycle Jitter		18			16		ps	1,000 cycles, Peak
Start-up Time		10			10		ms	Output valid time after power up, 25°C
Aging		± 5			± 5		ppm	25°C, 10 years

Block Diagram

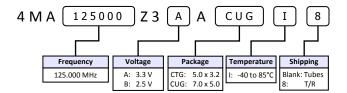


Output Waveform



Part Ordering Information

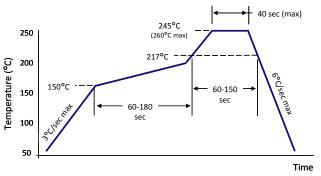
Package Size	Voltage	Ordering Code		
7.0 x 5.0 mm	3.3 V	4MA125000Z3AACUGI		
	2.5 V	4MA125000Z3BACUGI		
5.0 x 3.2 mm	3.3 V	4MA125000Z3AACTGI		
	2.5 V	4MA125000Z3BACTGI		
 Factory minimum order quantity: 500pcs (T/R) 				



Pin Description

Pin #	Name	Description			
1	OE	Output Enable*			
2	NC	No Connect			
3	GND	Ground			
4	OUT+	Output			
5	OUT-	Complementary Output			
6	VDD	Power Supply Voltage			
* Pulled high internally					

Solder Reflow Profile



Package Outline and Dimensions Typical PCB Land Pattern Pin #1 ID → 3.20 ±0.05 → 1.00 ± 0.05 0.85 ±0.05 2.60 Chamfer 0.0-0.05 0.30 x 45° 0.64 ±0.05 VDD OE + 6L SMD OUT-5.00 ±0.05 ⊻ NC ý 5.0 x 3.2mm -0.30 Ref 1.27 Bsc OUT+ GND + ★ ⋪ 0.90 0.203 Ref. 1.50 3.60 5.0 ±0.05 → 0.85 ±0.05 1.30 ±0.05 0.0-0.05 OE VDD 1.50 ± 0.05 Pin #1 ID Chamfer 0.30 x 45° 6L SMD .08 NC OUT-7.00 ±0.05 ↑ 7.0 x 5.0mm 2.54 Bsc ᡟ ¥ GND OUT+ + 1.00 Ref ٨ 1.60 0.203 Ref. 1.60-Unit: (mm)

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