

Fast switching diode chip in EMCON 3-Technology

FEATURES:

- 600V EMCON 3 technology 70 µm chip
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient

This chip is used for:

- power module
- discrete components



Applications:

drives

Chip Type	V_R	I _F	Die Size	Package
SIDC14D60C6	600V	50A	4.6 x 3.05 mm ²	sawn on foil

MECHANICAL PARAMETER:

Raster size	4.6 x 3.05				
Area total / active	14.03 / 11.12	mm ²			
Anode pad size	3.9 x 2.35				
Thickness	70	μm			
Wafer size	150	mm			
Flat position	180	deg			
Max. possible chips per wafer	1013 pcs				
Passivation frontside	Photoimide				
Anode metallization	3200 nm AlSiCu				
Cathode metallization	Ni Ag -system suitable for epoxy and soft solder die	Ni Ag –system suitable for epoxy and soft solder die bonding			
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤500μm				
Reject ink dot size	Ø 0.65mm; max 1.2mm				
Recommended storage environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				



Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V_{RRM}		600	V
Continuous forward current limited by	1_		1)	
T_{jmax}	I _F			Α
Maximum repetitive forward current	l		100	
limited by T _{jmax}	/ FRM		100	
Operating junction and storage temperature	$T_{\rm j}$, $T_{ m stg}$		-40+175	°C

¹⁾ depending on thermal properties of assembly

Static Electrical Characteristics (tested on chip), T_i =25 °C, unless otherwise specified

Parameter	Symbol	Cond	Value			Unit	
Parameter Symbol Condition		itions	min.	Тур.	max.	Oille	
Reverse leakage current	I_{R}	V _R =600V	<i>T_j</i> =25 °C			27	μΑ
Cathode-Anode breakdown Voltage	V _{Br}	I _R =0.25mA	<i>T_j</i> =25°C	600			V
Forward voltage drop	V_{F}	I _F =50A	<i>T_j</i> =25 °C	1.2	1.6	1.9	V

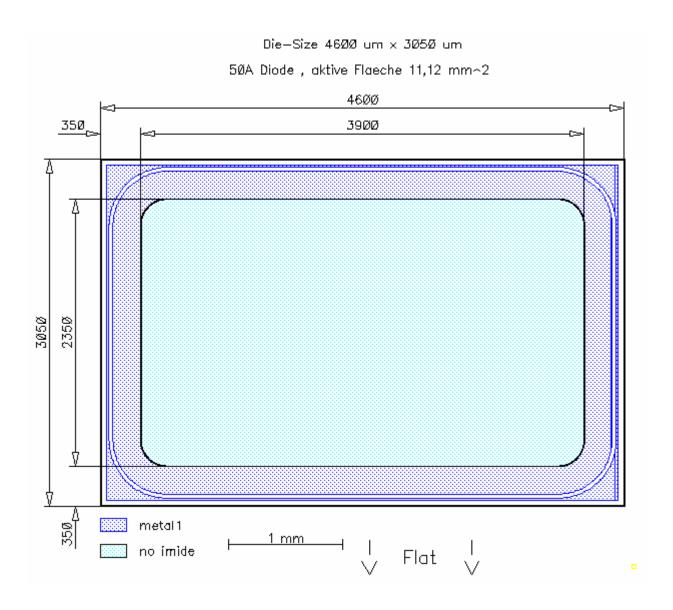
Dynamic Electrical Characteristics (verified by design/characterization), inductive load

Parameter	Symbol	Conditions		Value 2)			Unit
raiailletei	Syllibol			min.	Тур.	max.	
Peak reverse recovery current	I _{RM}	$I_F=50A$ $di/dt=2800A/ms$ $V_R=300V$ $V_{GE}=-15V$	$T_j = 25 \text{ °C}$ $T_j = 125 \text{ °C}$ $T_j = 150 \text{ °C}$		69.0 76.0 80.0		А
Recovered charge	Q _r	I_F =50A di/dt=2800A/ms V_R =300V V_{GE} = -15V	$T_j = 25 ^{\circ}\text{C}$ $T_j = 125 ^{\circ}\text{C}$ $T_j = 150 ^{\circ}\text{C}$		1.90 3.40 3.95		μC
Reverse recovery energy	E _{rec}	I_F =50A di/dt=2800A/ms V_R =300V V_{GE} = -15V	$T_j = 25 \text{ °C}$ $T_j = 125 \text{ °C}$ $T_j = 150 \text{ °C}$		0.60 0.95 1.10		mJ

²⁾ values also influenced by parasitic L- and C- in measurement and package.



CHIP DRAWING:





FURTHER ELECTRICAL CHARACTERISTICS	:	
This chip data sheet refers to the device data sheet	FS50R06KE3	
Description:		
AQL 0,65 for visual inspection according to failu	ıre catalog	
Electrostatic Discharge Sensitive Device accord	ding to MIL-STD 883	
Test-Normen Villach/Prüffeld		

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