

#### **NPN Silicon RF Transistor\***

- For low-distortion broadband output amplifier stages in antenna and telecommunication systems up to 2 GHz at collector currents from 120 mA to 250 mA
- Power amplifiers for DECT and PCN systems
- Integrated emitter ballast resistor
- $f_T = 5.5 \text{ GHz}$
- Pb-free (RoHS compliant) package 1)
- Qualified according AEC Q101
- \* Short term description





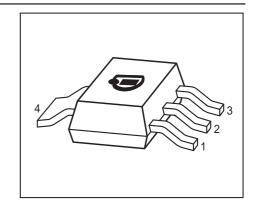
### **ESD** (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Marking	Pin Configuration					Package	
BFG235	BFG235	1 = E	2 = B	3 = E	4 = C	-	-	SOT223

### **Maximum Ratings**

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{\sf CEO}$	15	V
Collector-emitter voltage	$V_{CES}$	25	
Collector-base voltage	$V_{CBO}$	25	
Emitter-base voltage	$V_{EBO}$	2	
Collector current	I <sub>C</sub>	300	mA
Base current	I <sub>B</sub>	40	
Total power dissipation <sup>2)</sup>	P <sub>tot</sub>	2	W
<i>T</i> <sub>S</sub> ≤ 80°C			
Junction temperature	$T_{i}$	150	°C
Ambient temperature	$T_{A}$	-65 150	
Storage temperature	$T_{ m stg}$	-65 150	

<sup>&</sup>lt;sup>1</sup>Pb-containing package may be available upon special request



<sup>&</sup>lt;sup>2</sup>T<sub>S</sub> is measured on the collector lead at the soldering point to the pcb



### **Thermal Resistance**

Parameter	Symbol	Value	Unit
Junction - soldering point <sup>1)</sup>	R <sub>thJS</sub>	≤ 35	K/W

# **Electrical Characteristics** at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics	,				
Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	15	-	-	V
$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$					
Collector-emitter cutoff current	I <sub>CES</sub>	-	-	200	μA
$V_{CE} = 25 \text{ V}, \ V_{BE} = 0$					
Collector-base cutoff current	I <sub>CBO</sub>	-	-	100	nA
$V_{CB} = 10 \text{ V}, I_{E} = 0$					
Emitter-base cutoff current	I <sub>EBO</sub>	-	-	2	μA
$V_{\rm EB} = 1 \text{ V}, I_{\rm C} = 0$					
DC current gain-	h <sub>FE</sub>	75	120	160	
$I_{\rm C}$ = 200 mA, $V_{\rm CE}$ = 8 V, pulse measured					

 $<sup>^{1}\</sup>mbox{For calculation of}~R_{\mbox{\scriptsize thJA}}$  please refer to Application Note Thermal Resistance



**Electrical Characteristics** at  $T_A = 25$ °C, unless otherwise specified

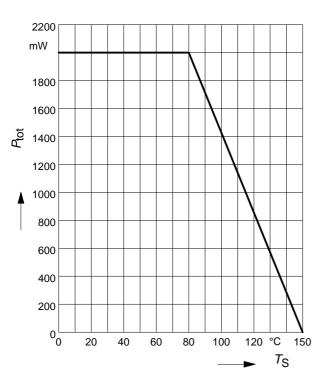
Symbol		Values			
	min.	typ.	max.		
g)					
$f_{T}$	4	5.5	-	GHz	
C <sub>cb</sub>	-	2.2	3	pF	
C <sub>ce</sub>	-	1.5	-		
C <sub>eb</sub>	-	14	-		
F	-	1.7	-	dB	
G <sub>ma</sub>	-	12.5	-		
S <sub>21e</sub>   <sup>2</sup>	-	6.5	-	dB	
IP <sub>3</sub>	-	33	-	dBm	
	g) $f_{\rm T}$ $C_{\rm cb}$ $C_{\rm ce}$ $F$ $G_{\rm ma}$	$min.$ $g)$ $f_T$ $A$ $C_{Cb}$ $C_{Ce}$ $F$	min.     typ.       g) $f_T$ 4     5.5 $C_{Cb}$ -     2.2 $C_{Ce}$ -     1.5 $C_{eb}$ -     14 $F$ -     1.7 $G_{ma}$ -     12.5 $ S_{21e} ^2$ -     6.5	min.     typ.     max.       g) $f_T$ 4     5.5     - $C_{cb}$ -     2.2     3 $C_{ce}$ -     1.5     - $F$ -     1.7     - $G_{ma}$ -     12.5     - $ S_{21e} ^2$ -     6.5     -	

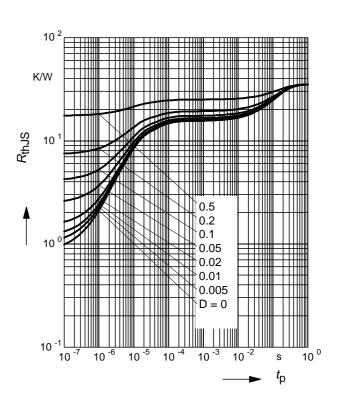
 $<sup>{}^{1}</sup>G_{ma} = |S_{21}/S_{12}| (k-(k^{2}-1)^{1/2})$ 



# Total power dissipation $P_{\text{tot}} = f(T_{\text{S}})$

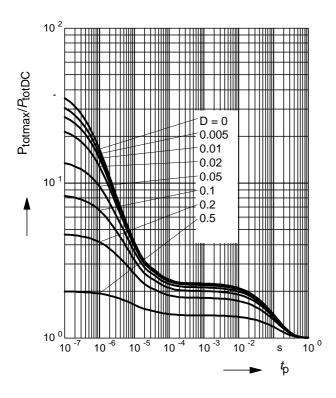
# Permissible Pulse Load $R_{thJS} = f(t_p)$



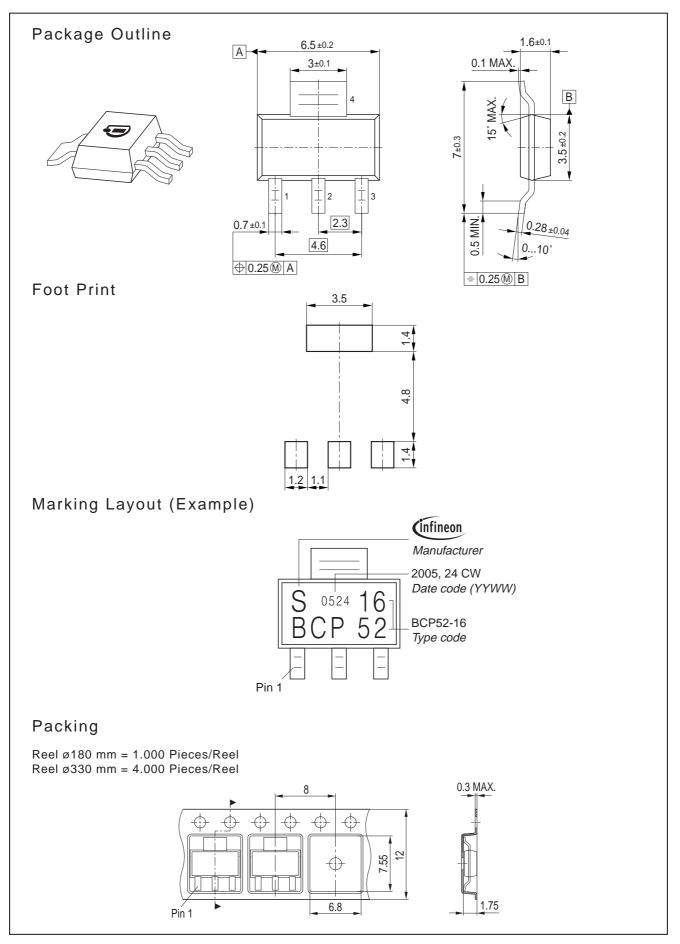


### **Permissible Pulse Load**

$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_{p})$$









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