### May 9<sup>th</sup> 2012 Automotive grade AUIPS2041(R)(L)

# INTELLIGENT POWER LOW SIDE SWITCH

#### Features

- Over temperature shutdown
- Over current shutdown
- Active clamp
- Low current & logic level input
- ESD protection
- Optimized Turn On/Off for EMI
- Diagnostic on the input current

### Description

The AUIPS2041(R)(L) is a three terminal Intelligent Power Switch (IPS) that features a low side MOSFET with overcurrent, over-temperature, ESD protection and drain to source active clamp. This device offers protections and the high reliability required in harsh environments. The switch provides efficient protection by turning OFF the power MOSFET when the temperature exceeds 165°C or when the drain current reaches 5A. The device restarts once the input is cycled. A serial resistance connected to the input provides the diagnostic. The avalanche capability is significantly enhanced by the active clamp and covers most inductive load demagnetizations.

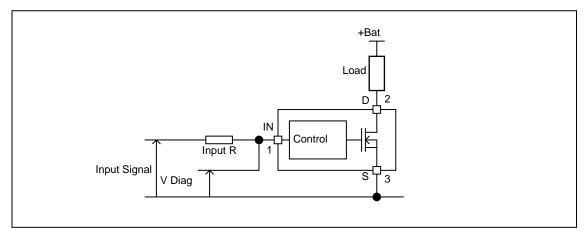
### **Product Summary**

Rds(on)	130mΩ (max.)
Vclamp	68V
Ishutdown	5A (typ.)

#### Packages



### **Typical Connection**



### **Qualification Information**<sup>†</sup>

Qualifica	tion Level	Automotive (per AEC-Q100 <sup>††</sup> ) Comments: This family of ICs has passed an Automotive qualification. Industrial and Consumer qualification level is granted by extension of the hig Automotive level.		
Moiotura	Sonoitivity Lovel	DPAK-3L	MSL1, 260°C (per IPC/JEDEC J-STD-020)	
woisture	Sensitivity Level	SOT223-3L	MSL2, 260°C <sup>†††</sup> (per IPC/JEDEC J-STD-020)	
	Machine Model		s M4 (+/-450V) AEC-Q100-003)	
ESD	Human Body Model		s H3A (+/4500V) AEC-Q100-002)	
	Charged Device Model	Class C4 (+/-900V) (per AEC-Q100-011)		
IC Latch	-Up Test		assII, Level A AEC-Q100-004)	
RoHS Co	ompliant		Yes	

† Qualification standards can be found at International Rectifier's web site <u>http://www.irf.com/</u>

t+ Exceptions to AEC-Q100 requirements are noted in the qualification report.

+++ Higher MSL ratings may be available for the specific package types listed here. Please contact your International Rectifier sales representative for further information.

#### **Absolute Maximum Ratings**

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. (Tj= -40°C..150°C, Vcc=6..50V unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vds	Maximum drain to source voltage	-0.3	60	V
Vin	Maximum input voltage	-0.3	6	V
Isd cont.	Max diode continuous current (limited by thermal dissipation) Rth=125°C/W	_	1.4	А
Pd	Maximum power dissipation (internally limited by thermal protection) Rth=125C°/W		1	W
Tj max.	Max. storage & operating temperature junction temperature	-40	150	°C

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
Rth1	Thermal resistance junction to ambient IPS2041L	100		
Rth2	Thermal resistance junction to ambient with 1" square footprint	50		
Rth1	Thermal resistance junction to ambient IPS2041R D-Pak std. footprint	70		°C/W
Rth2	Thermal resistance junction to ambient IPS2041R D-Pak 1" sqr. footprint	50		
Rth3	Thermal resistance junction to case IPS2041R D-Pak	4		

**Recommended Operating Conditions** These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
VIH	High level input voltage	4	5.5	
VIL	Low level input voltage	0	0.5	
lds	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V,Rth=100°C/W	_	1.4	А
Rin	Recommended resistor in series with IN pin to generate a diagnostic	0.5	5	kΩ
Max L	Max recommended load inductance (including line inductance) (1)	_	10	mН
Max. t rise	Max. input rising time	_	1	μs

(1) Higher inductance is possible if maximum load current is limited - see figure 11

### **Static Electrical Characteristics**

Tj= -40..150°C, Vcc=14V (unless otherwise specified), typical value are given for Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Rds(on)	ON state resistance Tj=25°C	_	100	130	mΩ	Vin=5V, Ids=1A
	ON state resistance Tj=150°C(2)	—	180	240	1115.2	VIII=5V, IUS=TA
ldss1	Drain to source leakage current	_	0.01	1		Vcc=14V, Tj=25°C
ldss2	Drain to source leakage current	_	0.04	2	μA	Vcc=50V, Tj=25°C
V clamp1	Drain to source clamp voltage 1	63	68	—		Id=20mA See fig. 3 & 4
V clamp2	Drain to source clamp voltage 2	_	68	75	V	Id=1A
Vin clamp	IN to source pin clamp voltage	5.5	6.2	7.5	v	lin=1mA
Vth	Input threshold voltage	1.1	2	2.8		Id=50mA
lin, on	ON state IN positive current	10	40	80		Vin=5V
lin, off	OFF state IN positive current	120	250	350	μA	
	(after protection latched)					

### **Switching Electrical Characteristics**

Tj= -40..150°C, Vcc=14V, Resistive load=10Ω, Rinput=50Ω, Vin=5V, typical value are given for Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tdon	Turn-on delay time to 20%	0.25	1.3	4		
Tr	Rise time 20% to 80%	0.1	0.9	2	110	See figure 2
Tdoff	Turn-off delay time to 80%	1	3	8	μs	See ligure 2
Tf	Fall time 80% to 20%	0.1	0.6	3		
Eon + Eoff	Turn on and off energy	_	12	_	μJ	

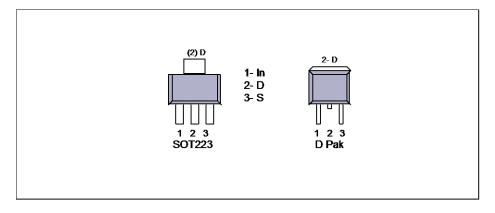
### **Protection Characteristics**

Tj= -40..150°C, Vcc=14V (unless otherwise specified), typical value are given for Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tsd	Over temperature threshold	150(2)	165	—	°C	See figure 1
lsd	Over current threshold	4	5.5	7.5	۸	Tj=25150°C see fig 1
		3.6	6	8	A	Tj= -40°C
Vreset	IN protection reset threshold	0.9	1.6	2	V	
Treset	Time to reset protection	15	50	500	μs	Vin=0V

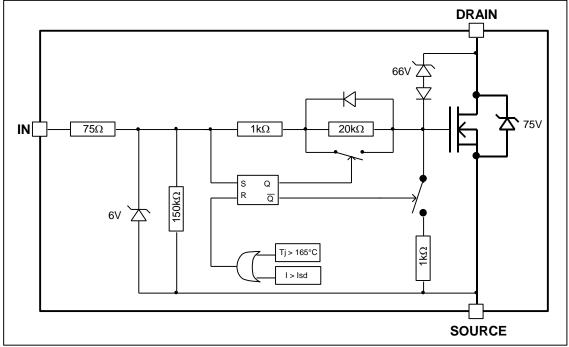
(2) Guaranteed by design

### Lead Assignments

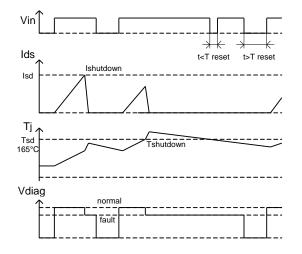


### **Functional Block Diagram**

All values are typical



### AUIPS2041(L)(R)



All curves are typical values. Operating in the shaded area is not recommended.

Figure 1 – Timing diagram

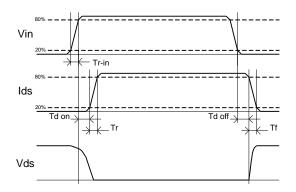


Figure 2 – IN rise time & switching definitions

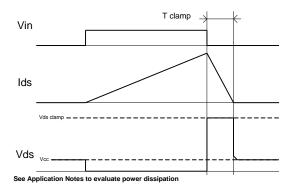


Figure 3 – Active clamp waveforms

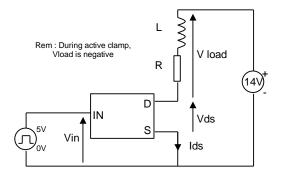


Figure 4 – Active clamp test circuit

# International **IOR** Rectifier

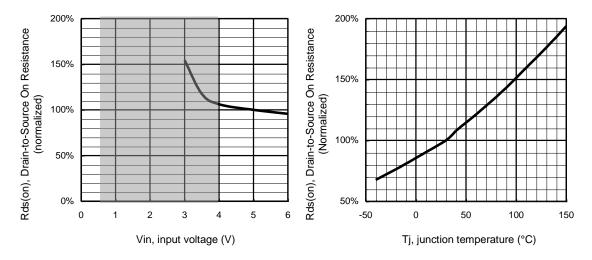
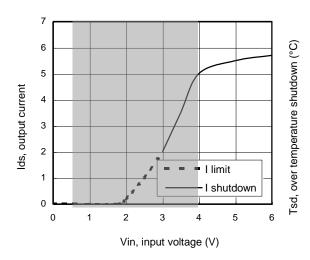
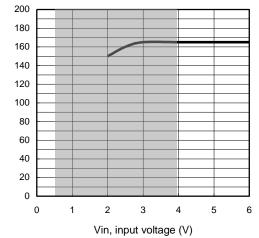
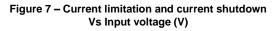


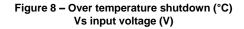
Figure 5 – Normalized Rdson (%) Vs Input voltage (V)



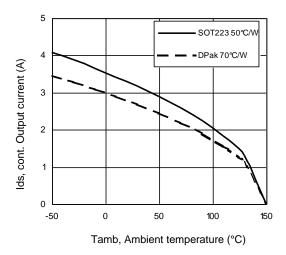


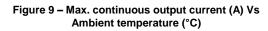


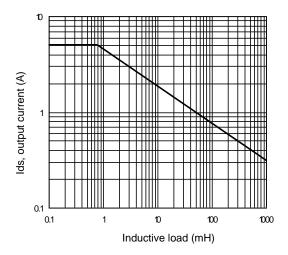


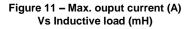


# International **TOR** Rectifier









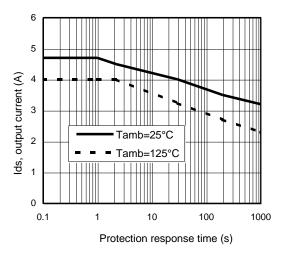


Figure 10 – Ids (A) Vs over temperature protection response time (s)

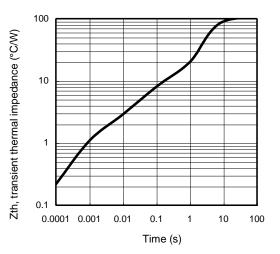
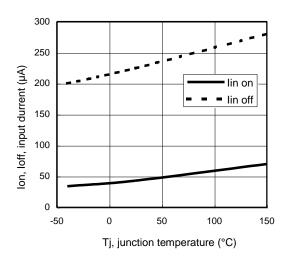
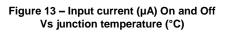


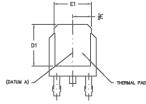
Figure 12 – Transient thermal impedance (°C/W) Vs time (s)

# International **ICR** Rectifier

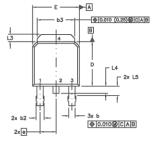


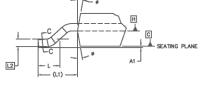


### Case outline - Dpak - Automotive Q100 PbF MSL1 qualified

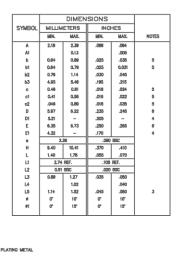


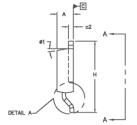






DETAIL "A" ROTATED 90\*





NOTES:

PLATING

DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994. DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS], LEAD DIMENSION UNCONTROLLED IN L5 1.0

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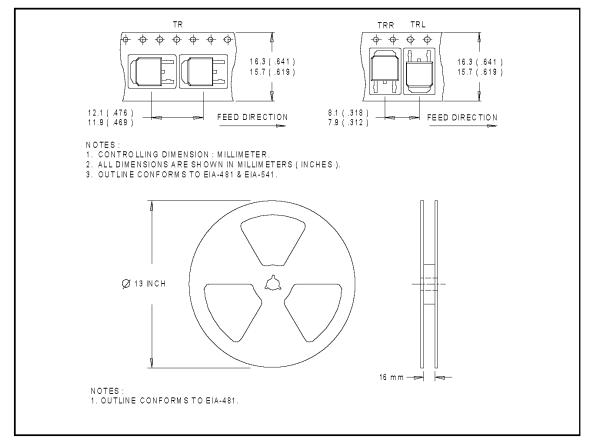
2.0

SECTION C-C

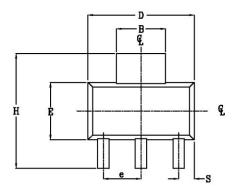
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- 7.0 OUTLINE CONFORMS TO JEDEC OUTLINE TO-252AA.
- LEADS AND DRAIN ARE PLITED WITH 100% Sn 8.0

### Tape & Reel – Dpak

Dimensions are shown in millimeters (inches)



### Case Outline - SOT-223 - Automotive Q100 PbF MSL2 qualified



POS	MILLIME	TERS	INC	HES	
1	MAX	MIN	MAX	MIN	
A	1.70	1.50	.067	.060	
A1	0.10	0.02	.004	.0008	
B	3.15	2.95	.124	.116	
B1	0.85	0.65	.033	.026	
C	0.35	0.25	.014	.010	
D	6.70	6.30	.264	.248	
e	2.30	NOM	.0905 NOM		
e1	4.60	NOM	.181 NOM		
E	3.70	3.30	.146	.130	
H	7.30	6.70	.287	.264	
S	1.05	0.85	.041	.033	
t	1.30	1.10	.051	.043	
Θ	10° h	(AX	10°	MAX	
Θ1	16°	10"	16*	10"	
Θ2	16"	10*	16"	10*	
L	0.75	MIN	0.02	95 MIN	

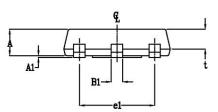
E1

0.25

NOTE: 1. PACKAGE OUTLINE EXCLUSIVE OF ANY MOLD FLASHES DELENSION. 2. PACKAGE OUTLINE EXCLUSIVE OF BURR DIMENSION. G

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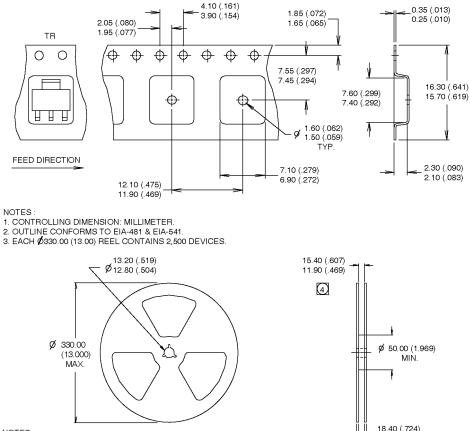


Leads and drain are plated with 100% Sn

### International **IOR** Rectifier

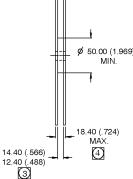
#### Tape & Reel - SOT-223

Dimensions are shown in milimeters (inches)



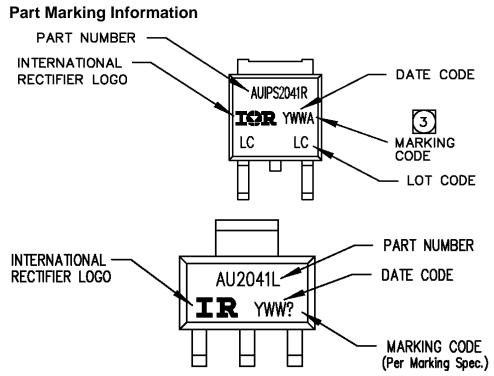
NOTES :

- OUTLINE COMFORMS TO EIA-418-1. 1.
- 2. CONTROLLING DIMENSION: MILLIMETER. DIMENSION MEASURED @ HUB.
- 3
- INCLUDES FLANGE DISTORTION @ OUTER EDGE.



**IOR** Rectifier

International



### **Ordering Information**

Base Part Number		Standard Pack		
Dase Fait Number	Package Type	Form	Quantity	Complete Part Number
		Tube	75	AUIPS2041R
AUIPS2041L	D-Pak-5-Lead	Tape and reel	2000	AUIPS2041RTR
AUIPS2041L		Tape and reel left	3000	AUIPS2041RTRL
		Tape and reel right	3000	AUIPS2041RTRR
AUIPS2041L	SOT223	Tube	80	AUIPS2041L
AUIP32041L	301223	Tape and reel	2500	AUIPS2041LTR

# International

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### **Revision History**

Revision	Date	Notes/Changes
С	26/02/2009	AU number update / OV removed
D	09/12/2010	Final release
E	14/03/2011	Update part marking
F	November, 14 <sup>th</sup> 2011	Update T&R SOT223
G	May 9 <sup>th</sup> , 2012	Update the component number of the SOT223 tube