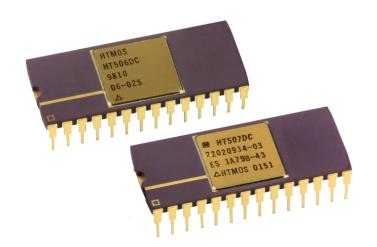


High Temperature Analog Multiplexers 16-Channel Single-Ended / 8-Channel Differential HT506 / HT507

The High Temperature HT506/HT507 monolithic multiplexers consist of sixteen analog switches, 4-bit decode for channel selection, reference for logic switching thresholds, and enable pin for device deactivation where applications require. These multiplexers are fabricated with Honeywell's dielectrically isolated latch-up free high temperature (HTMOS™) linear process. Performance is specified over the full -55 to +225°C temperature range. Typically, parts will operate up to +300°C for a year, with derated performance. All parts are burned in at 250°C. The input buffers are designed to operate from either TTL or CMOS levels while providing a break-before-make action. The HT506 switches one of the sixteen single-ended inputs to a common output, while the HT507 switches one of the eight differential inputs to a differential output. These parts are available in standard pinout 28-pin DIP Ceramic Packages.

Applications

- Down-Hole Oil, Gas, and Geothermal Well
- Avionics
- Turbine Engine Control
- Industrial Process Control
- Electric Power Conversion
- Heavy Duty Internal Combustion Engine



FEATURES AND BENEFITS

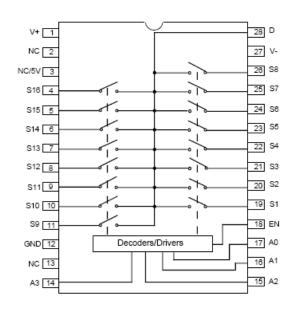
- ▶ Specified over -55 to +225°C
- ▶ 16:1 Single-Ended or 8:1 Differential Configuration
- No latch-up
- On resistance 400Ω at 225°C
- ▶ Output leakage less than 2.5µA at 225°C

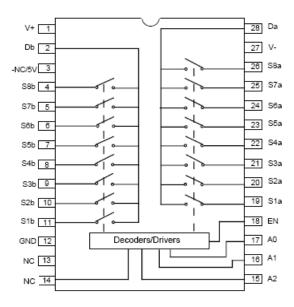
- ▶ Designed to continuously operate for at least 5 years at 225°C
- Enable and address inputs compatible with TTL and/or 5V CMOS logic
- ▶ 10V analog input/output range (±5V or 0 to 10V)
- Split and single supply capability
- Break-Before-Make Switching

PACKAGE PINOUTS

PACKAGE PINOUT HT506

PACKAGE PINOUT HT507





ELECTRICAL CHARACTERISTICS (Split Supply)

Temperature range -55 to +225°C, typical @ +25°C, V+ = +5V, V- = -5V, GND=0V, V_{IL}=0.8, V_{IH} = 2.4V, unless otherwise specified

Symbol	Parameters	Test Conditions	Typical	Worst Case (2)		Units		
- ,			(1)	MIN	MAX			
Analog Swi	Analog Switch							
V _{ANALOG}	Analog Signal Range			-5	5	V		
r _{DS(ON)}	Drain-Source On-Resistance	V _D ±5V, I _S = -10mA Sequence Each Switch On			400	Ω		
$\Delta r_{DS(ON)}$	r _{DS(ON)} Matching between Channels	$V_D = \pm 5V$	2			%		
I _{S(OFF)}	Source Off Leakage Current	$V_{EN} = 0V$	0.01		200	nA		
I _{D(OFF)}	Drain Off Leakage Current	$V_{D} = \pm 5V, V_{EN} = 0V, V_{S} = \pm 5V$	0.04	-2500	2500	nA		
I _{D(ON)}	Drain On Leakage Current	Sequence Each Switch On	0.04	-2500	2500	nA		
Digital Conf	trol							
V _{IH}	Logic High Input Voltage			2.4		V		
V _{IL}	Logic Low Input Voltage		0.8			V		
I _{IH}	Logic High Input Current	$V_A = 2.4V, 10V$		-1	1	μΑ		
I _{IL}	Logic Low Input Current	$V_{EN} = 0V, 2.4V, V_A = 0V$		-1	1	μΑ		
C _{IN}	Logic Input Capacitance	f=1MHz	7			pF		
Dynamic Cl	naracteristics	·						
ton	Address/Enable Turn-On Time	trise/tfall<50ns		100	400	ns		
t _{OFF}	Address/Enable Turn-Off Time	trise/tfall<50ns		30	200	ns		
Q	Charge Injection	$C_L=1nF, V_S=0V, R_S=0\Omega$	TBD			рC		
O _{IS}	Off Isolation	$V_{EN}=0V$, $R_L=1k\Omega$, $f=100kHZ$	TBD			dB		
Power Supp	olies							
l+	Positive Supply Current	$V_{EN} = V_A = 0V \text{ or } 5V$	50		250	μA		
I-	Negative Supply Current	VEN = VA = UV OI SV	-0.01	-20		μA		

⁽¹⁾ Typical operating conditions: V+ = 5V, V- = -5V, TA = 25°C.

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⁽²⁾ Worst case operating conditions: $V+ = +5V \pm 10\%$, $V- = -5V \pm 10\%$, TA = -55 to $125^{\circ}C$.

HT506/HT507

ELECTRICAL CHARACTERISTICS (Single Supply)

Temperature range -55 to +225°C, typical @ +25°C, V+ = +10V, GND=V- = 0V, V_{IL}=0.8, V_{IH} = 2.4V, unless otherwise specified

Nama Nama	Symbol	Parameters	Test Conditions	Typical	Worst Case (2)		Units
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$				(1)	MIN	MAX	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	nalog Swit	ch			•		•
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	ANALOG	Analog Signal Range		11			V
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		Drain-Source On-Resistance	$V_D = 3V, 10V, I_S = 1mA$	80		400	Ω
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	r _{DS(ON)}	r _{DS(ON)} Matching between Channels	Sequence Each Switch On	2			%
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		Source Off Leakage Current	V _{EN} = 0V	0.01		200	nA
$ \begin{array}{ c c c c c } \hline Digital Control \\ \hline \hline Digital Control \\ \hline \hline \hline V_{IH} & Logic High Input Voltage \\ \hline V_{IL} & Logic Low Input Voltage \\ \hline \hline \hline \hline V_{IH} & Logic High Input Voltage \\ \hline \hline \hline V_{IL} & Logic Low Input Current \\ \hline $	D(OFF)	Drain Off Leakage Current	V _S =0.5V or 10V	0.04	-2500	2500	nA
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	D(ON)	Drain On Leakage Current		0.04	-2500	2500	nA
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	igital Cont	rol		•			·
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$					2.4		V
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	'IL	Logic Low Input Voltage		0.8			V
CIN Logic Input Capacitance f=1MHz 7 Dynamic Characteristics ton(EN) Address/Enable Turn-On Time trise/tfall<50ns		Logic High Input Current	$V_A = 2.4V, 10V$		-1	1	μΑ
CIN Logic Input Capacitance f=1MHz 7 Dynamic Characteristics ton(EN) Address/Enable Turn-On Time trise/tfall<50ns		Logic Low Input Current	$V_{EN} = 0V, 2.4V, V_A = 0V$		-1	1	μA
ton(EN) Address/Enable Turn-On Time trise/tfall<50ns 100 toFF(EN) Address/Enable Turn-Off Time 30 Q Charge Injection CL=1nF, Vs=6, Rs=0 TBD Power Supplies I+ Positive Supply Current 50		Logic Input Capacitance		7			pF
trise/tfall<50ns dof=F(EN) Address/Enable Turn-Off Time C Charge Injection CL=1nF, Vs=6, Rs=0 Power Supplies I+ Positive Supply Current 50	ynamic Ch	aracteristics					
$t_{OFF(EN)}$ Address/Enable Turn-Off Time 30 Q Charge Injection $C_L=1nF, V_S=6, R_S=0$ TBD Power Supplies L+ Positive Supply Current 50	DN(EN)	Address/Enable Turn-On Time	tria a /tfall .FOra		100	400	ns
Q Charge Injection CL=1nF, Vs=6, Rs=0 TBD Power Supplies I+ Positive Supply Current 50	OFF(EN)	Address/Enable Turn-Off Time	trise/trail<50ns		30	200	
Power Supplies I+ Positive Supply Current 50		Charge Injection	C _L =1nF, V _S =6, R _S =0	TBD			рС
I+ Positive Supply Current 1/ 01/07/51/ 50	ower Supp			•	•	•	•
	+	Positive Supply Current	\\ 0\\ == \\\ \\ \\ 0\\ == \\\\	50		250	μA
I- Negative Supply Current $V_{EN} = 0V \text{ or } 5V, V_A = 0V \text{ or } 5V$ $-0.01 -20$		Negative Supply Current	VEN = UV OF SV, VA = UV OF SV	-0.01	-20		μA

⁽¹⁾ Typical operating conditions: V+ = 10V, V- = GND = 0V, TA= 25°C.

TRUTH TABLE - HT506

А3	A2	A1	A0	EN	On Switch
X	X	Х	Х	0	None
0	0	0	0	1	1
0	0	0	1	1	2
0	0	1	0	1	3
0	0	1	1	1	4
0	1	0	0	1	5
0	1	0	1	1	6
0	1	1	0	1	7
0	1	1	1	1	8
1	0	0	0	1	9
1	0	0	1	1	10
1	0	1	0	1	11
1	0	1	1	1	12
1	1	0	0	1	13
1	1	0	1	1	14
1	1	1	0	1	15
1	1	1	1	1	16

TRUTH TABLE - HT507

A2	A1	Α0	EN	On Switch
Х	Х	Х	0	None
0	0	0	1	1
0	0	1	1	2
0	1	0	1	3
0	1	1	1	4
1	0	0	1	5
1	0	1	1	6
1	1	0	1	7
1	1	1	1	8

Logic "0" = VAL ≤ 0.8V Logic "1" = VAH ≥ 2.4V

X =Irrelevent

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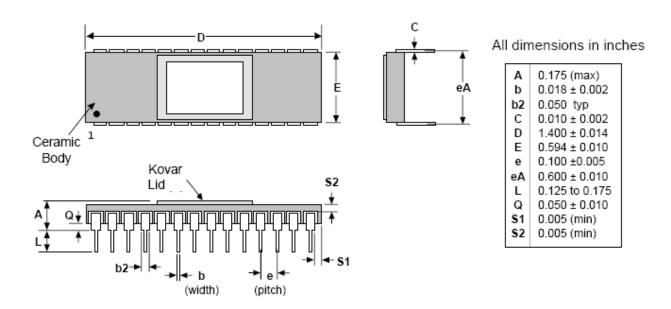
⁽²⁾ Worst case operating conditions: $V+ = +10V \pm 10\%$, V- = GND = 0V, TA = -55 to $125^{\circ}C$.

HT506/HT507

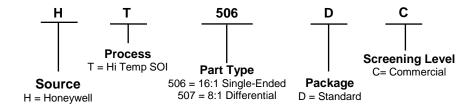
ABSOLUTE MAXIMUM RATINGS

Parameter	Value	Units
Voltages Referenced to V-, V+	+15	V
Digital Inputs VS, VD	-0.5 to VDD +0.5	V
Current (any terminal)	10	mA
Peak Current, S or D, (Pulsed at 1ms, 10% Duty Cycle Max)	15	mA
Storage Temperature	-65 to +325	°C
Power Dissipation (Package)	500	mW
ESD Protection	1000	V

28-LEAD PACKAGE



ORDERING INFORMATION



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For more information on Honeywell's High Temperature Electronics visit us online at www.honeywell.com/hightemp or contact us at 800-323-8295 or 763-954-2474. Customer Service Email: ps.customer.support@honeywell.com.

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