

## ZXMN2F34MA 20V N-channel enhancement mode MOSFET in DFN322

### **Summary**

V <sub>(BR)DSS</sub>	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)
20	0.060 @ V <sub>GS</sub> = 4.5V	8.5
	0.120 @ V <sub>GS</sub> = 2.5V	

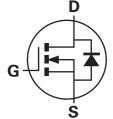


### **Description**

This new generation Trench MOSFET from Zetex features low onresistance achievable with low (2.5V) gate drive. The 2mm x 2mm DFN package provides superior thermal performance versus alternative leaded devices

### **Features**

- · Low on-resistance
- Superior thermal performance (versus to SOT23)
- · 2.5V gate drive capability
- · DFN 2x2 package

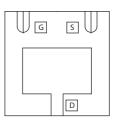


### **Applications**

- Buck/Boost DC-DC Converters
- Motor Control
- LED Lighting

### **Ordering information**

DEVICE	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN2F34MATA	7	8	3,000



### **Device marking**

1M4

## **ZXMN2F34MA**

### **Absolute maximum ratings**

Parameter	Symbol	Limit	Unit
Drain source voltage	$V_{DSS}$	20	V
Gate source voltage	$V_{GS}$	±12	V
$ \begin{array}{c} \text{Continous Drain Current @ $V_{GS}$=$4.5; $T_{A}$=$25°C$^{(b)}$} \\ \text{@ $V_{GS}$=$4.5; $T_{A}$=$70°C$^{(b)}$} \\ \text{@ $V_{GS}$=$4.5; $T_{A}$=$25°C$^{(a)}$} \\ \text{@ $V_{GS}$=$4.5; $T_{A}$=$25°C$^{(d)}$} \end{array} $	I <sub>D</sub>	5.1 4.1 4.0 8.5	A A A
Pulsed drain current <sup>(c)</sup>	I <sub>DM</sub>	19	А
Continuous source current (body diode)(b)	I <sub>S</sub>	3.1	Α
Pulsed source current (body diode)(c)	I <sub>SM</sub>	19	Α
Power dissipation at T <sub>A</sub> =25°C <sup>(a)</sup> Linear derating factor	P <sub>D</sub>	1.35 10.8	W mW/°C
Power dissipation at T <sub>A</sub> =25°C <sup>(b)</sup> Linear derating factor	P <sub>D</sub>	2.2 17.8	W mW/°C
Power dissipation at T <sub>A</sub> =25°C <sup>(d)</sup> Linear derating factor	P <sub>D</sub>	6.6 52.9	W mW/°C
Operating and storage temperature range	T <sub>j</sub> , T <sub>stg</sub>	-55 to 150	°C

### Thermal resistance

Parameter	Symbol	Limit	Unit
Junction to ambient <sup>(a)</sup>	$R_{\Theta JA}$	92.5	°C/W
Junction to ambient <sup>(b)</sup>	$R_{\Theta JA}$	56	°C/W
Junction to lead <sup>(d)</sup>	$R_{ ext{ hetaJL}}$	18.9	°C/W

### NOTES:

<sup>(</sup>a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions

<sup>(</sup>b) For a device surface mounted on FR4 PCB measured at t  $\! \leq \! 5$  sec.

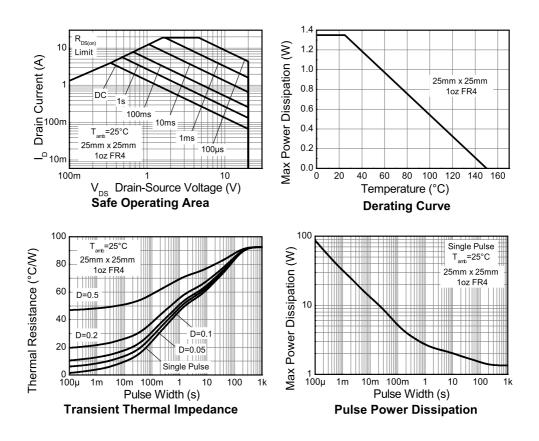
<sup>(</sup>c) Repetitive rating -  $25mm \times 25mm \text{ FR4 PCB}$ , D=0.02, pulse width  $300\mu\text{s}$  - pulse width limited by maximum junction temperature.

<sup>(</sup>d) Thermal resistance from junction to solder-point (at end of drain lead).

## **ZXMN2F34MA**

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### Thermal characteristics



## **ZXMN2F34MA**

## Electrical characteristics (at T<sub>amb</sub> = 25°C unless otherwise stated)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Static							
Drain-Source breakdown voltage	V <sub>(BR)DSS</sub>	20			V	$I_{D}$ = 250 $\mu$ A, $V_{GS}$ =0V	
Zero gate voltage drain current	I <sub>DSS</sub>			1	μΑ	V <sub>DS</sub> = 20V, V <sub>GS</sub> =0V	
Gate-Body leakage	I <sub>GSS</sub>			100	nA	$V_{GS}=\pm 12V$ , $V_{DS}=0V$	
Gate-Source threshold voltage	V <sub>GS(th)</sub>	0.5	0.8	1.5	V	$I_D=250\mu A,V_{DS}=V_{GS}$	
Static Drain-Source on-state resistance (*)	R <sub>DS(on)</sub>			0.060 0.120	Ω Ω	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 2.5A V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 1.0A	
Forward transconductance <sup>(*)(†)</sup>	9 <sub>fs</sub>		7.5		S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 2.5A	
Dynamic <sup>(†)</sup>							
Input capacitance	C <sub>iss</sub>		277		рF	V <sub>DS</sub> = 10V, V <sub>GS</sub> =0V f=1MHz	
Output capacitance	C <sub>oss</sub>		65		pF		
Reverse transfer capacitance	C <sub>rss</sub>		35		pF		
Switching (‡)(†)							
Turn-on-delay time	t <sub>d(on)</sub>		2.65		ns		
Rise time	t <sub>r</sub>		4.2		ns	V <sub>DD</sub> = 10V, V <sub>GS</sub> = 4.5V I <sub>D</sub> = 1A	
Turn-off delay time	t <sub>d(off)</sub>		9.9		ns	$R_{\rm G} \approx 6.0\Omega$	
Fall time	t <sub>f</sub>		5.1		ns		
Total gate charge	$Q_g$		2.8		nC	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 4.5V	
Gate-Source charge	Q <sub>gs</sub>		0.61		nC	I <sub>D</sub> = 2.5A	
Gate Drain charge	O <sub>gd</sub>		0.63		nC		
Source-drain diode							
Diode forward voltage <sup>(*)</sup>	$V_{SD}$		0.73	1.2	V	I <sub>S</sub> = 1.25A, V <sub>GS</sub> =0V	
Reverse recovery time <sup>(†)</sup>	t <sub>rr</sub>		6.5		ns	T <sub>j</sub> =25°C, I <sub>F</sub> =1.65A	
Reverse recovery charge <sup>(†)</sup>	Q <sub>rr</sub>		1.4		nC	di/dt=100A/μs	

### NOTES:

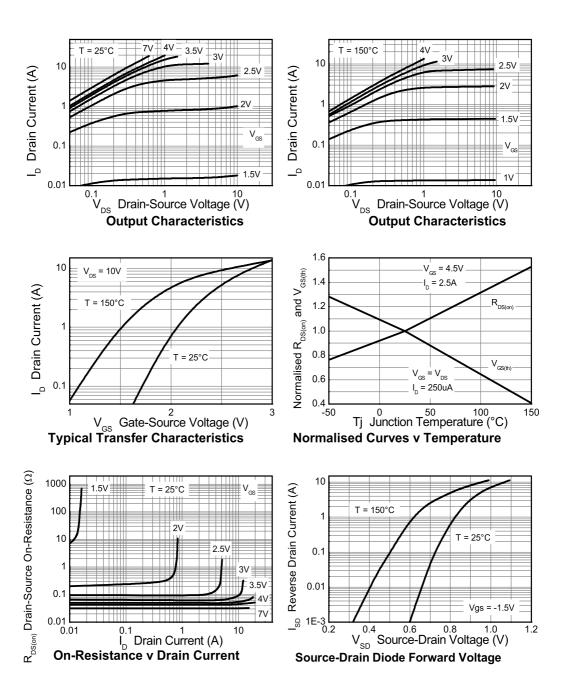
<sup>(\*)</sup> Measured under pulsed conditions. Pulse width  $\leq 300 \mu s$ ; duty cycle  $\leq 2\%$ .

<sup>(†)</sup> For design aid only, not subject to production testing.

<sup>(‡)</sup> Switching characteristics are independent of operating junction temperature.

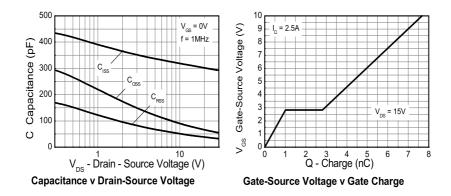
### **ZXMN2F34MA**

### **Typical characteristics**

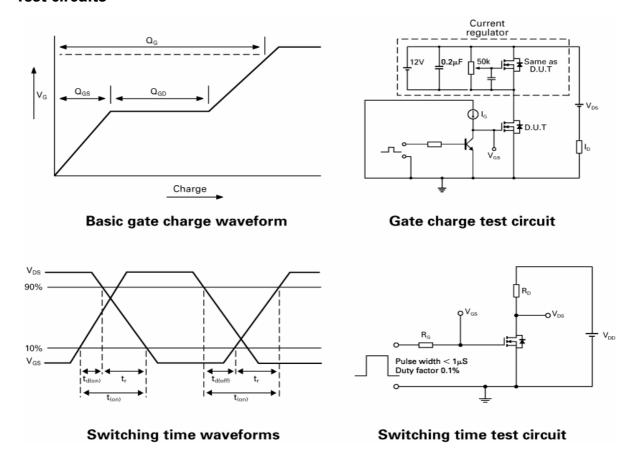


## **ZXMN2F34MA**

### **Typical characteristics**

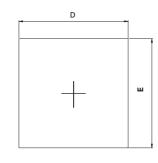


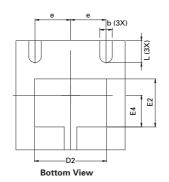
### **Test circuits**

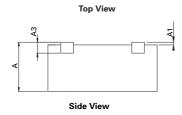


## **ZXMN2F34MA**

### Package outline - DFN322







DIM	Millim	neters	Inc	hes	DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Мах.	Min.	Max.
Α	0.80	1.00	0.0315	0.0393	D2	1.22	1.42	0.0480	0.0559
A1		0.05		0.002	е	0.65 BSC.		0.02559 BSC	
A3	0.153	0.253	0.0060	0.0099	Е	1.900	2.100	0.0748	0.0826
b	0.180	0.300	0.0071	0.0118	E2	0.780	0.990	0.0307	0.0389
D	1.900	2.100	0.0748	0.0826	E4	0.480	0.680	0.0189	0.0267
					L	0.300	0.500	0.0118	0.0196

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

### ZXMN2F34MA

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