

# **SBL1030CT - SBL1060CT**

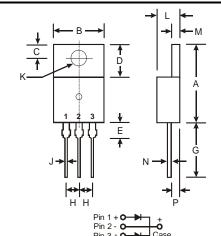
**10A SCHOTTKY BARRIER RECTIFIER** 

### **Features**

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- Lead Free Finish, RoHS Compliant (Note 3)

#### **Mechanical Data**

- Case: TO-220AB
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Polarity: As Marked on Body
- Terminals: Finish Bright Tin. Solderable per MIL-STD-202, Method 208
- Marking: Type Number
- Weight: 2.24 grams (approximate)



TO-220AB						
Dim	Min	Max				
Α	14.48	15.75				
В	10.00	10.40				
С	2.54	3.43				
D	5.90	6.40				
Е	2.80	3.93				
G	12.70	14.27				
Н	2.40	2.70				
J	0.69	0.93				
K	3.54	3.78				
L	4.07	4.82				
M	1.15	1.39				
N	0.30	0.50				
Р	2.04	2.79				
All Dimensions in mm						

# Maximum Ratings and Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

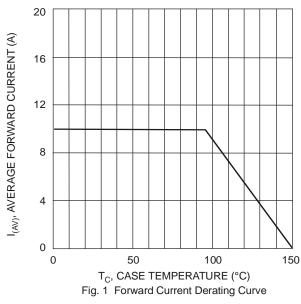
Characteristic		Symbol	SBL 1030CT	SBL 1035CT	SBL 1040CT	SBL 1045CT	SBL 1050CT	SBL 1060CT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	30	35	40	45	50	60	٧
RMS Reverse Voltage		V <sub>R(RMS)</sub>	21	24.5	28	31.5	35	42	V
Average Rectified Output Current	@ T <sub>C</sub> = 95°C (Note 1)	lo	10				Α		
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	175				Α		
Forward Voltage Drop @	$I_F = 5.0A, T_C = 25^{\circ}C$	$V_{FM}$	0.55 0.70		70	V			
Peak Reverse Current at Rated DC Blocking Voltage	@ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 125°C	I <sub>RM</sub>	0.5 50			mA			
Typical Junction Capacitance	(Note 2)	C <sub>i</sub>	450			pF			
Typical Thermal Resistance Junction to Case	(Note 1)	R <sub>θ</sub> JC			5	.5			°C/W
Operating and Storage Temperature Range		T <sub>i,</sub> T <sub>STG</sub>	-65 to +150					°C	

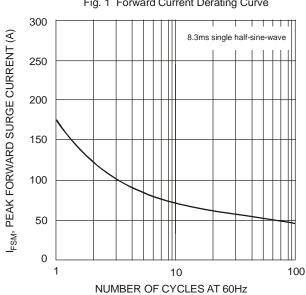
Notes:

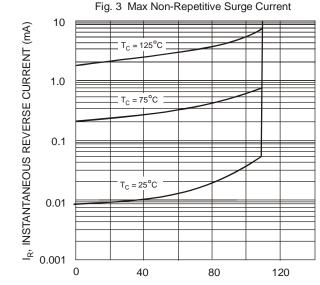
- 1. Thermal resistance junction to case mounted on heatsink.
- 2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
- 3. RoHS revision 13.2.2003. Glass and high temperature solder exemptions applied, see EU Directive Annex Notes 5 and 7.

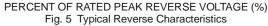


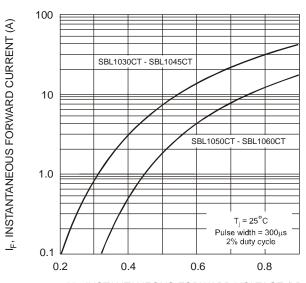
#### **DISCONTINUED**

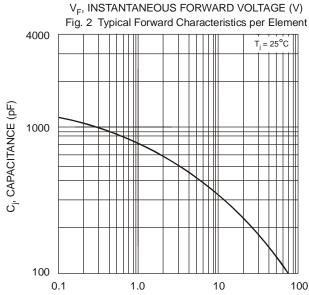












 $\rm V_R$ , REVERSE VOLTAGE (V) Fig. 4 Typical Junction Capacitance per Element



## Ordering Information (Note 4)

Device	Packaging	Shipping
SBL10xxCT*	TO-220AB	50/Tube

<sup>\*</sup> xx = Device type, e.g. SBL1045CT

Notes: 4. For packaging details, visit our website at http://www.diodes.com/datasheets/ap02008.pdf.

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