

## MBRB1530CT - MBRB1545CT

## 15A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

## **Features and Benefits**

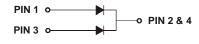
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- Surge Overload Rating to 150A Peak
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- Lead Free Finish, RoHS Compliant (Note 1)

### **Mechanical Data**

- Case: D<sup>2</sup>PAK
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Tin. Solderable per MIL-STD-202, Method 208 <sup>(C)</sup>
- Polarity: See Diagram
- Weight: 1.7 grams (approximate)







Polarity

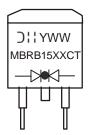
## Ordering Information (Note 2)

Device	Packaging	Shipping
MBRB1530CT-T	D <sup>2</sup> PAK	800/Tape & Reel, 13-inch
MBRB1535CT-T	D <sup>2</sup> PAK	800/Tape & Reel, 13-inch
MBRB1540CT-T	D <sup>2</sup> PAK	800/Tape & Reel, 13-inch
MBRB1545CT-T	D <sup>2</sup> PAK	800/Tape & Reel, 13-inch

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes

2. For packaging details, visit our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Marking Information**



MBRB15XXCT = Product Type Marking Code Where xx = 30, 35, 40 or 45, Depending on Device Type CH = Manufacturers' Code Marking YWW = Date Code Marking Y = Last Digit of Year (ex: 2 for 2002) WW = Week Code (01 to 53)



# **Maximum Ratings** $@T_A = 25^{\circ}C$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.						
Characteristic	Symbol	MBRB 1530CT	MBRB 1535CT	MBRB 1540CT	MBRB 1545CT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	30	35	40	45	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	24.5	28	31.5	V
Average Rectified Output Current @ T <sub>C</sub> = 105°C	lo		1	5		A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>		1	50		А

# Thermal Characteristics

Characteristic		Symbol	Value	Unit
Typical Thermal Resistance Junction to Terminal		$R_{\theta JT}$	3.0	°C/W
	V <sub>R</sub> ≤ 80% V <sub>RRM</sub>		-65 to +150	
Operating Temperature Range (Note 3)	V <sub>R</sub> ≤ 50% V <sub>RRM</sub>	TJ	≤180	°C
	DC Forward Mode		≤200	
Storage Temperature Range		T <sub>STG</sub>	-65 to +175	°C

## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit	
Forward Voltage, per Element	@ I <sub>F</sub> = 7.5A	V <sub>FM</sub>	0.7	V	
Voltage Rate of Change		dv/dt	10,000	V/µs	
Peak Reverse Current at Rated DC Blocking Voltage (Note 4)	@ T <sub>A</sub> = 25°C @ T <sub>A</sub> = 100°C	I <sub>RM</sub>	0.1 15	mA	
Maximum Reverse Recovery Time (Note	e 5)	t <sub>rr</sub>	30	ns	
Typical Total Capacitance (Note 6)		CT	250	pF	

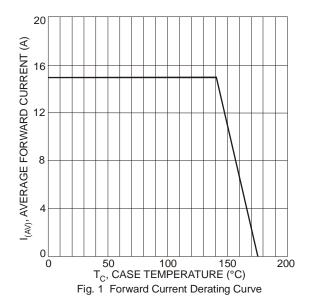
Notes:

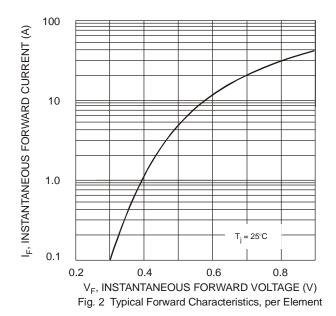
3. The heat generated must be less than the thermal conductivity from Junction-to-Ambient:  $dP_D/dT_J < 1/R_{BJA}$ 

4. 300µs pulse width, 2% duty cycle.

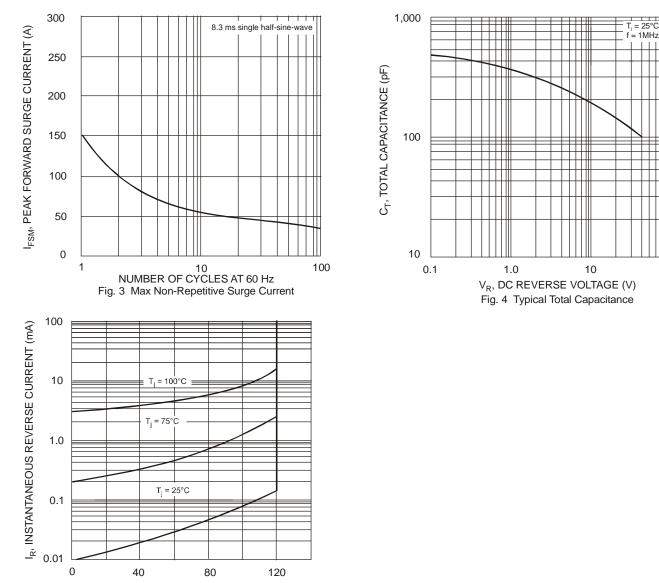
5. Reverse recovery test conditions: IF = 0.5A, IR = 1.0A, Irr = 0.25A (see figure 1).

6. Measured at 1.0 MHz and applied reverse voltage of 4.0V DC.



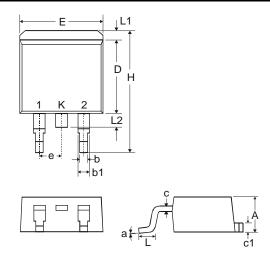






PERCENT OF RATED PEAK REVERSE VOLTAGE (%) Fig. 5 Typical Reverse Characteristics, per element

# Package Outline Dimensions



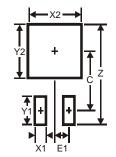
D <sup>2</sup> PAK				
Dim	Min	Max		
Α	4.07	4.82		
b	0.51	0.99		
b1	1.15	1.77		
C	0.356	0.58		
c1	1.143	1.65		
D	8.39	9.65		
ш	9.66	10.66		
e	2.54	Тур		
Н	14.61	15.87		
L	1.78	2.79		
L1		1.67		
L2		1.77		
а	0°	8°		
All Dimensions in mm				

MBRB1530CT - MBRB1545CT

100



## Suggested Pad Layout



Dimensions	Value (in mm)
Z	16.9
X1	1.1
X2	10.8
Y1	3.5
Y2	11.4
С	9.5
E1	2.5

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