

NOT RECOMMENDED FOR NEW DESIGN USE DMP3130L



DMP3120L

P-CHANNEL ENHANCEMENT MODE MOSFET

Features

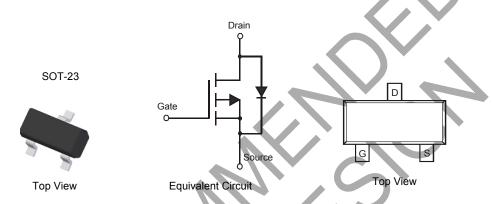
Low On-Resistance:

 $R_{DS(ON)} < 120m\Omega$ @ $V_{GS} = -4.5V$ $R_{DS(ON)} < 240m\Omega$ @ $V_{GS} = -2.5V$

- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approximate)



Ordering Information (Note 4)

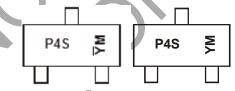
		4	100	 7	
Part Number	ľ		Case		Packaging
DMP3120L-7			SOT-23		3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- <1000ppm antimony compounds.

 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information



Chengdu A/T Site Shanghai A/T Site

P4S = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site)

YM = Date Code Marking for CAT (Chengdu Assembly/ Test site)

Y or \overline{Y} = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key

Year	2008		2009	2010		2011	2012		2013	2014		2015
Code	V		W	Х		Υ	Z		Α	В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	2	1	5	6	7	Ω	٥	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain Source Voltage		V_{DSS}	-30	V
Gate-Source Voltage		V_{GSS}	±12	V
Drain Current (Note 5)	T _A =+25°C T _A = +70°C	I _D	-2.8 -2.2	А
Drain Current (Note 5)	Pulsed	I _{DM}	-9	А
Body-Diode Continuous Current (Note 5)		I _S	-2.0	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	1.4	W
Thermal Resistance, Junction to Ambient @T _A = 25°C (Note 5)	$R_{ hetaJA}$	90	°C/W
Operating and Storage Temperature Range	T _{J.} T _{STG}	-55 to +150	°C

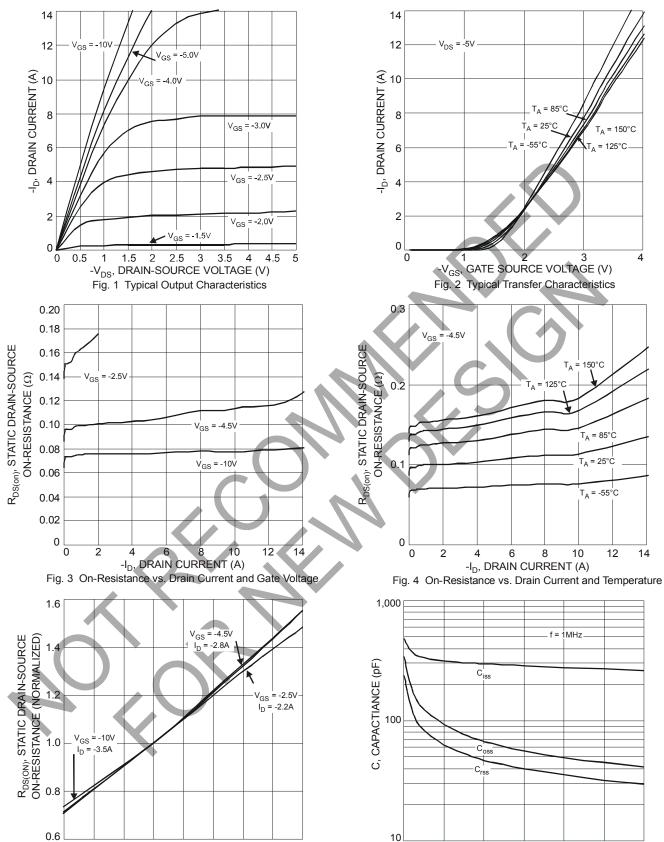
Notes: 5. Device mounted on FR-4 PCB. $t \le 5$ sec.

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30			V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I_{DSS}			-1	μA	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Body Leakage	I _{GSS}			±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(th)}	-0.6	-/	-1.4	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	//	7	-	120	mΩ	$V_{GS} = -4.5V$, $I_D = -2.8A$	
Static Dialii-Source Oil-Resistance	R _{DS (ON)}	_	\	240	11122	$V_{GS} = -2.5V$, $I_D = -1.8A$	
Forward Transconductance	g _{fs}		5	V	S	$V_{DS} = -5V$, $I_D = -2.8A$	
Source-Drain Diode Forward Voltage	V _{SD}	1		-1.1	V	$V_{GS} = 0V$, $I_S = -2.0A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C _{iss}		285	_	pF	V 45V V 0V	
Output Capacitance	Coss	_	56	_	pF	V _{DS} = -15V, V _{GS} = 0V f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	<i>/</i> – ,	40	_	pF	1 = 1.0IVIH2	
Gate Resistance	R_G		13	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$ f = 1.0MHz	
SWITCHING CHARACTERISTICS (Note 7)							
Turn-On Delay Time	t _{d(on)}		5.6	_			
Rise Time	tr		6.8	_	ns	$V_{DS} = -15V$, $V_{GS} = -4.5V$,	
Turn-Off Delay Time	t _{d(off)}	_	35.3	_	IIS	$I_D = -1A, R_G = 6.0\Omega$	
Fall Time	t _f	_	19.2	_			
Total Gate Charge	Q_{G}	_	6.7 3.0	_	200	$V_{DS} = -15V$, $V_{GS} = -10V$, $I_{D} = -1.0A$	
Gate-Source Charge	Q_GS	_	8.0	_	nC	$V_{DS} = -15V$, $V_{GS} = -4.5V$, $I_{D} = -1.0A$	
Gate-Drain Charge	Q_{GD}	_	0.5	_			

 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing. Notes:





0

15

Fig. 6 Typical Capacitance

-V_{DS}, DRAIN-SOURCE VOLTAGE (V)

25

30

50

T_A, AMBIENT TEMPERATURE (C)

Fig. 5 Normalized Static Drain-Source On-Resistance

vs. Ambient Temperature

75

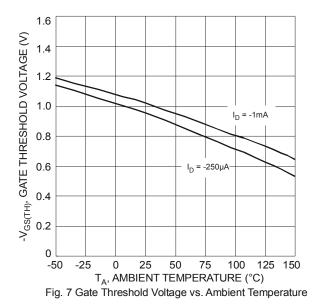
25

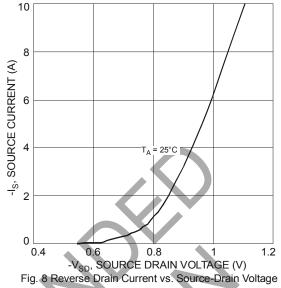
-50 -25

100

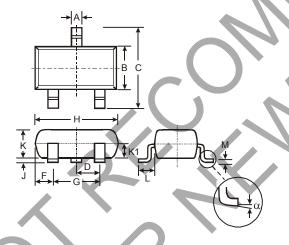
125 150





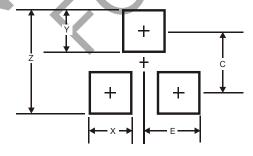


Package Outline Dimensions



SOT-23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
7	0.013	0.10	0.05				
K	0.903	1.10	1.00				
K1	-	-	0.400				
L	0.45	0.61	0.55				
М	0.085	0.18	0.11				
α	0°	8°	-				
All Dimensions in mm							

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
С	2.0
E	1.35



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DMP3120L

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