

#### N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(on) max</sub>	I <sub>D</sub> T <sub>C</sub> = +25°C	
700V	$1.3\Omega$ @ $V_{GS} = 10V$	4.6A	

#### **Features and Benefits**

- Low On-Resistance
- High BVDss rating for power application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(on)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Motor Control
- Backlighting
- **AC-DC Converters**

### **Mechanical Data**

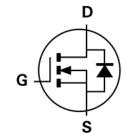
- Case: TO251
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208@3
- Weight: 0.33 grams (Approximate)

(uIPAK)









TO251 Top View

TO251 **Bottom View** 

Top View Pin Configuration

Internal Schematic

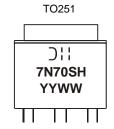
### Ordering Information (Note 4)

Part Number	Case	Packaging
DMJ70H1D3SH3	TO251	75pieces / tube

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

## **Marking Information**



☐ ☐ Manufacturer's Marking 7N70SH = Product Type Marking Code YYWW = Date Code Marking YY or YY= Last Digit of Year (ex: 15 = 2015) WW or WW = Week Code (01 to 53)



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage		$V_{DSS}$	700	V
Gate-Source Voltage		$V_{GSS}$	±30	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	$T_C = +25$ °C $T_C = +100$ °C	I <sub>D</sub>	4.6 2.9	А
Maximum Body Diode Forward Current (Note 6)		I <sub>S</sub>	3.0	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)		I <sub>DM</sub>	5.4	Α
Avalanche Current (Note 7)	L = 60mH	I <sub>AS</sub>	1.1	Α
Avalanche Energy (Note 7)	L = 60mH	E <sub>AS</sub>	40	mJ
Peak Diode Recovery dv/dt (Note 7)	•	dv/dt	5	V/ns

# **Thermal Characteristics** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	T <sub>C</sub> = +25°C	D-	41	- W
Total Power Dissipation (Note 5)	T <sub>C</sub> = +100°C	$P_{D}$	16	
Thermal Resistance, Junction to Ambient (Note 6)		R <sub>0JA</sub>	79	°C/W
Thermal Resistance, Junction to Case (Note 5)	R <sub>0JC</sub>	3.0	C/VV	
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

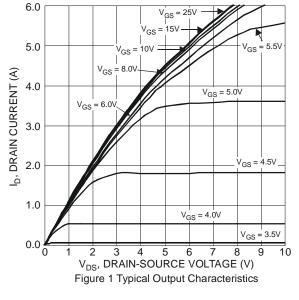
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

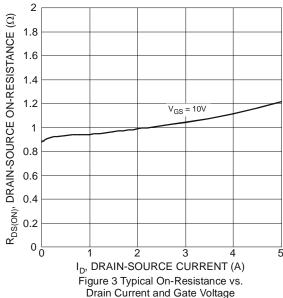
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	700	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μA	V <sub>DS</sub> = 700V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	$V_{GS(th)}$	2	2.9	4	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	1.0	1.3	Ω	$V_{GS} = 10V, I_D = 2.5A$	
Diode Forward Voltage	V <sub>SD</sub>	_	0.9	1.3	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 5A	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C <sub>iss</sub>		351	_		V <sub>DS</sub> = 50V, f = 1MHz, V <sub>GS</sub> = 0V	
Output Capacitance	Coss	_	66		pF		
Reverse Transfer Capacitance	Crss	_	1.1	_		VGS = UV	
Gate Resistance	$R_G$	_	3.5	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	Qg	_	13.9	_		V 500V I 50	
Gate-Source Charge	Qgs	_	1.9	_	nC	$V_{DD} = 560V, I_D = 5A,$	
Gate-Drain Charge	Q <sub>gd</sub>	_	8.5	_		$V_{GS} = 10V$	
Turn-On Delay Time	t <sub>D(on)</sub>	_	8.5	_		$V_{DD} = 350V, V_{GS} = 10V,$ $R_G = 4.7\Omega, I_D = 2.5A$	
Turn-On Rise Time	t <sub>r</sub>	_	11.6	_	ns		
Turn-Off Delay Time	t <sub>D(off)</sub>	_	24.5	_	115		
Turn-Off Fall Time	t <sub>f</sub>	_	10	_		1	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	_	212	_	ns		
Body Diode Reverse Recovery Time (T <sub>J</sub> = +150°C)	t <sub>rr</sub>	_	251	_	ns		
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	_	1.8	_	μC	I <sub>S</sub> = 5A, dl/dt = 100A/µs	
Body Diode Reverse Recovery Charge (T <sub>J</sub> = +150°C)	Q <sub>rr</sub>	_	2.3	_	μC		

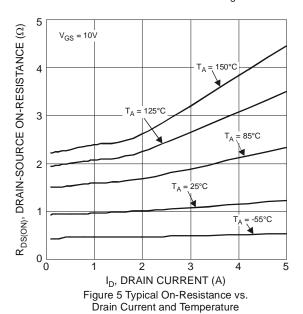
Notes:

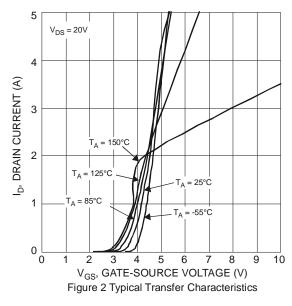
- 5. Device mounted on infinite heatsink.6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.7. Guaranteed by design. Not subject to production testing.
- 8. Short duration pulse test used to minimize self-heating effect.

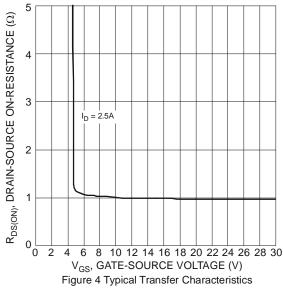












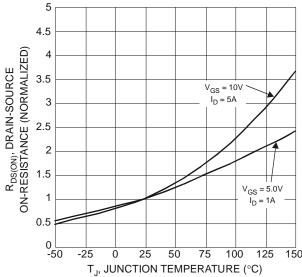


Figure 6 On-Resistance Variation with Temperature



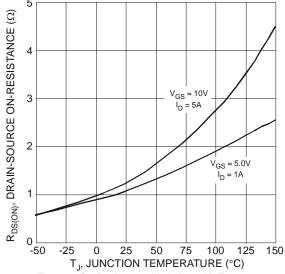
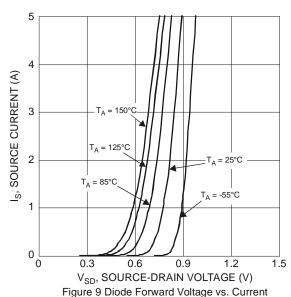


Figure 7 On-Resistance Variation with Temperature



10 V<sub>GS</sub> GATE THRESHOLD VOLTAGE (V) 8  $V_{DS} = 560V$ 2 0, 6 9 12 15 Q<sub>g</sub>, TOTAL GATE CHARGE (nC) Figure 11 Gate Charge

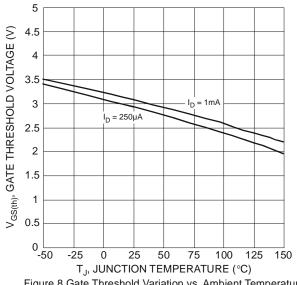
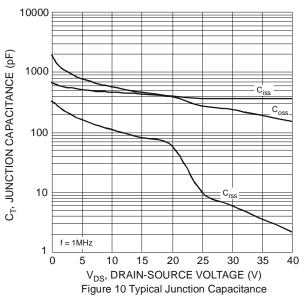
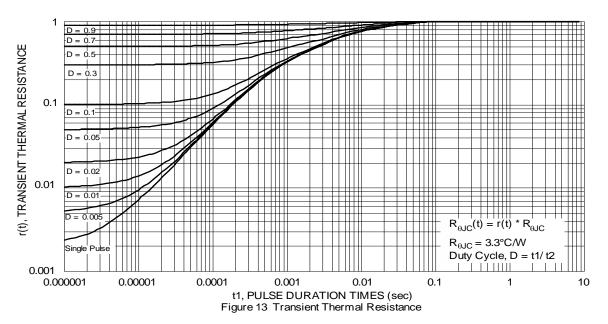


Figure 8 Gate Threshold Variation vs. Ambient Temperature



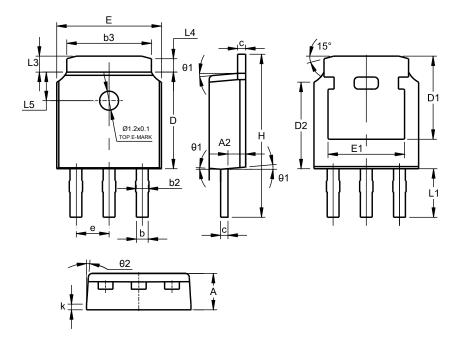
10 -I<sub>D</sub>, DRAIN CURRENT (A) 0.1  $T_{J(max)} = 150$ °C  $T_A = 25^{\circ}C$ V<sub>GS</sub> = 10V Single Pulse DUT on infinite heatsink 0.01 10 1000 100  ${
m -V_{DS}}$ , DRAIN-SOURCE VOLTAGE (V) Figure 12 SOA, Safe Operation Area





# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



TO251						
(Type TH3)						
Dim	Min	Max	Тур			
Α	2.20	2.40	2.30			
A2	0.97	1.17	1.07			
b	0.68	0.90	0.78			
b2	0.76	0.95	0.84			
b3	5.20	5.50	5.33			
С	0.43	0.63	0.53			
D	5.98	6.22	6.10			
D1	5	.30 RE	F			
D2	5.26	5.66	5.46			
е	2.	286 BS	C			
Е	6.40	6.80	6.60			
E1	4.63	5.03	4.83			
Н	9.40	9.85	9.62			
k		0.40REF				
L1	2.30	2.70	2.50			
L3	0.88	1.28	1.02			
L4	0.75 REF					
L5	1.65	1.95	1.80			
θ1	5°	9°	7°			
θ2	5°	9°	7°			
All Dimensions in mm						



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