

Not Recommended for New Design Alternative is BCP51 & BCP5116



PNP SURFACE MOUNT TRANSISTOR

DCP51/-16

Features

- **Epitaxial Planar Die Construction**
- Complementary NPN Type Available (DCP54)
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

Mechanical Data

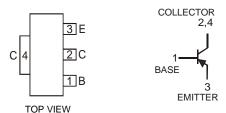
Case: SOT-223 •

- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking & Type Code Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.115 grams (approximate)

Maximum Ratings @T_A = 25°C unless otherwise specified



SOT-223



Schematic and Pin Configuration

| Characteristic | Symbol | Value | Unit |
|------------------------------|------------------|-------|------|
| Collector-Base Voltage | V _{CBO} | -45 | V |
| Collector-Emitter Voltage | V _{CEO} | -45 | V |
| Emitter-Base Voltage | V _{EBO} | -5 | V |
| Peak Pulse Current | I _{CM} | -1.5 | A |
| Continuous Collector Current | lc | -1 | A |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|---|----------------------------------|-------------|------|
| Power Dissipation @ $T_A = 25^{\circ}C$ (Note 3) | Pd | 1 (Note 3) | W |
| Operating and Storage Temperature Range | T _{j,} T _{STG} | -55 to +150 | °C |
| Thermal Resistance Junction to Ambient Air @ $T_A = 25^{\circ}C$ (Note 3) | $R_{	hetaJA}$ | 125 | °C/W |

Electrical Characteristics @T_A = 25°C unless otherwise specified

| Characteristic | Symbol | Min | Тур | Max | Unit | Conditions |
|--------------------------------------|----------------------|-----|-----|------|------|--|
| OFF CHARACTERISTICS (Note 4) | | | | | | |
| Collector-Base Breakdown Voltage | V _{(BR)CBO} | -45 | _ | _ | V | $I_{C} = -100 \mu A, I_{E} = 0A$ |
| Collector-Emitter Breakdown Voltage | V _{(BR)CEO} | -45 | _ | _ | V | $I_{\rm C} = -10 {\rm mA}, I_{\rm B} = 0 {\rm A}$ |
| Emitter-Base Breakdown Voltage | V _{(BR)EBO} | -5 | _ | _ | V | $I_{E} = -10 \mu A, I_{C} = 0 A$ |
| Collector Cut-Off Current | I _{CBO} | _ | _ | -100 | nA | $V_{CB} = -30V, I_E = 0A$ |
| | | | _ | -10 | μΑ | $V_{CB} = -30V, I_E = 0A, T_A = 150^{\circ}C$ |
| Emitter Cut-Off Current | I _{EBO} | | _ | -10 | μA | $V_{EB} = -5V, I_{C} = 0A$ |
| ON CHARACTERISTICS (Note 4) | | | | | | |
| Collector-Emitter Saturation Voltage | V _{CE(SAT)} | | _ | -0.5 | V | I _C = -500mA, I _B = -50mA |
| Base-Emitter Turn-On Voltage | V _{BE(ON)} | _ | _ | -1.0 | V | I _C = -500mA, V _{CE} = -2V |
| DC Current Gain | h _{FE} | 40 | | 250 | _ | I _C = -150mA, V _{CE} = -2V |
| | | 25 | _ | — | | I _C = -500mA, V _{CE} = -2V |
| DCP51-16 | | 100 | _ | 250 | | I _C = -150mA, V _{CE} = -2V |
| SMALL SIGNAL CHARACTERISTICS | | • | | | | |
| Transition Frequency | f _T | _ | 200 | _ | MHz | I _C = -50mA, V _{CE} = -5V, f = 100MHz |

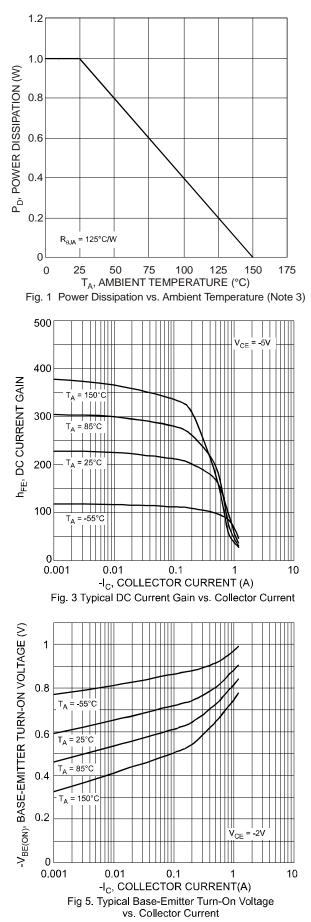
Note: 1. No purposefully added lead.

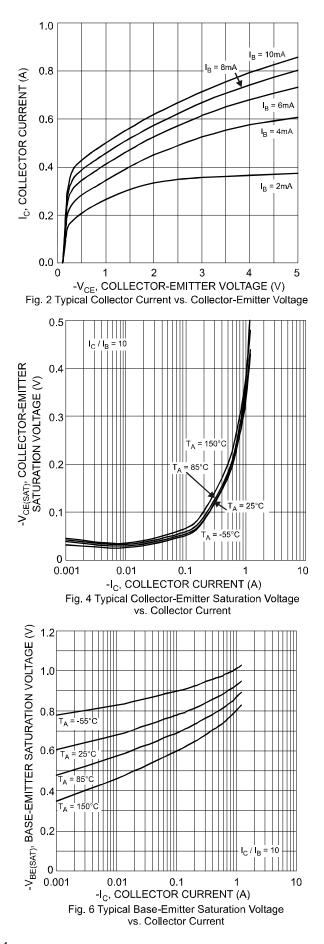
 Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
Device mounted on FR-4 PCB pad layout as shown on page 4 or on Diodes, Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

4. Measured under pulsed conditions. Pulse width = 300µs. Duty cycle ≤2%



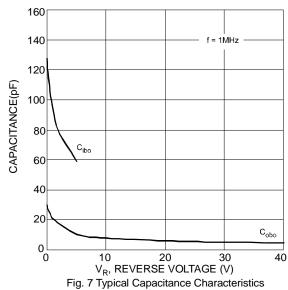
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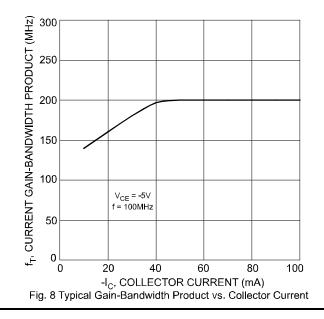






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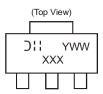


Ordering Information (Note 5)

| Part Number | Case | Packaging |
|-------------|---------|--------------------|
| DCP51-13 | SOT-223 | 2500 / Tape & Reel |
| DCP51-16-13 | SOT-223 | 2500 / Tape & Reel |

Notes: 5. For packaging details, please visit our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

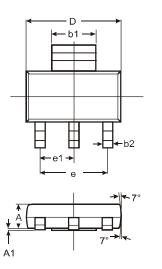


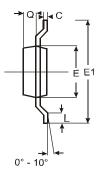
Dil = Manufacturer's code markingXXX = Product type marking codeEx:

P14 = DCP51 P14-16 = DCP51-16

 $\begin{array}{l} YWW = \text{Date code marking} \\ Y = \text{Last digit of year ex: } 7 = 2007 \\ WW = \text{Week code } 01 - 52 \end{array}$

Package Outline Dimensions

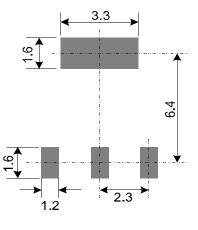




| SOT-223 | | | | | |
|----------------------|-------|------|------|--|--|
| Dim | Min | Max | Тур | | |
| Α | 1.55 | 1.65 | 1.60 | | |
| A1 | 0.010 | 0.15 | 0.05 | | |
| b1 | 2.90 | 3.10 | 3.00 | | |
| b2 | 0.60 | 0.80 | 0.70 | | |
| С | 0.20 | 0.30 | 0.25 | | |
| D | 6.45 | 6.55 | 6.50 | | |
| Е | 3.45 | 3.55 | 3.50 | | |
| E1 | 6.90 | 7.10 | 7.00 | | |
| е | _ | _ | 4.60 | | |
| e1 | _ | _ | 2.30 | | |
| L | 0.85 | 1.05 | 0.95 | | |
| Q | 0.84 | 0.94 | 0.89 | | |
| All Dimensions in mm | | | | | |



Suggested Pad Layout: (Based on IPC-SM-782)



(Unit:mm)

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