

Delphi Series IPM24S0B0, Non-Isolated, Integrated Point-of-Load Power Modules: 11V~36V input, 3.3~6.5V and 3A Output

The Delphi Series IPM24S0B0 non-isolated, fully integrated Point-of-Load (POL) power modules, are the latest offerings from a world leader in power systems technology and manufacturing --Delta Electronics, Inc. This product family provides up to 3A of output current or 20W of output power in an industry standard, compact, IC-like, molded package. It is highly integrated and does not require external components to provide the point-of-load function. A copper pad on the back of the module; in close contact with the internal heat dissipation components; provides excellent thermal performance. The assembly process of the modules is fully automated with no manual assembly involved. These converters possess outstanding electrical and thermal performance, as well as extremely high reliability under highly stressful operating conditions. IPM24S0B0 operates from an 11V~36V source and provides a programmable output voltage from 3.3V to 6.5V. The IPM product family is available in both a SMD or SIP package. IPM24S family is also available for output 1.2V~2.5V. Please refer to IPM240A0 datasheet for details.

DATASHEET IPM24S0B0S/R03_02262013

FEATURES

High efficiency: 91% @ 12Vin, 6.5V/3A

88% @ 24Vin, 6.5V/3A

- Small size and low profile:
 17.8x15.0x7.8mm (0.70"x0.59"x0.31")
- Output voltage adjustment: 3.3V~6.5V
- Monotonic startup into normal and pre-biased loads
- Input UVLO, output OCP
- Remote ON/OFF
- Output short circuit protection
- Fixed frequency operation
- Copper pad to provide excellent thermal performance
- ISO 9001, TL 9000, ISO 14001, QS9000, OHSAS18001 certified manufacturing facility
- UL/cUL 60950 (US & Canada) Recognized, and TUV (EN60950) Certified
- CE mark meets 73/23/EEC and 93/68/EEC directives

OPTIONS

SMD or SIP package

APPLICATIONS

- Telecom/DataCom
- Wireless Networks
- Optical Network Equipment
- Server and Data Storage
- Industrial/Test Equipment



TECHNICAL SPECIFICATIONS

 $T_A = 25$ °C, airflow rate = 300 LFM, $V_{in} = 24$ Vdc, nominal Vout unless otherwise noted.

PARAMETER	NOTES and CONDITIONS	IPM24S0	IPM24S0B0R/S03FA			
		Min.	Typ.	Max.	Units	
ABSOLUTE MAXIMUM RATINGS			71			
Input Voltage (Continuous)		0		40	Vdc	
Operating Temperature	Please refer to Fig.32 for the measuring point	-40		+125	°C	
Storage Temperature		-55		+125	°C	
INPUT CHARACTERISTICS						
Operating Input Voltage		11		36	V	
Input Under-Voltage Lockout						
Turn-On Voltage Threshold			10.31		V	
Turn-Off Voltage Threshold	\(\alpha \) \(\begin{array}{cccccccccccccccccccccccccccccccccccc		10.10	0.5	V	
Maximum Input Current	Vin=Vin,min to Vin,max, Io=Io,max		50	2.5	A	
No-Load Input Current Off Converter Input Current			50 3	10	mA	
Input Reflected-Ripple Current	P-P 0.5µH inductor, 5Hz to 20MHz		60	150	mA mAp-p	
Input Voltage Ripple Rejection	120 Hz		TBD	150	dB	
OUTPUT CHARACTERISTICS	120112		TOD		UD	
Output Voltage Set Point	Vin=24V, Io=Io,max, Ta=25°C	3.251	3.3	3.350	Vdc	
Output Voltage Adjustable Range	VIII—2 1 V, 10—10,111dA, 14—20	3.3	0.0	6.5	V	
Output Voltage Regulation		0.0		5.5	·	
Over Line	Vin=Vin,min to Vin,max		0.3		% Vo,set	
Over Load	lo=lo,min to lo,max		0.3		% Vo,set	
Over Temperature	Ta=Ta,min to Ta,max		0.01	0.025	%Vo,set/°(
Total Output Voltage Range	Over sample load, line and temperature	-3.0	0.01	+3.0	% Vo.set	
Output Voltage Ripple and Noise	5Hz to 20MHz bandwidth				,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
Peak-to-Peak	Full Load, 1µF ceramic, 220µF Poscap		50	100	mVp-p	
RMS	Full Load, 1µF ceramic, 220µF Poscap		25	50	mV	
Output Current Range	Vo>3.3Vdc	0		3	Α	
Output Voltage Over-shoot at Start-up	Vin=11V to 36V, Io=0A to 3A, Ta=25°C		0	1	% Vo,set	
Output DC Current-Limit Inception			200		% lo	
DYNAMIC CHARACTERISTICS						
Dynamic Load Response	220µF Poscap & 1µF Ceramic load cap, 0.5A/µs					
Positive Step Change in Output Current	50% lo, max to 100% lo, max		75	200	mVpk	
Negative Step Change in Output Current	100% lo, max to 50% lo, max		75	200	mVpk	
Setting Time to 10% of Peak Devitation	•		200	300	μs	
Turn-On Transient	lo=lo.max					
Start-Up Time, From On/Off Control			17	50	ms	
Start-Up Time, From Input	Ti () () () () () () () () () (_	17	50	ms	
Output Voltage Rise Time	Time for Vo to rise from 10% to 90% of Vo,set,	5	9	17	ms	
Maximum Output Startup Capacitive Load	Full load; ESR_≥25mΩ			220	μF	
EFFICIENCY	Full load; ESR_≧18mΩ			1220	μF	
Vo=3.3V	Vin-12V Io-Io may To-25°C	85.0	86.5		%	
Vo=4.0V	Vin=12V, lo=lo,max, Ta=25°C Vin=12V, lo=lo,max, Ta=25°C	86.5	88.0.		%	
Vo=4.0V Vo=5.0V	Vin=12V, io=io,max, Ta=25 € Vin=12V, io=lo,max, Ta=25 €	88.5	88.0.		%	
Vo=6.5V	Vin=12V, Io=Io,max, Ta=25℃ Vin=12V, Io=Io,max, Ta=25℃	90.0	91.0		%	
Vo=3.3V	Vin=24V, lo=lo,max, Ta=25℃	81.0	82.5		%	
Vo=4.0V	Vin=24V, Io=Io,max, Ta=25°C	83.0	84.5		%	
Vo=5.0V	Vin=24V, Io=Io,max, Ta=25°C	85.0	86.0		%	
Vo=6.5V	Vin=24V, Io=Io,max, Ta=25℃	87.0	88.0		%	
FEATURE CHARACTERISTICS	,, 200	55	23.0			
Switching Frequency			300		kHz	
ON/OFF Control, (Logic High-Module ON)						
Logic High	Module On	2.4		Vin,max	V	
Logic Low	Module Off	-0.2		0.8	V	
ON/OFF Current	Ion/off at Von/off=0		0.25	1	mA	
Leakage Current	Logic High, Von/off=5V			50	μA	
GENERAL SPECIFICATIONS						
Calculated MTBF	lo=80% lo,max, Ta=25°C		13.74		M hours	
Weight			6		grams	

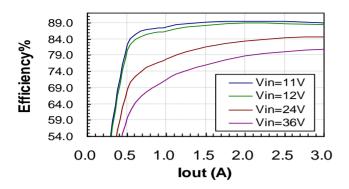


Figure 1: Converter efficiency vs. output current (3.30V output voltage)

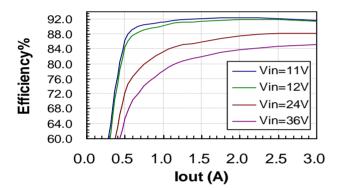


Figure 3: Converter efficiency vs. output current (5.0V output voltage)

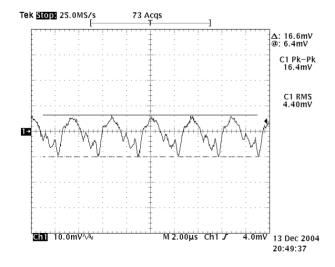


Figure 5: Output ripple & noise at 12Vin, 3.3V/3A out

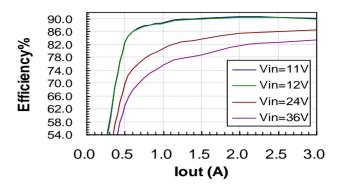


Figure 2: Converter efficiency vs. output current (4.0V output voltage)

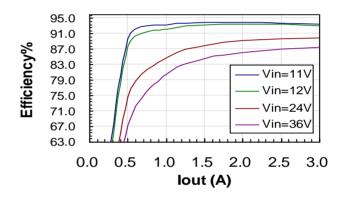


Figure 4: Converter efficiency vs. output current (6.5V output voltage)

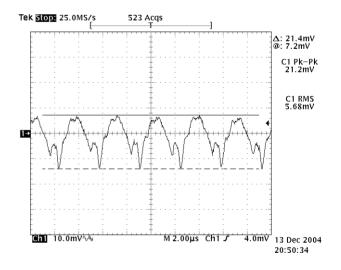


Figure 6: Output ripple & noise at 24Vin, 3.3V/3A out

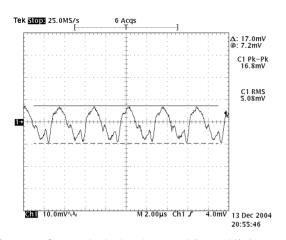


Figure 7: Output ripple & noise at 12Vin, 4.0V/3A out

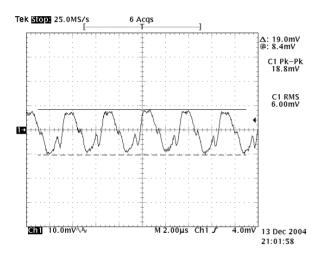


Figure 9: Output ripple & noise at 12Vin, 5.0V/3A out

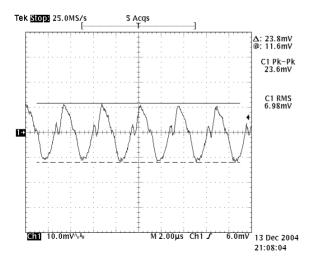


Figure 11: Output ripple & noise at 12Vin, 6.5V/3A out

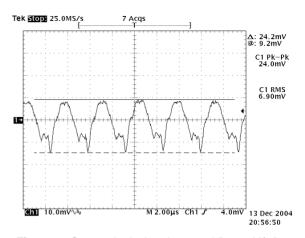


Figure 8: Output ripple & noise at 24Vin, 4.0V/3A out

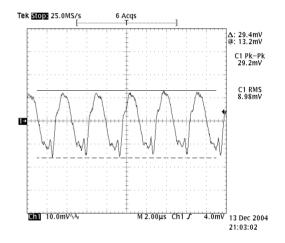


Figure 10: Output ripple & noise at 24Vin, 5.0V/3A out

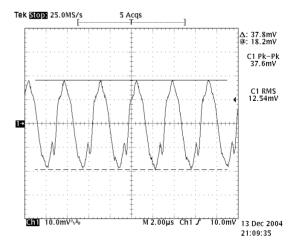


Figure 12: Output ripple & noise at 24Vin, 6.5V/3A out

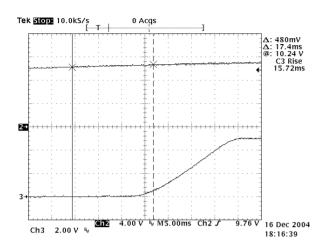


Figure 13: Power on waveform at 12vin, 3.3V/3A out with application of Vin

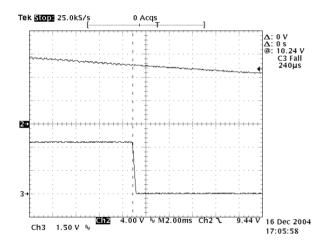


Figure 15: Power off waveform at 12vin, 3.3V/3A out with application of Vin

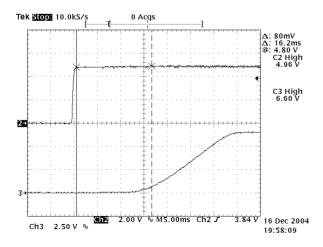


Figure 17: Remote turn on delay time at 24vin, 6.5V/3A out

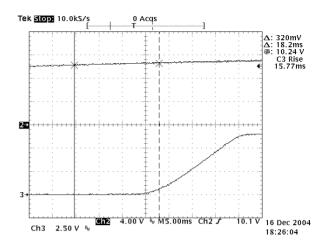


Figure 14: Power on waveform at 12vin, 6.5V/3A out with application of Vin

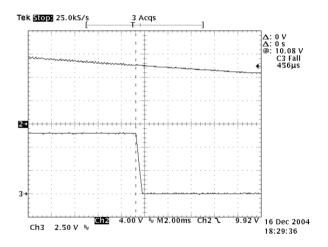


Figure 16: Power off waveform 12vin,6.5V/3A out with application of Vin

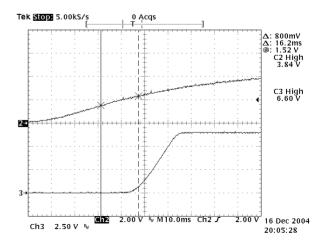


Figure 18: Remote turn on delay time at 24vin, 6.5V/3A out

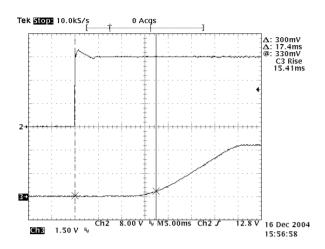


Figure 19: Turn on delay at 24vin, 3.3V/3A out with application of Vin

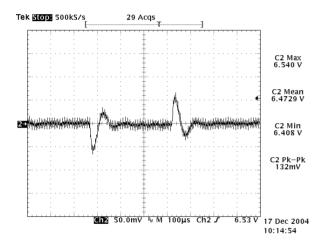


Figure 21: Typical transient response to step load change at 0.5A/µS from 100% to 50% of lo, max at 12Vin, 6.5V out (measurement with a 1uF ceramic

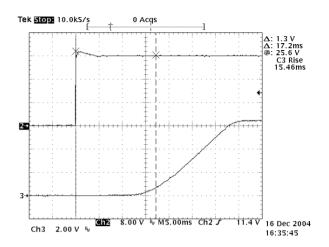


Figure 20: Turn on delay at 24vin, 6.5V/3A out with application of Vin

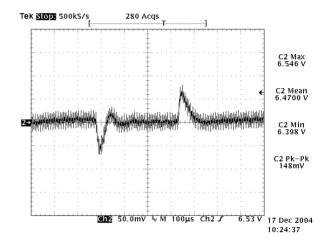
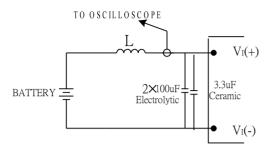


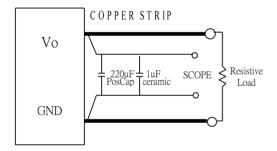
Figure 22: Typical transient response to step load change at 0.5A/µS from 50% to 100% of lo, max at 24Vin, 6.5V out (measurement with a 1uF ceramic)

TEST CONFIGURATIONS



Note: Input reflected-ripple current is measured with a simulated source inductance. Current is measured at the input of the module.

Figure 23: Input reflected-ripple current test setup



Note: Use a 220μF PosCap and 1μF capacitor. Scope measurement should be made using a BNC connector.

Figure 24: Peak-peak output noise and startup transient measurement test setup

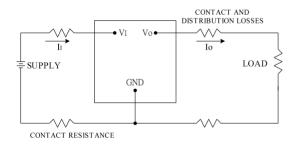


Figure 25: Output voltage and efficiency measurement test setup

Note: All measurements are taken at the module terminals. When the module is not soldered (via socket), place Kelvin connections at module terminals to avoid measurement errors due to contact resistance.

$$\eta = (\frac{Vo \times Io}{Vi \times Ii}) \times 100 \quad \%$$

DESIGN CONSIDERATIONS

Input Source Impedance

To maintain low-noise and ripple at the input voltage, it is critical to use low ESR capacitors at the input to the module. Figure 26 shows the input ripple voltage (mVp-p) for various output models using 2x100uF low ESR electrolytic capacitors (Rubycon P/N:50YXG100, 100uF/50V or equivalent) and 1x3.3.0 uF very low ESR ceramic capacitors (TDK P/N: C4532JB1H335M, 3.3uF/50V or equivalent).

The input capacitance should be able to handle an AC ripple current of at least:

$$Irms = Iout \sqrt{\frac{Vout}{Vin} \left(1 - \frac{Vout}{Vin}\right)} \quad Arms$$

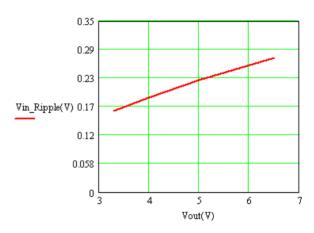


Figure 26: Input ripple voltage for various output models, Io = 3A (Cin =2x100uF electrolytic capacitors 1x3.3uF ceramic capacitors at the input)

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the module. An input capacitance must be placed close to the modules input pins to filter ripple current and ensure module stability in the presence of inductive traces that supply the input voltage to the module.

DESIGN CONSIDERATIONS

Remote On/Off

The IPM series power modules have an On/Off control pin for output voltage remote On/Off operation. The On/Off pin is an open collector/drain logic input signal that is referenced to ground. When On/Off control pin is not used, leave the pin unconnected.

The remote on/off pin is internally connected to +5Vdc through an internal pull-up resistor. Figure 27 shows the circuit configuration for applying the remote on/off pin. The module will execute a soft start ON when the transistor Q1 is in the off state.

The typical rise for this remote on/off pin at the output voltage of 2.5V and 5.0V are shown in Figure 17 and 18.

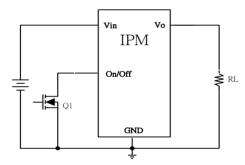


Figure 27: Remote on/off implementation

FEATURES DESCRIPTIONS

Over-Current Protection

To provide protection in an output over load fault condition, the unit is equipped with internal over-current protection. When the over-current protection is triggered, the unit enters hiccup mode. The units operate normally once the fault condition is removed.

Output Voltage Programming

The output voltage shall be externally adjustable by use of a Trim pin. The module output shall be adjusted by either a voltage source referenced to ground or an external resistor be connected between trim pin and Vo or ground. To trim-down using an external resistor, connect a resistor between the Trim and Vo pin of the module. To trim-up using an external resistor, connect a resistor between the Trim and ground pin of the module. The value of resistor is defined below. The module outputs shall not be adversely affected (regulation and operation) when the Trim pin is left open.

Trim up

Rtrim =
$$\frac{\text{(Vout-0.7)*1.43}}{\text{Vadj-Vout}}$$
 (K Ω)

Trim Down

Rtrim =
$$\frac{\text{(Vadj-0.7)*5.36}}{\text{Vout-Vadj}}$$
 (K Ω)

Rtrim is the external resistor in $K\Omega$ Vadj is the desired output voltage Vout is the output voltage without trim resistor

IPM can also be programmed by applying a voltage between the TRIM and GND pins (Figure 30). The following equation can be used to determine the value of Vtrim needed for a desired output voltage Vo:

FEATURES DESCRIPTIONS (CON.)

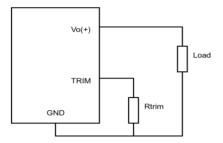


Figure 28: Trim up Circuit configuration for programming output voltage using an external resistor

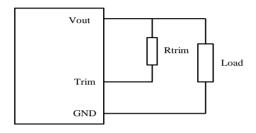


Figure 29: Trim down Circuit configuration for programming output voltage using an external resistor

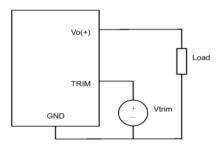


Figure 30: Circuit configuration for programming output voltage using external voltage source

Table 1 provides Rtrim values required for some common output voltages. By using a 0.5% tolerance resistor, set point tolerance of $\pm 2\%$ can be achieved as specified in the electrical specification.

Table 1 Rtrim is the external resistor in $K\Omega$; Vadj is the desired output voltage

		Rtrim s	Output	
		Kuiii s	Measurement	
		R.trim_Up R.trim_Down		0A
Vo	3.3	NC	NC	3.323V
Vadj	4.0	5.36K	NC	4.023V
Vadj	5.0	2.21K	NC	5.019V
Vadj	6.5	1.18K	NC	6.493V
Vadj	3.3*(1-10%)	NC	36.5K	2.984V

The amount of power delivered by the module is the voltage at the output terminals multiplied by the output current. When using the trim feature, the output voltage of the module can be increased, which at the same output current would increase the power output of the module. Care should be taken to ensure that the maximum output power of the module must not exceed the maximum rated power (Vo.set x Io.max ≤ P max).

Voltage Margining

Output voltage margining can be implemented in the IPM modules by connecting a resistor, R_{margin-up}, from the Trim pin to the ground pin for margining-up the output voltage and by connecting a resistor, R_{margin-down}, from the Trim pin to the output pin for margining-down. Figure 32 shows the circuit configuration for output voltage margining. If unused, leave the trim pin unconnected.

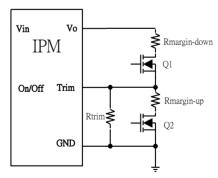


Figure 32: Circuit configuration for output voltage margining

THERMAL CONSIDERATIONS

Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

Thermal Testing Setup

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The height of this fan duct is constantly kept at 25.4mm (1").

Thermal Derating

Heat can be removed by increasing airflow over the module. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.

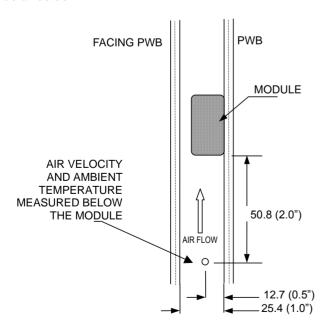


Figure 31: Wind tunnel test setup figure dimensions are in millimeters and (inches)

THERMAL CURVES

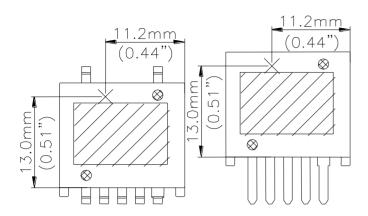


Figure 32: Temperature measurement location

* The allowed maximum hot spot temperature is defined at 125 \mathcal{C} .

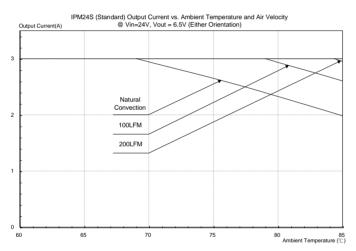


Figure 33: Output current vs. ambient temperature and air velocity @Vin=24V, Vout=6.5V(Either Orientation)

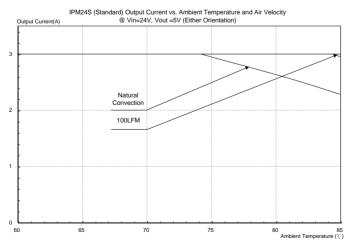


Figure 34: Output current vs. ambient temperature and air velocity @Vin=24V, Vout=5V(Either Orientation)

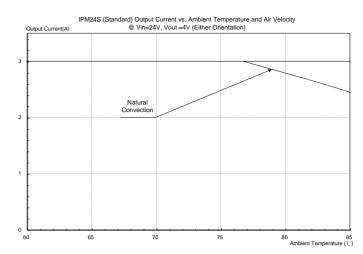


Figure 35: Output current vs. ambient temperature and air velocity Vin=24V, Vout=4V(Either Orientation)

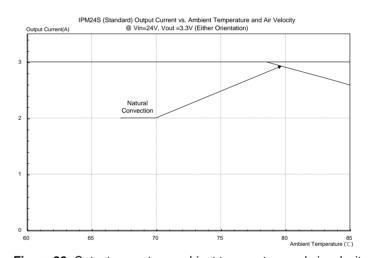
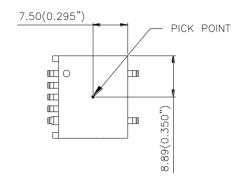
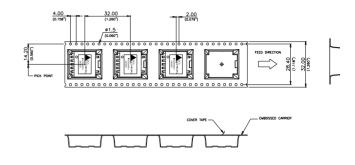


Figure 36: Output current vs. ambient temperature and air velocity @Vin=24V, Vout=3.3V(Either Orientation)

PICK AND PLACE LOCATION

SURFACE- MOUNT TAPE & REEL

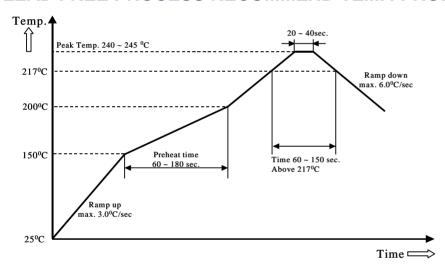




All dimensions are in millimeters (inches)

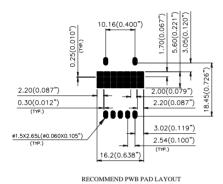
All dimensions are in millimeters (inches)

LEAD FREE PROCESS RECOMMEND TEMP. PROFILE

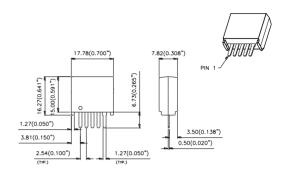


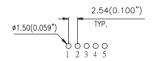
Note: All temperature refers to topside of the package, measured on the package body surface.

Mechanical Drawing SMD PACKAGE



SIP PACKAGE





RECOMMEND PWB HOLE LAYOUT

Note: The copper pad is recommended to connect to the ground.



Pin#	Function			
1	Remote on/off			
2	+Vin			
3	Ground			
4	+Vo			
5	Trim			
6	No connection			
7	No connection			



Pin#	Function			
1	Remote on/off			
2	+Vin			
3	Ground			
4	+Vo			
5	Trim			

Note: All dimension are in millimeters (inches) standard dimension tolerance is ± 0.10(0.004")

PART NUMBERING SYSTEM

IPM	24	S	0B0	S	03	F	Α
Product Family	Input Voltage	Number of Outputs	Output Voltage	Package	Output Current		Option Code
Integrated POL	11V ~ 36V	S - Single	0B0 - programmable	R - SIP	03 - 3A	F- RoHS 6/6	A - Standard
Module			output	S - SMD		(Lead Free)	Function
			3.3V~6.5V				

MODEL LIST

Model Name	Input Voltage	Output Voltage	Output Current	Efficiency (Full load@12Vin)
IPM24S0A0S/R03FA	8V ~ 36V	1.2V ~ 2.5V	3A	85%
IPM24S0B0S/R03FA	11V ~ 36V	3.3V ~ 6.5V	3A	91%
Model Name	Input Voltage	Output Voltage	Output Current	Efficiency (Full load@20Vin)
IPM24S0C0S/R03FA	20V ~ 36V	8.0V~15.0V	3A	95%

CONTACT: www.deltaww.com/dcdc

USA:Europe:Telephone:Telephone: +31-20-655-0967

East Coast: 978-656-3993 West Coast: 510-668-5100 Fax: (978) 656 3964

Email: DCDC@delta-corp.com

Asia & the rest of world:

Telephone: +886 3 4526107 x6220~6224

Fax: +886 3 4513485 Email: DCDC@delta.com.tw

WARRANTY

Delta offers a two (2) year limited warranty. Complete warranty information is listed on our web site or is available upon request from Delta.

Fax: +31-20-655-0999

Email: DCDC@delta-es.com

Information furnished by Delta is believed to be accurate and reliable. However, no responsibility is assumed by Delta for its use, nor for any infringements of patents or other rights of third parties, which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Delta. Delta reserves the right to revise these specifications at any time, without notice.