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8-bit Proprietary Microcontroller

CMOS

F²MC-8L MB89190/190A Series

MB89191/193/195/P195/PV190
MB89191A/191AH/193A/193AH/195A/P195A/PV190A

■ **OUTLINE**

The MB89190/190A series microcontrollers contain various resources such as timers, serial interfaces, A/D converters, external interrupts, and remote-control functions, as well as an F²MC*-8L CPU core for low-voltage and high-speed operations. These single-chip microcontrollers are suitable for small devices such as remote controllers with compact packages.

*: F²MC is the abbreviation of FUJITSU Flexible Microcontroller.

■ **FEATURES**

• Minimum execution time: 0.95 μs at 4.2 MHz (V_{CC} = 2.7 V)
• F²MC-8L family CPU core
• Two timers
  8/16-bit timer/counter
  20-bit timebase counter
• Serial interface
  8-bit synchronous serial (Selectable transfer direction allows communication with various equipment.)
• External interrupts
  Edge detection (Selectable edge): 3 channels
  Low-level interrupt (Wake-up function): 8 channels
• A/D converter (MB89190A series only)
  8-bit successive approximation type: 8 channels
• Built-in remote-control transmitting frequency generator
• Low-power consumption modes
  Stop mode (Almost no current consumption occurs because oscillation stops.)
  Sleep mode (The current consumption is reduced about 1/3 of that during normal operation because the CPU stops.)
• Packages
  SOP-28, SH-DIP-28, and DIP-28

For the information for microcontroller supports, see the following web site.

http://edevice.fujitsu.com/micom/en-support/

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■ PRODUCT LINEUP

Part number \ Item	MB89191 MB89191A MB89191AH	MB89193 MB89193A MB89193AH	MB89195 MB89195A	MB89P195 MB89P195A	MB89PV190 MB89PV190A
Classification	Mask ROM products			One-time product	For development and evaluation
ROM size	4 K × 8 bits (internal mask ROM)	8 K × 8 bits (internal mask ROM)	16 K × 8 bits (internal mask ROM)	16 K × 8 bits (internal PROM, to be programmed with general-purpose EPROM programmer)	32 K × 8 bits (external ROM)
RAM size	128 × 8 bits	256 × 8 bits			
CPU functions	The number of basic instructions: 136 Instruction bit length: 8 bits Instruction length: 1 to 3 bytes Data bit length: 1, 8, and 16 bits Minimum execution time: 0.95 µs at 4.2 MHz Interrupt processing time: 8.57 µs at 4.2 MHz				
Ports	Output port (N channel open drain): 4 (also serves as peripherals for MB89190A series)or 6 (for MB89190 series) I/O port (CMOS): 16 (also serves as peripherals) Total: 20 or 22				
Timer counter	2 channels of 8-bit timer counter or one 16-bit event counter (operation clock: 1.9 µs, 30.4 µs, and 487.6 µs at 4.2 MHz, and external clock)				
Serial I/O	8 bits LSB/MSB first selectable Transfer clock (external, 1.9 µs, 7.6 µs, 30.4 µs at 4.2 MHz)				
A/D converter (MB89190A series only)	8 bits x 8 channels A/D conversion mode (conversion time: 41.9 µs at 4.2 MHz) Sense mode (conversion time: 11.9 µs at 4.2 MHz) Capable of continuous activation by an internal timer. Reference voltage input				
External interrupt 1	3 independent channels (selectable edge, interrupt vector, and interrupt source flag) Rising/falling/both edge selectable Used for wake-up from stop/sleep mode. (Edge detection is also permitted in the stop mode.)				
External interrupt 2 (Wake-up function)	8 channels (low-level interrupt only)				
Remote-control transmitting frequency generator	The pulse width and cycle are software-programmable.				
Standby mode	Sleep mode and stop mode				
Process	CMOS				

(Continued)

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Part number Item	MB89191 MB89191A MB89191AH	MB89193 MB89193A MB89193AH	MB89195 MB89195A	MB89P195 MB89P195A	MB89PV190 MB89PV190A
Operating voltage*	2.2 V to 6.0 V			2.7 V to 6.0 V	
EPROM for use					MBM27C256A-20TVM

*: Varies with conditions such as operating frequencies (see "■ Electrical Characteristics".) It differs from the operating voltage of an A/D converter.

■ PACKAGE AND CORRESPONDING PRODUCTS

Package	MB89191 MB89191A MB89191AH MB89193 MB89193A MB89193AH MB89195 MB89195A	MB89P195 MB89P195A	MB89PV190 MB89PV190A
DIP-28P-M05	○	○	×
DIP-28P-M03	○	×	×
FPT-28P-M17	○	○	×
MQP-48C-P01	×	×	○ *

○ : Available   × : Not available

* : A socket for pin pitch conversion is available.

Note: For more information on each package, see "■ Package Dimensions".

■ DIFFERENCES AMONG PRODUCTS

1. Memory Size

Before evaluating using the piggyback model, verify its difference from the model that will actually be used. Take particular care on the following points:
- On the MB89191/191A, addresses 0140$_H$ to 0180$_H$ cannot be used for register banks.
- The stack area, etc., is set in the upper limit of the RAM.

2. Current Consumption

- In the case of MB89PV190/PV190A, added is the current consumed by the EPROM which is connected to the top socket.
- When operated at low speed, the products with an OTPROM (EPROM) will consume more current than the products with a mask ROM.
  However, the same is current consumption in the sleep/stop mode. (For more information, see "■ Electrical Characteristics".)

3. Mask Options

Functions that can be selected as options and how to designate these options vary with product.

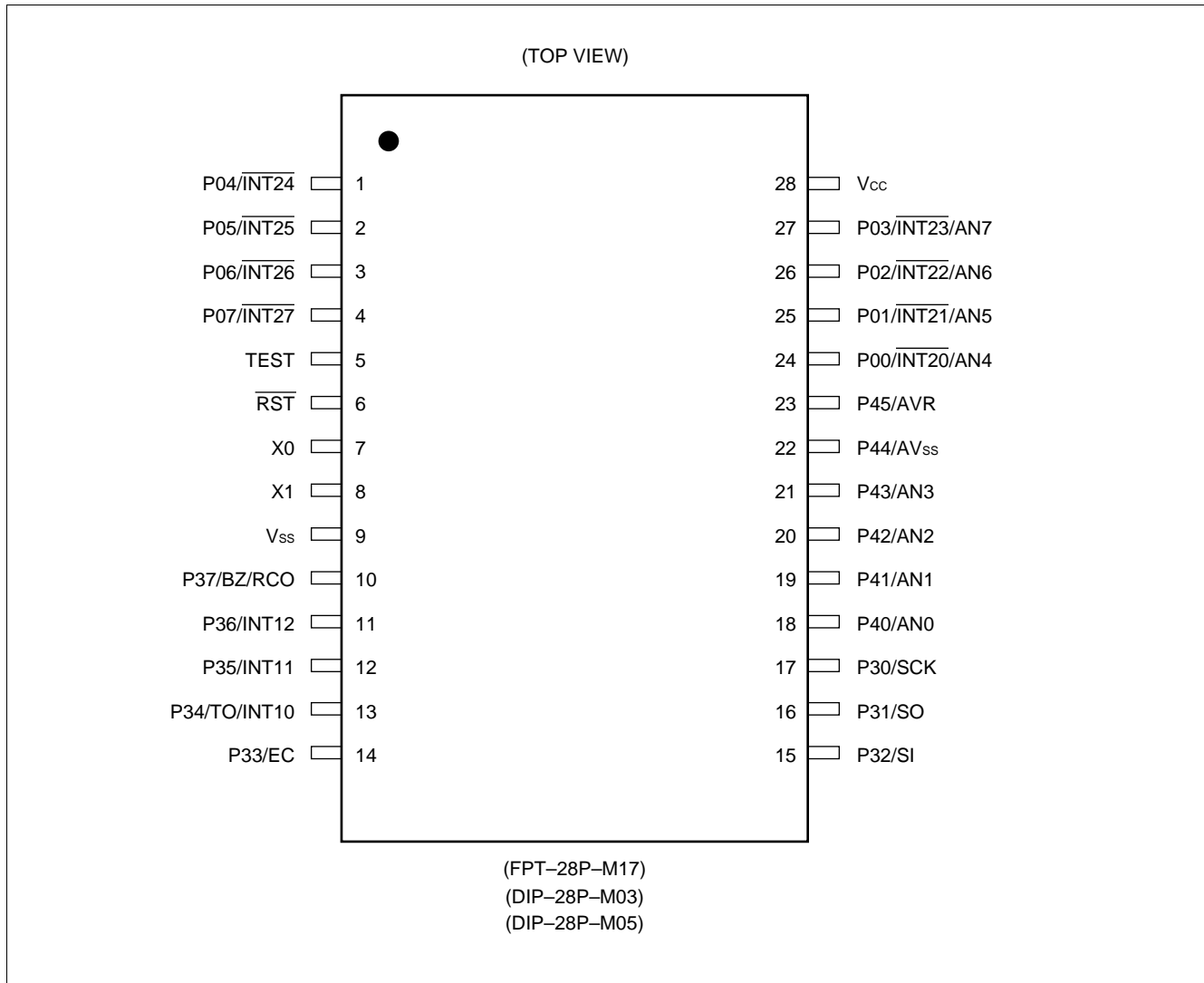Before using options, check "■ Mask Options".

Take particular care on the following points:
- Pull-up resistor optional cannot be set for P00 to P03, and P40 to P45 on the MB89191A/193A/195A/P195A.
- The power-on reset option is fixed as "enabled" for MB89P195/P195A.
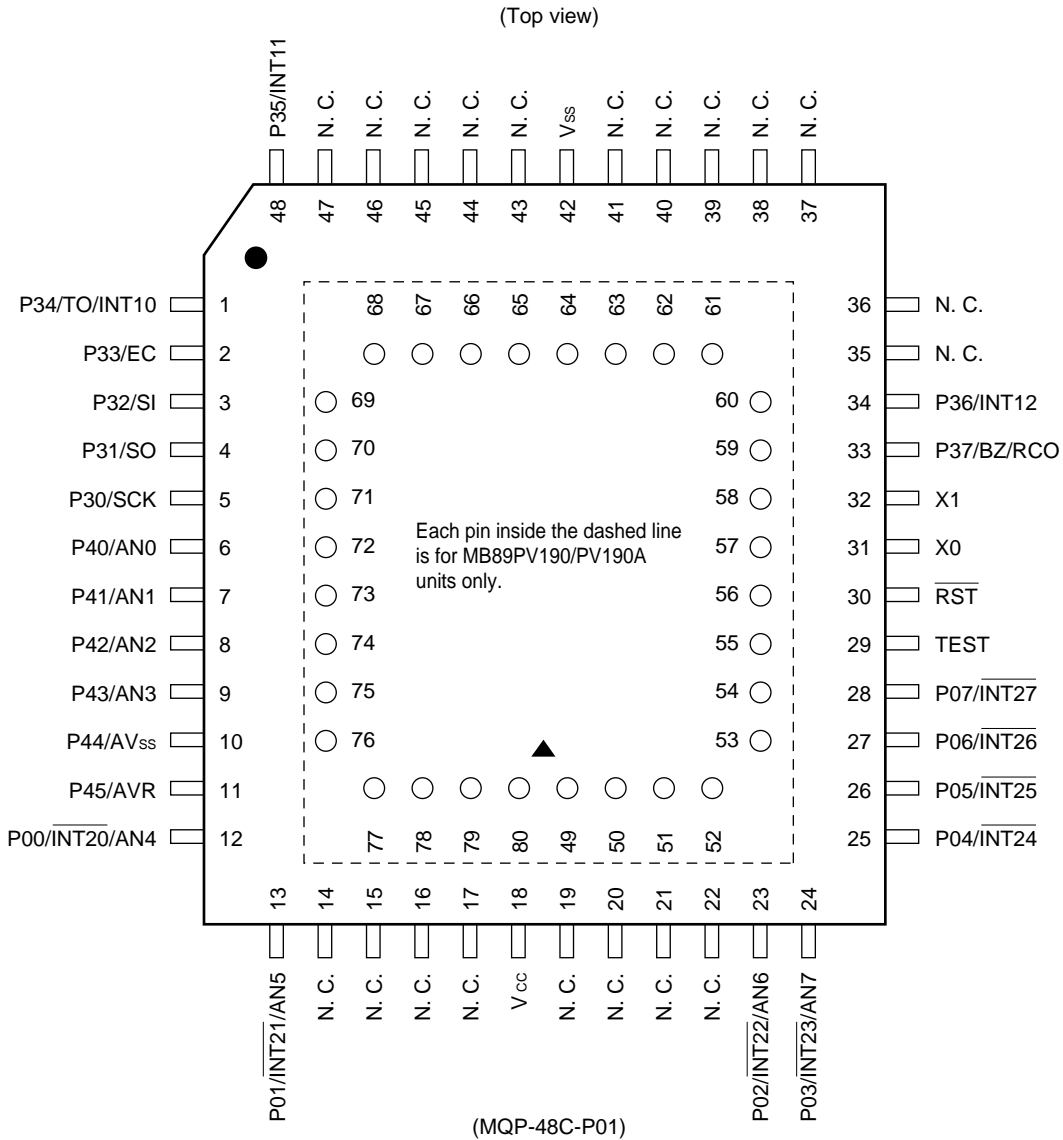- Options are fixed on the MB89PV190/PV190A.

4. MB89191AH/MB89193AH

MB89191AH/193AH are "L" level heavy output current drive type of P30 to P32 and P40 to P43 of MB89191A/193A.Characteristics other than "L" level output of P30 to P32 and P40 to P43 are the same as MB89191A/193A.

■ PIN ASSIGNMENT

(TOP VIEW)

Left pin	No.		No.	Right pin
P04/$\overline{INT24}$	1		28	V_{CC}
P05/$\overline{INT25}$	2		27	P03/$\overline{INT23}$/AN7
P06/$\overline{INT26}$	3		26	P02/$\overline{INT22}$/AN6
P07/$\overline{INT27}$	4		25	P01/$\overline{INT21}$/AN5
TEST	5		24	P00/$\overline{INT20}$/AN4
$\overline{RST}$	6		23	P45/AVR
X0	7		22	P44/AV_{SS}
X1	8		21	P43/AN3
V_{SS}	9		20	P42/AN2
P37/BZ/RCO	10		19	P41/AN1
P36/INT12	11		18	P40/AN0
P35/INT11	12		17	P30/SCK
P34/TO/INT10	13		16	P31/SO
P33/EC	14		15	P32/SI

(FPT–28P–M17)
(DIP–28P–M03)
(DIP–28P–M05)

(Top view)

(MQP-48C-P01)

- • **Pin assignment on the package top (MB89PV190/PV190A only)**

Pin no.	Pin name	Pin no.	Pin name	Pin no.	Pin name	Pin no.	Pin name
49	V_{PP}	57	N.C.	65	O4	73	$\overline{OE}$
50	A12	58	A2	66	O5	74	N.C.
51	A7	59	A1	67	O6	75	A11
52	A6	60	A0	68	O7	76	A9
53	A5	61	O1	69	O8	77	A8
54	A4	62	O2	70	$\overline{CE}$	78	A13
55	A3	63	O3	71	A10	79	A14
56	N.C.	64	V_{SS}	72	N.C.	80	V_{CC}

N.C.: Internally connected. Do not use.

■ PIN DESCRIPTION

Pin no.		Pin name	Circuit type	Function
SOP[*1], DIP[*2] SH-DIP[*3]	MQFP[*4]			
7	31	X0	A	Clock oscillation pins
8	32	X1		
5	29	TEST	B	Test input pin Connect directly to V_{SS}.
6	30	$\overline{RST}$	C	Reset I/O pin This pin consists of an N-ch open-drain output with a pull-up resistor and of hysteresis input. A low level is output from this pin by internal source. The internal circuit is initialized by the input of a low level.
24 to 27	12 13, 23, 24	P00/$\overline{INT20}$/ AN4 to P03/ $\overline{INT23}$/AN7	G	General-purpose I/O ports Also serve as external interrupt input pins. In the MB89190A series, also serve as analog input pins. External interrupt input is of hysteresis input type.
1 to 4	25 to 28	P04/$\overline{INT24}$ to P07/$\overline{INT27}$	D	General-purpose I/O ports Also serve as external interrupt input. External interrupt input is of hysteresis input type.
17	5	P30/SCK	D	General-purpose I/O port Also serves as clock I/O for the 8-bit serial I/O interface. The serial I/O clock input is of hysteresis input type with a built-in noise filter.
16	4	P31/SO	E	General-purpose I/O port Also serves as a serial I/O data output pin.
15	3	P32/SI	D	General-purpose I/O port Also serves as a serial I/O data input pin. The serial I/O data input is of hysteresis input type with a built-in noise filter.
14	2	P33/EC	D	General-purpose I/O port Also serves as an external clock input pin for the 8-bit timer/counter. External clock input of the 8-bit timer/counter is hysteresis input type with a built-in noise filter.
13	1	P34/TO/ INT10	D	General-purpose I/O port Also serves as the overflow output and external interrupt input for the 8-bit timer/counter. External interrupt input is of hysteresis input type with a built-in noise filter.

*1: FPT-28P-M17
*2: DIP-28C-M05
*3: DIP-28P-M03
*4: MQP-48C-P01

(Continued)

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Pin no.		Pin name	Circuit type	Function
SOP[*1], DIP[*2] SH-DIP[*3]	MQFP[*4]			
12	48	P35/INT11	D	General-purpose I/O port Also serve as external interrupt input pins. External interrupt input is of hysteresis input type with a built-in noise filter.
11	34	P35/INT12		
10	33	P37/BZ/RCO	E	General-purpose I/O port Also serves as a buzzer output pin and remote-control output pin.
18 to 21	6 to 9	P40/AN0 to P43/AN3	F	N-ch open-drain output ports Also serve as analog input pins for the A/D converter.
23	11	P45/AVR	F	In the MB89190A series, also serves as a reference voltage input pin for the A/D converter. In the MB89190 series, serves as an N-ch open-drain output port.
22	10	P44/AV$_{SS}$	F	In the MB89190A series, also serves as a power pin for the A/D converter, and should be applied the same voltage as V$_{SS}$ to. In the MB89190 series, also serves as an N-ch open-drain output port.
28	18	V$_{CC}$	—	Power supply pin
9	42	V$_{SS}$	—	Power supply (GND) pin

*1: FPT-28P-M17
*2: DIP-28P-M05
*3: DIP-28P-M03
*4: MQP-48C-P01

- **External EPROM pins (MB89PV190/PV190A)**

Pin no.	Pin name	I/O	Function
49	V_{PP}	O	"H" level output pin
79 78 50 75 71 76 77 51 52 53 54 55 58 59 60	A14 A13 A12 A11 A10 A9 A8 A7 A6 A5 A4 A3 A2 A1 A0	O	Address output pins
61 62 63 65 66 67 68 69	O1 O2 O3 O4 O5 O6 O7 O8	I	Data input pins
70	$\overline{CE}$	O	ROM chip enable pin Outputs "H" during standby.
73	$\overline{OE}$	O	ROM output enable pin Outputs "L" at all times.
80	V_{CC}	O	EPROM power pin
64	V_{SS}	O	Power supply (GND) pin

■ I/O CIRCUIT TYPE

Type	Circuit	Remarks
A	X1 X0 ... Standby control signal	• Oscillation feedback registor of approximately 1 MΩ at 5 V • When crystal and ceramic oscillators are selected optionally
	X1 X0 ... Standby control signal	• When CR oscillation is selected optionally
B		• CMOS input
C	R, P-ch, N-ch	• Output pull-up resistor (P-ch): Approx. 50 kΩ at 5 V • Hysteresis input
D	R, P-ch, P-ch, N-ch	• CMOS output • CMOS input • Hysteresis input (resource input) • Pull-up resistor optional

(Continued)

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Type	Circuit	Remarks
E		• CMOS output • CMOS input • Pull-up resistor optional
F		• N-ch open-drain output • Analog input • Pull-up resistor optional (MB89190 series only)
G		• CMOS output • CMOS input • Hysteresis input (resource input) • Analog input • Pull-up resistor optional (MB89190 series only)

■ HANDLING DEVICES

1. Preventing Latch-up

Latchup may occur on CMOS ICs if voltage higher than V_{CC} or lower than V_{SS} is applied to input or output pins other than medium- and high-voltage pins or if higher than the voltage which shows on " 1. Absolute Maximum Ratings" in "■ Electrical Characteristics" is applied between V_{CC} to V_{SS}.

When latchup occurs, power supply current increases rapidly and might thermally damage elements. When using, take great care not to exceed the absolute maximum ratings.

Also, take care to prevent the analog power supply (AVR) and analog input from exceeding the digital power supply (V_{CC}) when the analog system power supply is turned on and off.

2. Treatment of Unused Input Pins

Leaving unused input pins open could cause malfunctions. They should be connected to pull-up or pull-down resistor.

3. Treatment of Power Supply Pins on Microcontrollers with A/D and D/A Converters

Connect to be $AV_{SS}=AVR=V_{SS}$ even if the A/D and D/A converters are not in use.

4. Treatment of N.C. Pin

Be sure to leave (internally connected) N.C. pins open.

5. Power Supply Voltage Fluctuations

Although operation is assured within the rated range of V_{CC} power supply voltage, a rapid fluctuation of the voltage could cause malfunctions within the rated range. Stabilizing voltage supplied to the IC is therefore important. As stabilization guidelines, it is recommended to control power so that V_{CC} ripple fluctuations (P-P value) will be less than 10% of the standard V_{CC} value at the commercial frequency (50 Hz to 60 Hz) and the transient fluctuation rate will be less than 0.1 V/ms at the time of a momentary fluctuation such as when power is switched.

6. Precautions when Using an External Clock

Even when an external clock is used, oscillation stabilization time is required for power-on reset (optional) and release from stop mode.

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■ PROGRAMMING TO PROM ON THE MB89P195/P195A

The MB89P195/P195A can program data in the internal PROM using a dedicated conversion adaptor and specified general-purpose EPROM programmer.
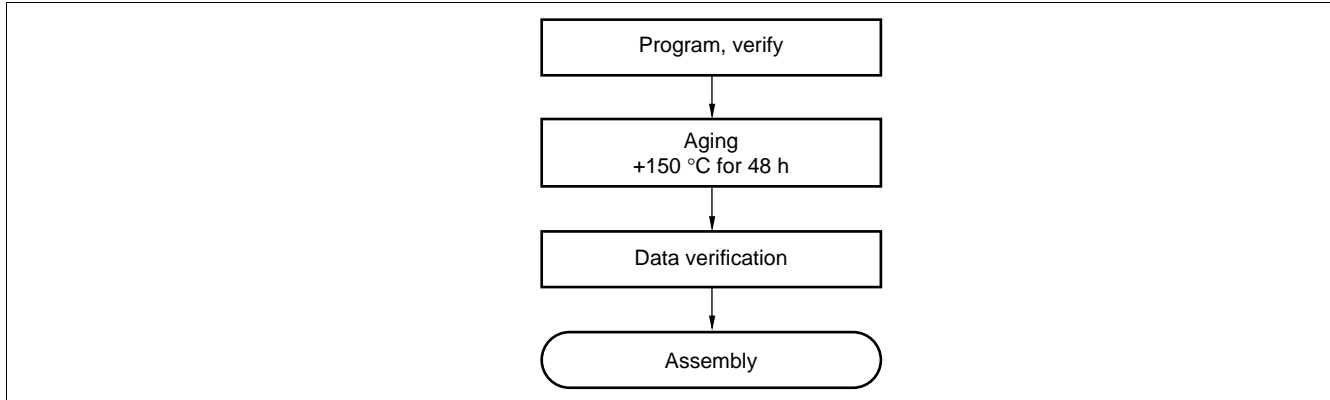
1. Memory Space
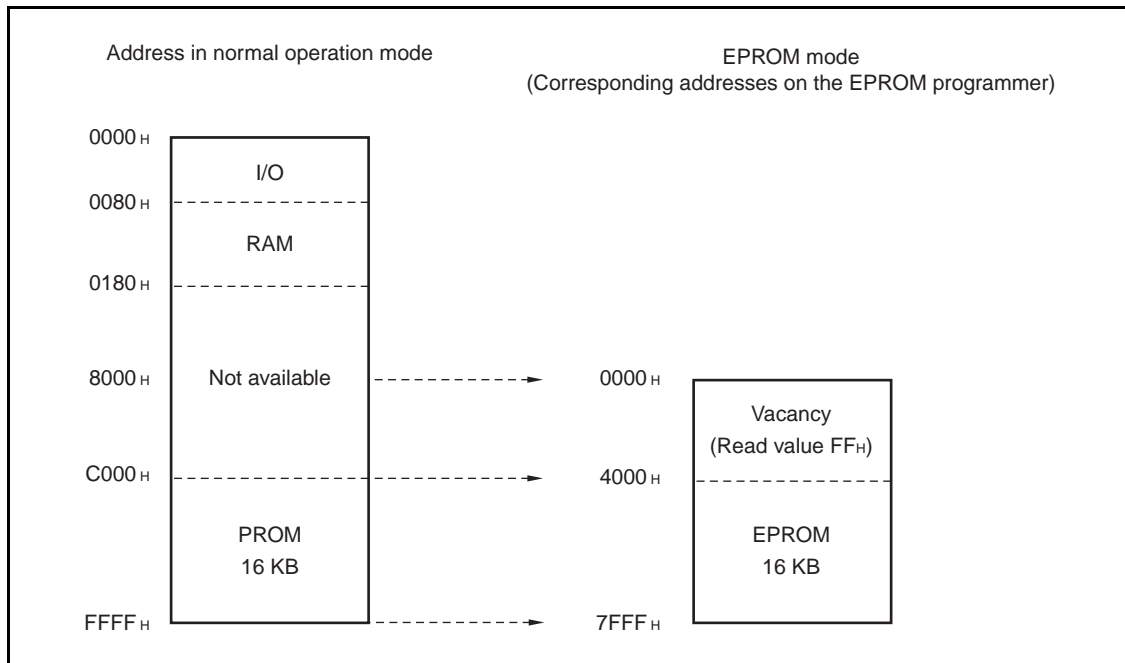
• **Programming procedure**

(1) Set the EPROM programmer for the MB89P195.

(2) Load program data into the EPROM programmer at addresses 4000_H to $7FFF_H$. (Addresses $C000_H$ to $FFFF_H$ in the operation mode correspond to 4000_H to $7FFF_H$ in EPROM programmer. See the illustration above.)

(3) Set the data at addresses 0000_H to $3FFF_H$ of the programmer ROM in the EPROM programmer, to FF_H.

(4) Write data using the EPROM programmer.

Note: Program must be started at the address 0000_H.

2. Recommended Screening Conditions

High-temperature aging is recommended as the pre-assembly screening procedure for a product with a blanked OTPROM microcontroller program.

```
          ┌──────────────────────┐
          │    Program, verify    │
          └──────────────────────┘
                      │
                      ▼
          ┌──────────────────────┐
          │        Aging          │
          │   +150 °C for 48 h    │
          └──────────────────────┘
                      │
                      ▼
          ┌──────────────────────┐
          │   Data verification   │
          └──────────────────────┘
                      │
                      ▼
          (        Assembly        )
```
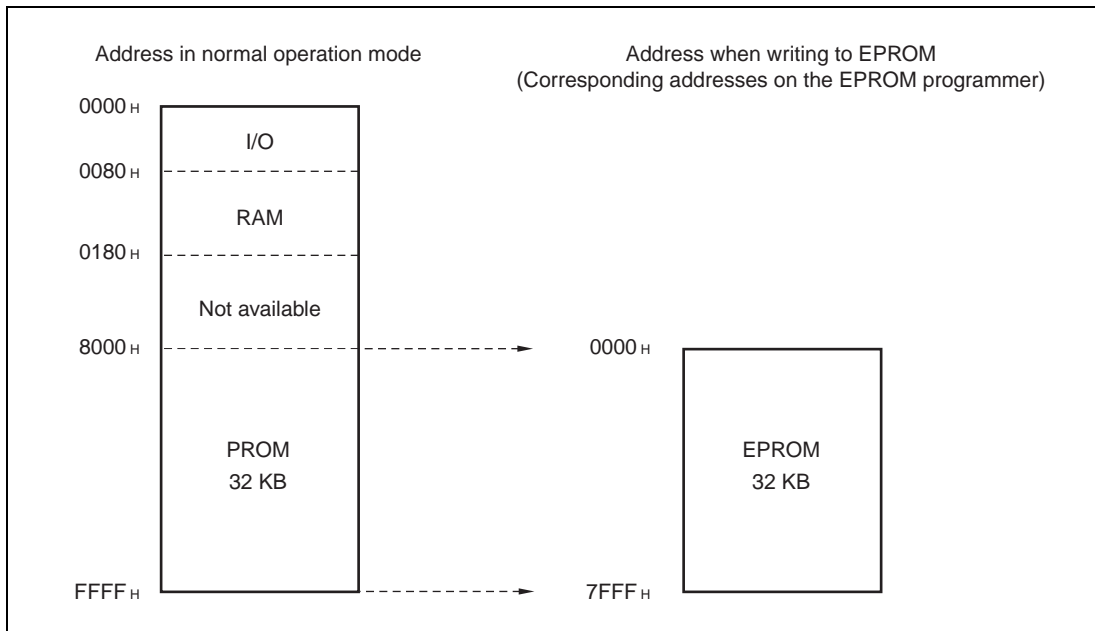
3. Programming Yield

Due to its nature, bit programming test can't be conducted as Fujitsu Microelectronics delivery test. For this reason, a programming yield of 100% cannot be assured at all times.

■ PROGRAMMING TO THE EPROM WITH PIGGYBACK/EVALUATION DEVICE

1. EPROM for Use
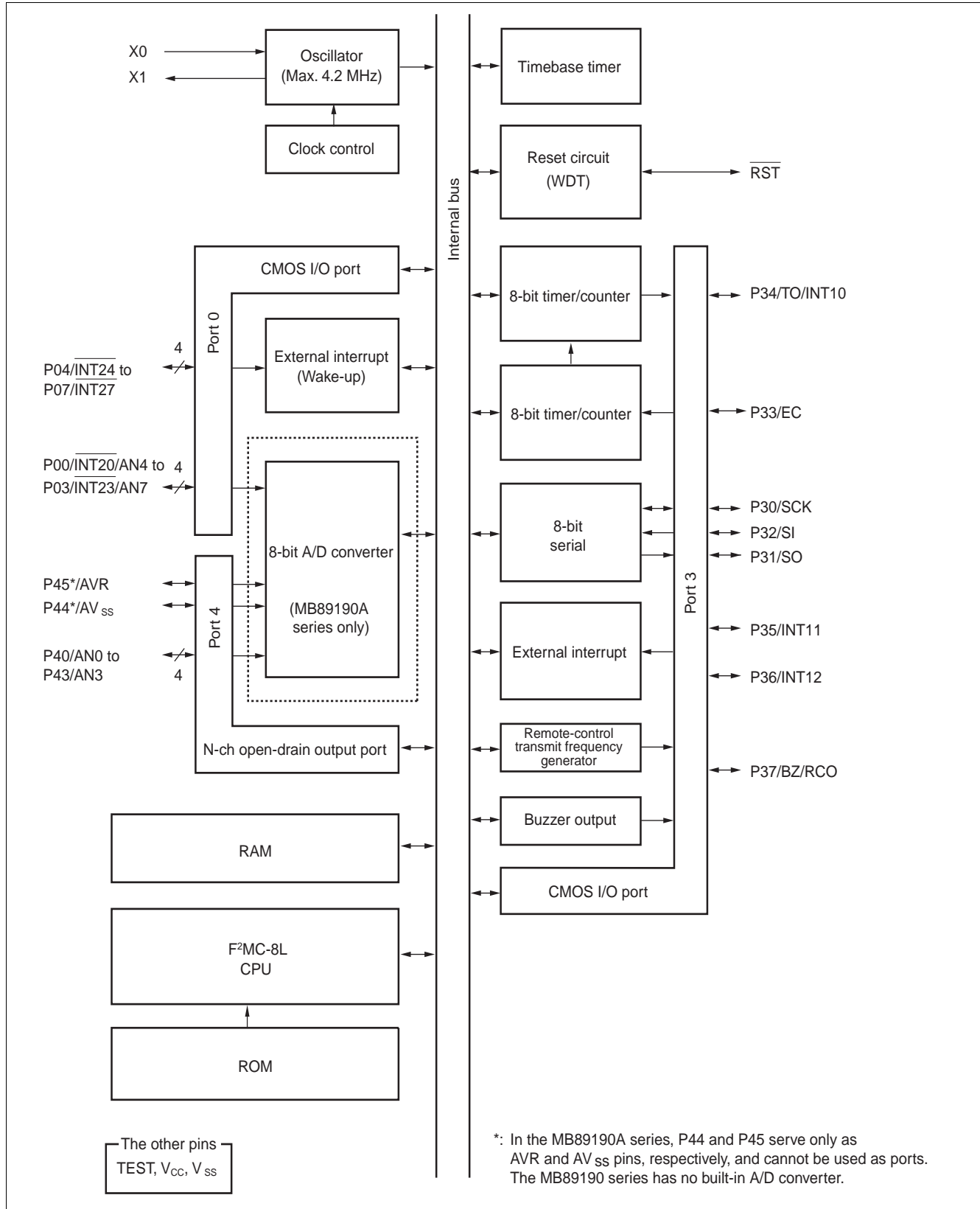
MBM27C256A-20TVM

2. Memory Space

Address in normal operation mode

Address when writing to EPROM
(Corresponding addresses on the EPROM programmer)

```
0000 H ┌──────────────┐
       │     I/O      │
0080 H ├──────────────┤
       │              │
       │     RAM      │
0180 H ├──────────────┤
       │              │
       │ Not available│
8000 H ├──────────────┤ ─ ─ ─ ─ ─ ─→  0000 H ┌──────────────┐
       │              │                       │              │
       │              │                       │              │
       │    PROM      │                       │    EPROM     │
       │    32 KB     │                       │    32 KB     │
       │              │                       │              │
FFFF H └──────────────┘ ─ ─ ─ ─ ─ ─→  7FFF H └──────────────┘
```

3. Programming to the EPROM

(1) Set the EPROM programmer for MBM27C256A.

(2) Load program data into the EPROM programmer at 0000_H to $7FFF_H$.

(3) Program to 0000_H to $7FFF_H$ with the EPROM programmer.

■ BLOCK DIAGRAM

*: In the MB89190A series, P44 and P45 serve only as AVR and AV$_{SS}$ pins, respectively, and cannot be used as ports. The MB89190 series has no built-in A/D converter.

■ CPU CORE

1. Memory Space

The microcontrollers of MB89190/190A series offer a 64 Kbytes of memory for storing all of I/O, data, and program areas. The I/O area is allocated from the lowest address. The data area is allocated immediately above the I/O area. The data area can be divided into register, stack, and direct areas according to the application. The program area is allocated from exactly the opposite end of I/O area, that is, the highest address. The tables of interrupt reset vectors, and vector call instructions are allocated from the highest address within the program area. The memory space of the MB89190/190A series is structured below:

Memory Space
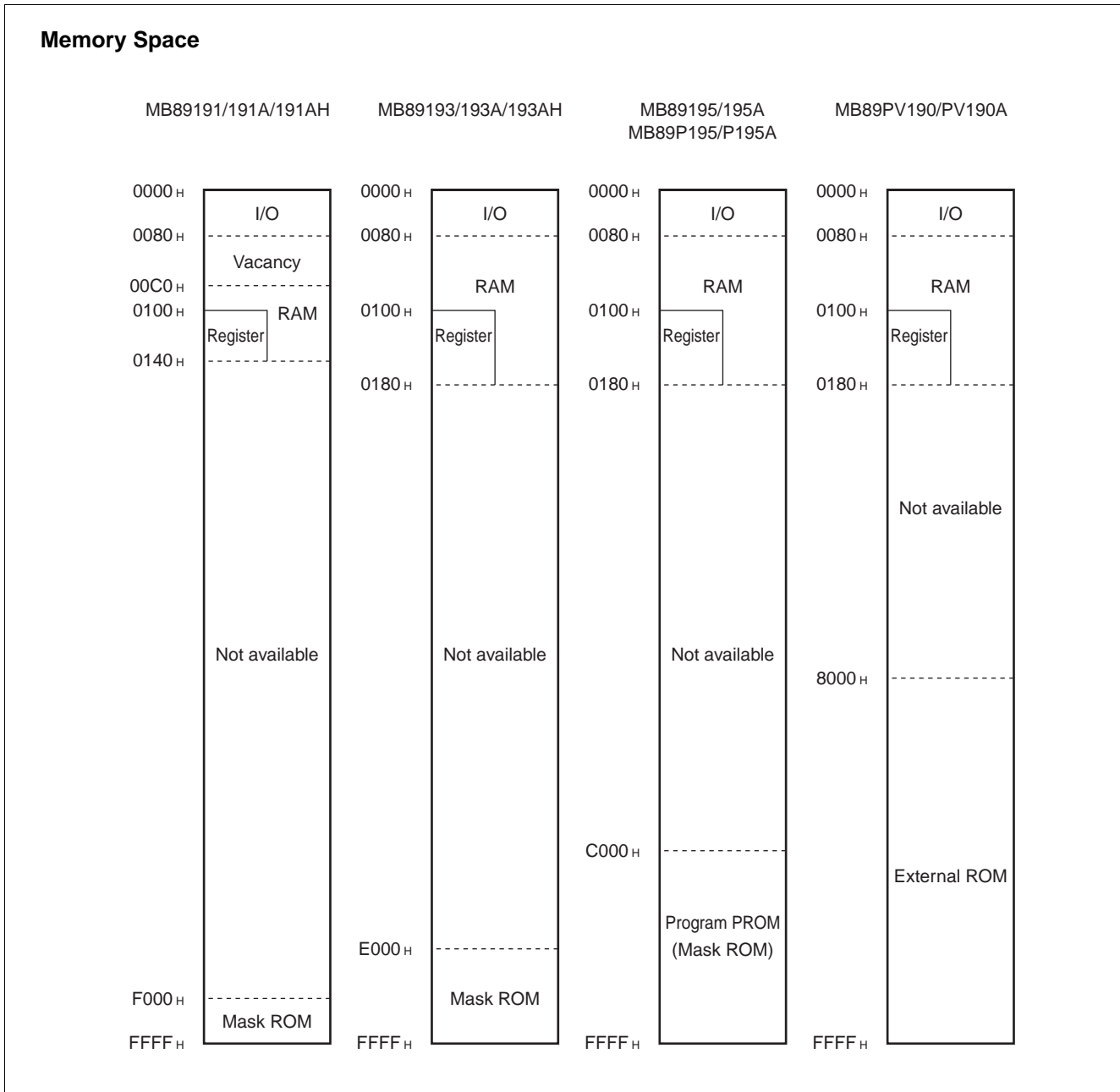
2. Registers

The F²MC-8L family has two types of registers; dedicated hardware registers and general-purpose memory registers. The following dedicated registers are provided:

Program counter (PC):	A 16-bit-long register for indicating the instruction storage positions
Accumulator (A):	A 16-bit-long temporary register for arithmetic operations, etc. When the instruction is an 8-bit data processing instruction, the lower byte is used.
Temporary accumulator (T):	A 16-bit-long register which is used for arithmetic operations with the accumulator. When the instruction is an 8-bit data processing instruction, the lower byte is used.
Index register (IX):	A 16-bit-long register for index modification
Extra pointer (EP) :	A 16-bit-long pointer for indicating a memory address
Stack pointer (SP) :	A 16-bit-long pointer for indicating a stack area
Program status (PS) :	A 16-bit-long register for storing a register pointer, a condition code

```
          16 bits                              Initial value
    ┌──────────────────┐
    │        PC        │    : Program counter          FFFD_H
    ├──────────────────┤
    │        A         │    : Accumulator              Indeterminate
    ├──────────────────┤
    │        T         │    : Temporary accumulator    Indeterminate
    ├──────────────────┤
    │        IX        │    : Index register           Indeterminate
    ├──────────────────┤
    │        EP        │    : Extra pointer            Indeterminate
    ├──────────────────┤
    │        SP        │    : Stack pointer            Indeterminate
    ├──────────────────┤
    │        PS        │    : Program status      I-flag = 0, IL1, 0 = 11
    └──────────────────┘                         The other bit values are indeterminate
```
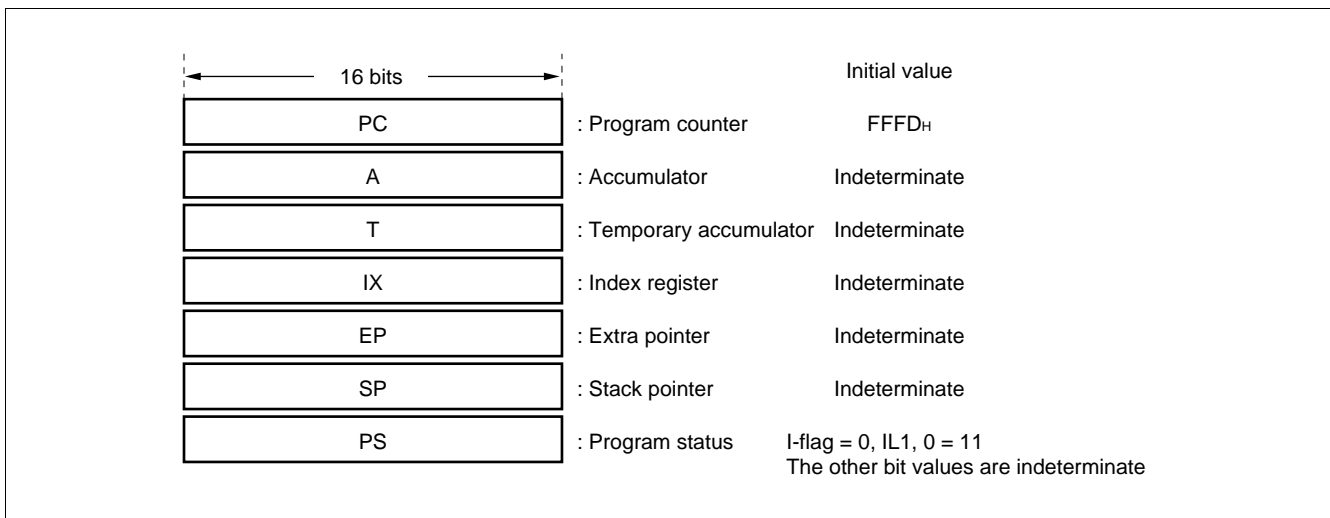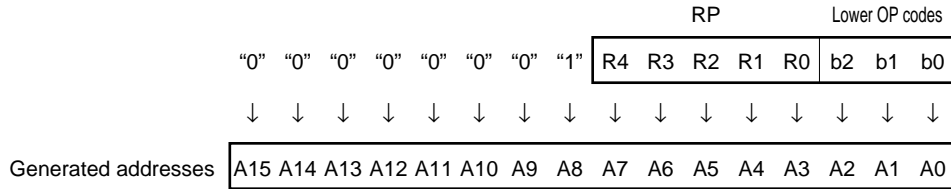
The PS can further be divided into higher 8 bits for use as a register bank pointer (RP) and the lower 8 bits for use as a condition code register (CCR) (see the diagram below).

Structure of the Program Status Register

	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
PS			RP			Vacancy	Vacancy	Vacancy	H	I	IL1, 0		N	Z	V	C
			RP								CCR					
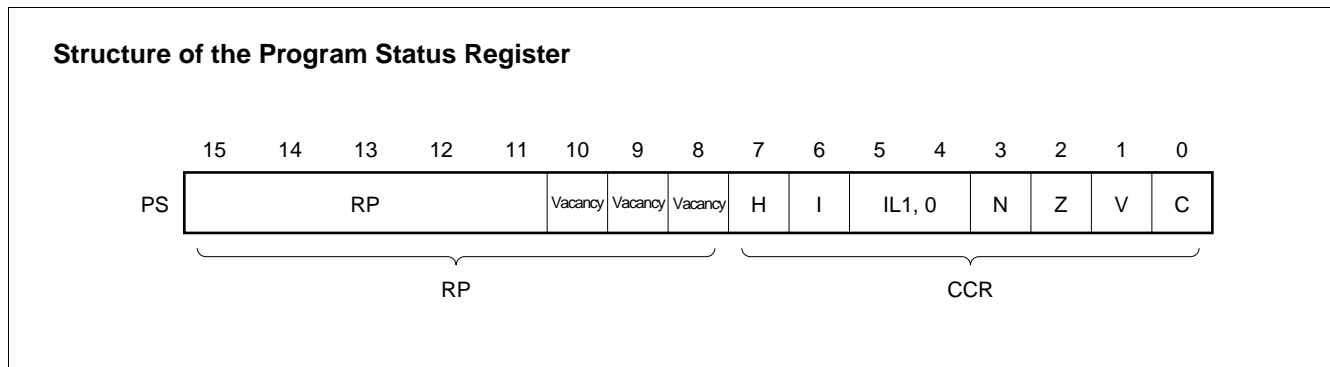
The RP indicates the address of the register bank currently in use. The relationship between the pointer contents and the actual address is based on the conversion rule illustrated below.

Rule for Conversion of Actual Addresses of the General-purpose Register Area

								RP					Lower OP codes		
"0"	"0"	"0"	"0"	"0"	"0"	"0"	"1"	R4	R3	R2	R1	R0	b2	b1	b0
↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓

Generated addresses   A15 A14 A13 A12 A11 A10 A9 A8 A7 A6 A5 A4 A3 A2 A1 A0

The CCR consists of bits indicating the results of arithmetic operations and the contents of transfer data, and bits for control of CPU operations at the time of an interrupt.

H-flag: Set to "1" when a carry or a borrow from bit 3 to bit 4 occurs as a result of an arithmetic operation. Cleared to "0" otherwise. This flag is for decimal adjustment instructions.

I-flag: Interrupt is enabled when this flag is set to "1". Interrupt is disabled when the flag is cleared to "0". Cleared to "0'" at the reset.

IL1, 0: Indicates the level of the interrupt currently allowed. Processes an interrupt only if its request level is higher than the value indicated by this bit.

IL1	IL0	Interrupt level	High-low
0	0	1	High
0	1		
1	0	2	
1	1	3	Low = no interrupt

N-flag: Set to "1" if the MSB becomes "1" as the result of an arithmetic operation. Cleared to "0" otherwise.

Z-flag: Set to "1" when an arithmetic operation results in 0. Cleared to "0" otherwise.

V-flag: Set to "1" if the complement on "2" overflows as a result of an arithmetic operation. Cleared to "0" if the overflow does not occur.

C-flag: Set to "1" when a carry or a borrow from bit 7 occurs as a result of an arithmetic operation. Cleared to "0" otherwise. Set to "1" the shift-out value in the case of a shift instruction.
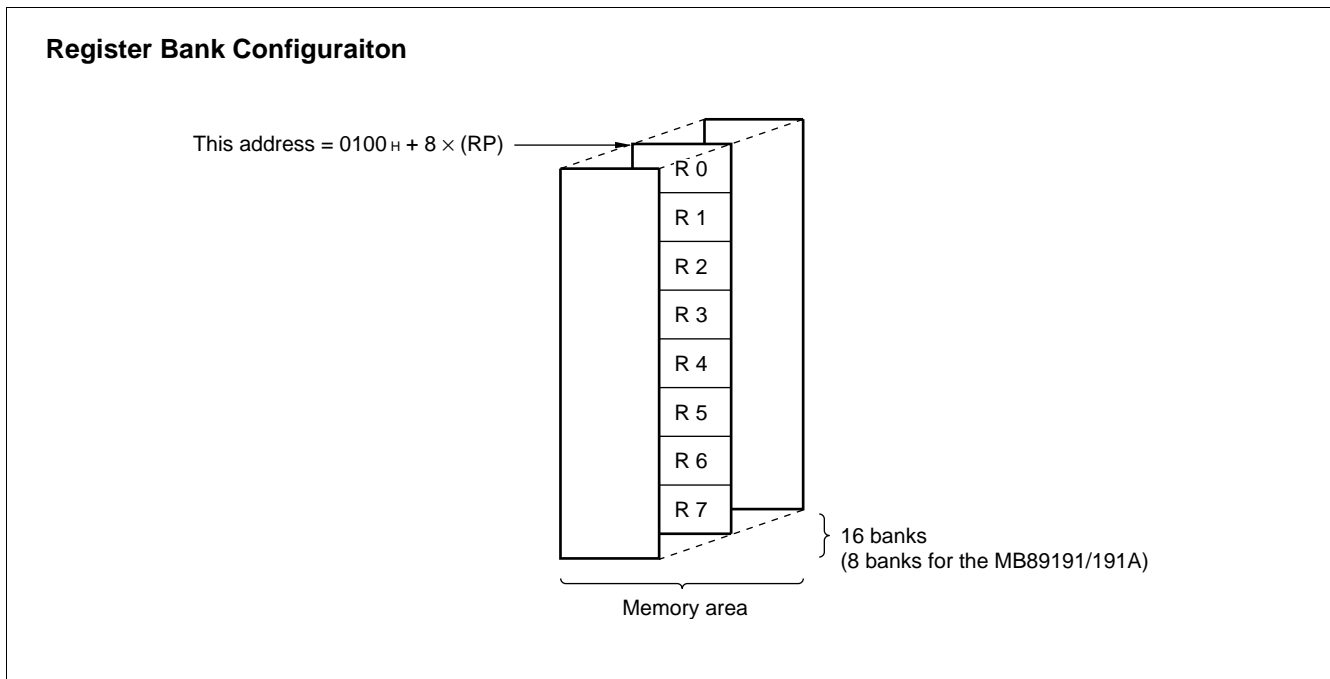
The following general-purpose registers are provided:

General-purpose registers: An 8-bit-long register for storing data

The general-purpose registers are of 8 bits and located in register banks of the memory. One bank contains eight registers and up to a total of 16 banks can be used on the MB89190/190A series (8 banks on MB89191/191A). The bank currently in use is indicated by the register bank pointer. (RP)

Note: The number of register banks that can be used varies with the RAM size.

Register Bank Configuraiton

This address $= 0100_H + 8 \times (RP)$

| R 0 |
| R 1 |
| R 2 |
| R 3 |
| R 4 |
| R 5 |
| R 6 |
| R 7 |

16 banks
(8 banks for the MB89191/191A)

Memory area

■ I/O MAP

Address	Read/write	Register name	Register description
00$_H$	(R/W)	PDR0	Port 0 data register
01$_H$	(W)	DDR0	Port 0 data direction register
02$_H$	(R/W)	ENI0	Port 0 input enable register
03$_H$ to 07$_H$			Vacancy
08$_H$	(R/W)	STBC	Standby control register
09$_H$	(R/W)	WDTC	Watchdog control register
0A$_H$	(R/W)	TBTC	Time-base timer control register
0B$_H$			Vacancy
0C$_H$	(R/W)	PDR3	Port 3 data register
0D$_H$	(W)	DDR3	Port 3 data direction register
0E$_H$	(R/W)	PDR4	Port 4 data register
0F$_H$	(R/W)	BUZR	Buzzer register
10$_H$ to 13$_H$			Vacancy
14$_H$	(R/W)	RCR1	Remote-control transmit control register 1
15$_H$	(R/W)	RCR2	Remote-control transmit control register 2
16$_H$			Vacancy
17$_H$			Vacancy
18$_H$	(R/W)	T2CR	Timer 2 control register
19$_H$	(R/W)	T1CR	Timer 1 control register
1A$_H$	(R/W)	T2DR	Timer 2 data register
1B$_H$	(R/W)	T1DR	Timer 1 data register
1C$_H$	(R/W)	SMR	Serial mode register
1D$_H$	(R/W)	SDR	Serial data register
1E$_H$			Vacancy
1F$_H$			Vacancy
20$_H$	(R/W)	ADC1	A/D converter control register 1
21$_H$	(R/W)	ADC2	A/D converter control register 2
22$_H$	(R/W)	ADCD	A/D converter data register
23$_H$	(R/W)	EIC1	External interrupt control register 1
24$_H$	(R/W)	EIC2	External interrupt control register 2
25$_H$ to 31$_H$			Vacancy
32$_H$	(R/W)	EIE2	External interrupt 2 enable register
33$_H$	(R/W)	EIF2	External interrupt 2 flag register
34$_H$ to 7B$_H$			Vacancy
7C$_H$	(W)	ILR1	Interrupt level register 1
7D$_H$	(W)	ILR2	Interrupt level register 2
7E$_H$	(W)	ILR3	Interrupt level register 3
7F$_H$			Vacancy

Note: Do not use vacancies.

■ ELECTRICAL CHARACTERISTICS

1. Absolute Maximum Rating

$(AV_{SS} = V_{SS} = 0.0 \text{ V})$

Parameter	Symbol	Value Min.	Value Max.	Unit	Remarks
Power supply voltage	V_{CC}	$V_{SS} - 0.3$	$V_{SS} + 7.0$	V	
	AVR	$V_{SS} - 0.3$	$V_{SS} + 7.0$	V	Must not exceed $V_{CC} + 0.3$ V. MB89190A series only
EPROM program voltage	V_{PP}	$V_{SS} - 0.3$	$V_{SS} + 13.0$	V	MB89P195/P195A only
Input voltage	V_I	$V_{SS} - 0.3$	$V_{CC} + 0.3$	V	
Output voltage	V_O	$V_{SS} - 0.3$	$V_{CC} + 0.3$	V	
"L" level maximum output current	I_{OL1}	—	10	mA	Except P33 and P34 (Except P30 toP34 and P40 to P43 for MB89191AH/193AH)
	I_{OL2}	—	20	mA	P33, P34(P30 toP34 and P40 to P43 for MB89191AH/193AH)
"L" level average output current	I_{OLAV1}	—	4	mA	Except P33 and P34 (Except P30 toP34 and P40 to P43 for MB89191AH/193AH) Average value (operating current × operation rate)
	I_{OLAV2}	—	8	mA	P33 and P34(P30 toP34 and P40 to P43 for MB89191AH/193AH) Average value (operating current × operation rate)
"L" level total average output current	ΣI_{OLAV}	—	20	mA	Average value (operating current × operation rate)
"L" level total maximum output current	ΣI_{OL}	—	100	mA	
"H" level maximum output current	I_{OH1}	—	−10	mA	Except P33, P34, and P37
	I_{OH2}	—	−20	mA	P33, P34, P37
"H" level average output current	I_{OHAV1}	—	−2	mA	Except P33, P34, and P37 Average value (operating current × operation rate)
	I_{OHAV2}	—	−4	mA	Except P33, P34, and P37 Average value (operating current × operation rate)
"H" level total average output current	ΣI_{OHAV}	—	−10	mA	Average value (operating current × operation rate)
"H" level total maximum output current	ΣI_{OH}	—	−30	mA	
Power consumption	P_D	—	200	mW	
Operating temperature	Ta	−40	+85	°C	
Storage temperature	Tstg	−55	+150	°C	

WARNING: Semiconductor devices can be permanently damaged by application of stress (voltage, current, temperature, etc.) in excess of absolute maximum ratings. Do not exceed these ratings.

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2. Recommended Operating Conditions

(Vss = 0.0 V)

Parameter	Symbol	Value		Unit	Remarks
		Min.	Max.		
Power supply voltage	Vcc	2.2*	6.0*	V	Normal operation assurance range* MB89191/191A/191AH/193/193A/193AH/195/195A
		2.7*	6.0*	V	Normal operation assurance range* MB89P195/P195A/PV190/PV190A
		1.5	6.0	V	Retains the RAM state in the stop mode
A/D converter reference input voltage	AVR	0.0	Vcc	V	
Operating temperature	TA	–40	+85	°C	

*: These values vary with the operation frequency and the assured analog operation range. See Figure 1 and " 5. A/D Converter Electrical Characteristics".

Figure 1   Operating Voltage vs. Main Clock Operating Frequency

Note: The shaded area is assured only for the MB89191/191A/191AH/193/193A/193AH/195/195A.
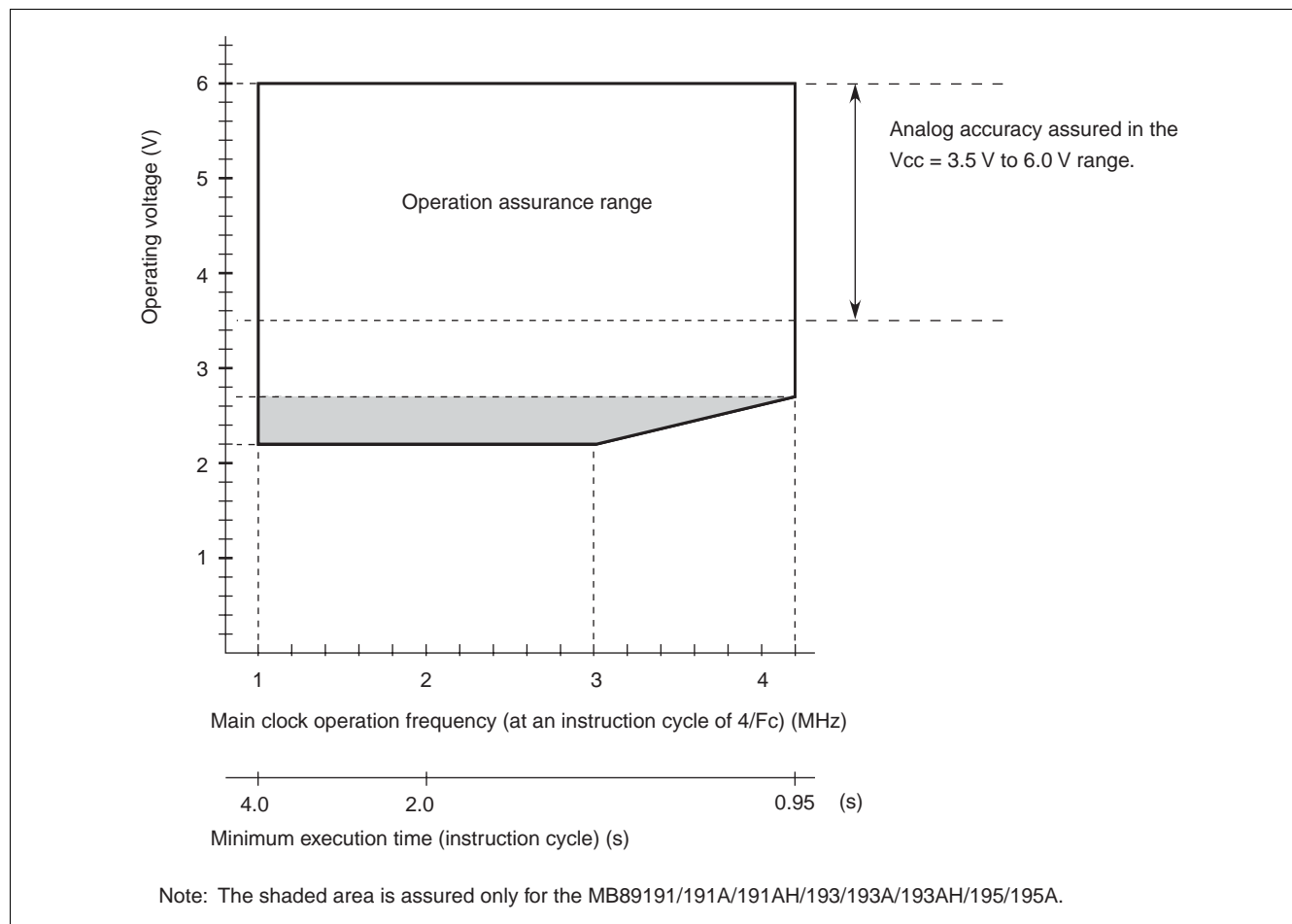
Figure 1 indicates the operating frequency of the external oscillator at an instruction cycle of 4/Fc.

WARNING: The recommended operating conditions are required in order to ensure the normal operation of the semiconductor device. All of the device's electrical characteristics are warranted when the device is operated within these ranges.

Always use semiconductor devices within their recommended operating condition ranges. Operation outside these ranges may adversely affect reliability and could result in device failure.

No warranty is made with respect to uses, operating conditions, or combinations not represented on the data sheet. Users considering application outside the listed conditions are advised to contact their representatives beforehand.

3. DC Characteristics

$(V_{CC} = +5.0 \text{ V}, AV_{SS} = V_{SS} = 0.0 \text{ V}, T_A = -40°C \text{ to } +85°C)$

Parameter	Symbol	Pin	Condition	Value			Unit	Remarks
				Min.	Typ.	Max.		
"H" level input voltage	V_{IH}	P00 to P07, P30 to P37, TEST	—	$0.7 V_{CC}$	—	$V_{CC} + 0.3$	V	
	V_{IHS}	$\overline{RST}$, INT10 to INT12, EC, SCK, SI, INT20 to INT27	—	$0.8 V_{CC}$	—	$V_{CC} + 0.3$	V	
"L" level input voltage	V_{IL}	P00 to P03, P33 to P36, TEST	—	$V_{SS} - 0.3$	—	$0.3 V_{CC}$	V	
	V_{ILS}	$\overline{RST}$, INT10 to INT12, EC, SCK, SI, INT 20 to INT27	—	$V_{SS} - 0.3$	—	$0.2 V_{CC}$	V	
Open-drain output pin applied voltage	V_D	P40 to P44	—	$V_{SS} - 0.3$	—	$V_{SS} + 0.3$	V	
"H" level output voltage	V_{OH1}	P00 to P07, P30 to P32, P35, P36	$I_{OH} = -2.0 \text{ mA}$	2.4	—	—	V	
	V_{OH2}	P33, P34	$I_{OH} = -15 \text{ mA}$	2.4	—	—	V	
	V_{OH3}	P37	$I_{OH} = -7.0 \text{ mA}$	2.4	—	—	V	
"L" level output voltage	V_{OL1}	P00 to P07, P40 to P45, P30 to P32, P35 to P37	$I_{OL} = 1.8 \text{ mA}$	—	—	0.4	V	Except MB89191AH/193AH
		P00 to P07, P35 to P37						MB89191AH/193AH
	V_{OL2}	$\overline{RST}$	$I_{OL} = 4.0 \text{ mA}$	—	—	0.4	V	
	V_{OL3}	P33, P34	$I_{OL} = 12 \text{ mA}$	—	—	0.4	V	Except MB89191AH/193AH
		P30 to P34, P40 to P43						MB89191AH/193AH
Input leakage current(Hi-z output leakage current)	I_{LI1}	P00 to P07, P30 to P37, TEST	$0.0 \text{ V} < V_I < V_{CC}$	—	—	±5	μA	Without pull-up resistor
Open-drain output leakage current (Off state)	I_{LD1}	P40 to P45	$0.0 \text{ V} < V_I < V_{CC}$	—	—	±1	μA	Without pull-up resistor

(Continued)

FUJITSU

(Continued)

$(V_{CC} = 5.0\ V, AV_{SS} = V_{SS} = 0.0\ V, T_A = -40°C\ to\ +85°C)$

Parameter	Symbol	Pin	Condition	Value			Unit	Remarks
				Min.	Typ.	Max.		
Pull-up resistance	R_{PULL}	P00 to P07, P30 to P37, P40 to P45, $\overline{RST}$	$V_I = 0.0\ V$	25	50	100	kΩ	
Power supply voltage*	I_{CC}	V_{CC}	$F_C = 4.2\ MHz$	—	5	10	mA	MB89191/ 191A/193/ 193A/195/ 195A/PV190/ PV190A
				—	7	12	mA	MB89P195/ P195A
	I_{CCS}		$F_C = 4.2\ MHz$	—	3	7	mA	Sleep mode
	I_{CCH}		$T_A = +25\ °C$	—	—	1	µA	Stop mode
	I_{CCA}		$F_C = 4.2\ MHz$ During A/D converter operation	—	6	13	mA	MB89191A/ 193A/195A/ PV190A
				—	8	15	mA	MB89P195A
Input capacitance	C_{IN}	Except AVR, AV_{SS}, V_{CC}, and V_{SS}	$f = 1\ MHz$	—	10	—	pF	

*: For the MB89PV190/PV190A, the current consumption of a connected EPROM and ICE is not included.
The mesurement condition of the power supply current are set as $V_{CC} = 5.0\ V$ with an external clock.

4. AC Characteristics

(1) Reset Timing

$(V_{CC} = +5.0 \text{ V} \pm 10\%, \text{AV}_{SS} = V_{SS} = 0.0 \text{ V}, T_A = -40°C \text{ to } +85°C)$

Parameter	Symbol	Condition	Value		Unit	Remarks
			Min.	Max.		
$\overline{\text{RST}}$ "L" pulse width	t_{ZLZH}	—	16 t_{XCYL}	—	ns	

Note: t_{XCYL} is the oscillation period $(1/F_C)$ input to the X0 pin.
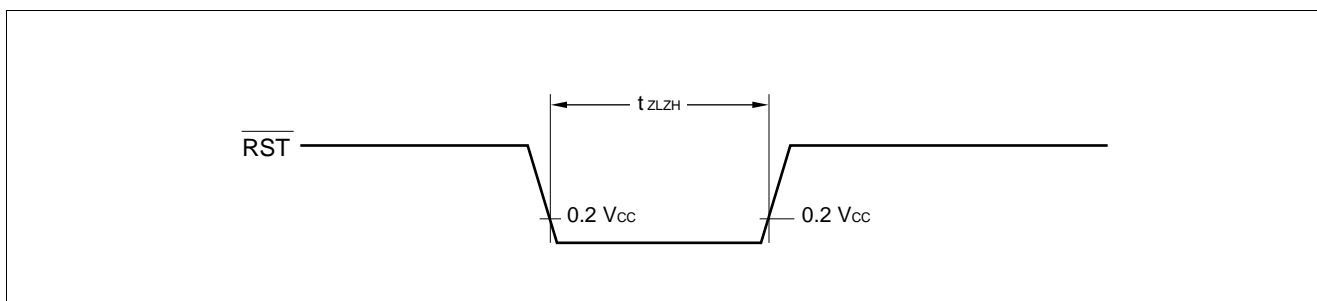
(2) Power-on Reset

$(\text{AV}_{SS} = V_{SS} = 0.0 \text{ V}, T_A = -40°C \text{ to } +85°C)$

Parameter	Symbol	Condition	Value		Unit	Remarks
			Min.	Max.		
Power supply rising time	t_R	—	—	50	ms	
Power supply cut-off time	t_{OFF}		1	—	ms	Due to repeated operations

Note: Make sure that power supply rises within the oscillation stabilization time selected.
    If power supply voltage needs to be varied in the course of operation, a smooth voltage rise is recommended.

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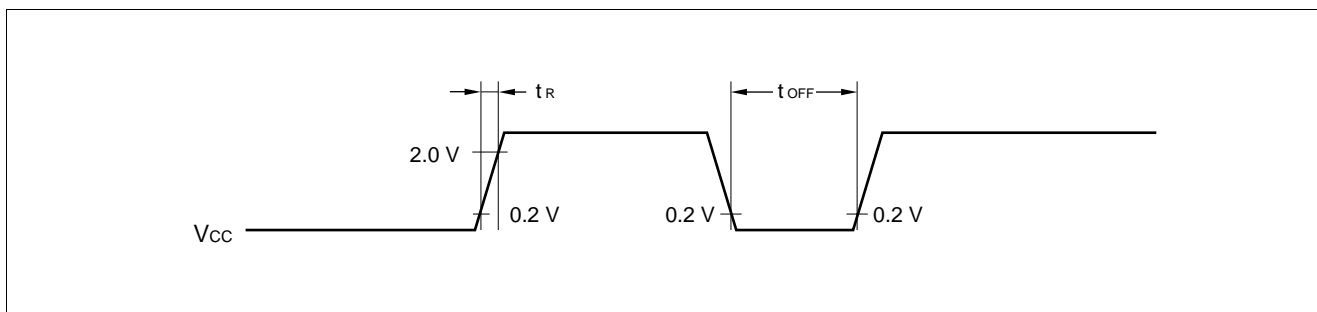
(3) Clock Timings

$(AV_{SS} = V_{SS} = 0.0 \text{ V}, T_A = -40°C \text{ to } +85°C)$

Parameter	Symbol	Pin	Condition	Value		Unit	Remarks
				Min.	Max.		
Clock frequency	F_C	X0, X1	—	1	4.2	MHz	
Clock cycle time	t_{XCYL}	X0, X1	—	238	1000	ns	
Input clock pulse width	P_{WH} P_{WL}	X0	—	20	—	ns	External clock
Input clock pulse risilng/falling time	t_{CR} t_{CF}	X0	—	—	10	ns	External clock

X0, X1 Timings and Conditions of Applied Voltage
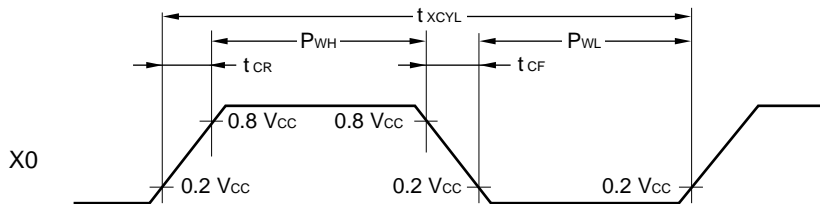
Clock Conditions

When a crystal or ceramic resonator is used

X0   X1

When an external clock is used

X0   X1
Open
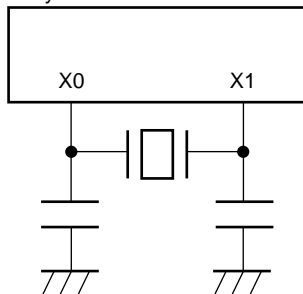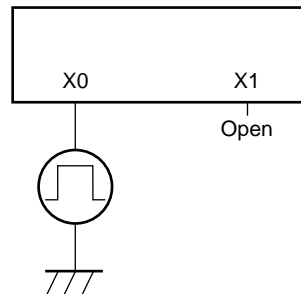
(4) Instruction Cycles

$(AV_{SS} = V_{SS} = 0.0 \text{ V}, T_A = -40°C \text{ to } +85°C)$

Parameter	Symbol	Value (typical)	Unit	Remarks
Instruction cycle (minimum execution time)	t_{inst}	$4/F_C$	µs	$t_{inst} = 0.95$ µs when operating at $F_C = 4.2$ MHz

(5) Serial I/O Timings

$(V_{CC} = 5.0\ V\pm10\%,\ AV_{SS} = V_{SS} = 0.0\ V,\ T_A = -40°C\ to\ +85°C)$

Parameter	Symbol	Pin	Condition	Value Min.	Value Max.	Unit	Remarks
Serial clock cycle time	t_{SCYC}	SCK	Internal clock operation	2 t_{inst}*	—	µs	
SCK ↓ → SO time	t_{SLOV}	SCK, SO		−200	200	ns	
Valid SI → SCK ↑	t_{IVSH}	SI, SCK		1/2 t_{inst}*	—	µs	
SCK ↑ → valid SI hold time	t_{SHIX}	SCK, SI		1/2 t_{inst}*	—	µs	
Serial clock "H" pulse width	t_{SHSL}	SCK	External clock operation	1 t_{inst}*	—	µs	
Serial clock "L" pulse width	t_{SLSH}			1 t_{inst}*	—	µs	
SCK ↓ → SO time	t_{SLOV}	SCK, SO		0	200	ns	
Valid SI → SCK ↑	t_{IVSH}	SI, SCK		1/2 t_{inst}*	—	µs	
SCK ↑ → valid SI hold time	t_{SHIX}	SCK, SI		1/2 t_{inst}*	—	µs	

*: For information on t_{inst}, see "(4) Instruction Cycles".

Internal Shift Clock Mode
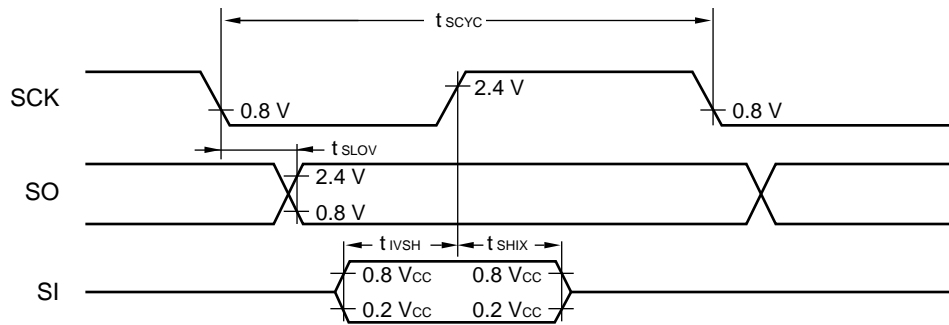
External Shift Clock Mode
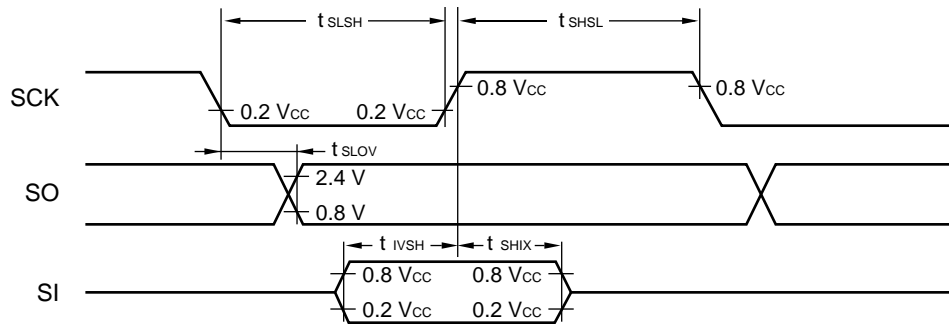
(6) Peripheral Input Timings

$(V_{CC} = 5.0\ V\pm10\%, AV_{SS} = V_{SS} = 0.0\ V, T_A = -40°C\ to\ +85°C)$

Parameter	Symbol	Pin	Value		Unit	Remarks
			Min.	Max.		
Peripheral input "H" pulse width 1	t_{ILIH1}	EC, INT10 to INT12, INT20 to INT27	$2\ t_{inst}$*	—	µs	
Peripheral input "L" pulse width 1	t_{IHIL1}		$2\ t_{inst}$*	—	µs	

*: For information on $t_{inst},$ see "(4) Instruction Cycles".

Peripheral Input Timing Diagram
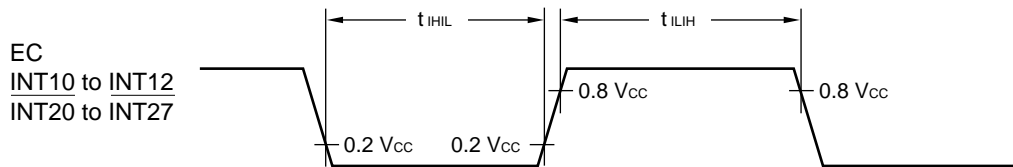
$(V_{CC} = 5.0\ V\pm10\%, AV_{SS} = V_{SS} = 0.0\ V, T_A = -40°C\ to\ +85°C)$

Parameter	Symbol	Pin	Value			Unit	Remarks
			Min.	Typ.	Max.		
Peripheral input "H" noise limit width	t_{IHNC}	EC, SI, SCK, INT10 to INT12	7	15	23	ns	
Peripheral input "L" noise limit width	t_{ILNC}	EC, SI, SCK, INT10 to INT12	7	15	23	ns	

Peripheral Input Timing Diagram
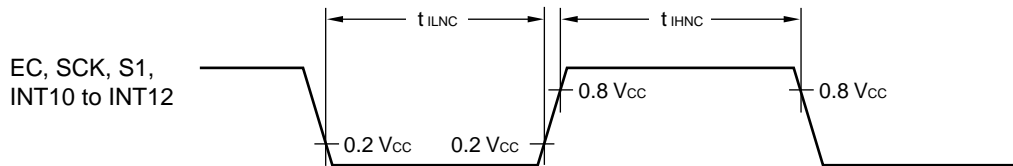
5. A/D Converter Electrical Characteristics (MB89190A Series Only)

$(AV_{CC} = V_{CC} = 3.5\ V\ to\ 6.0\ V, AV_{SS} = V_{SS} = 0.0\ V, T_A = -40°C\ to\ +85°C)$

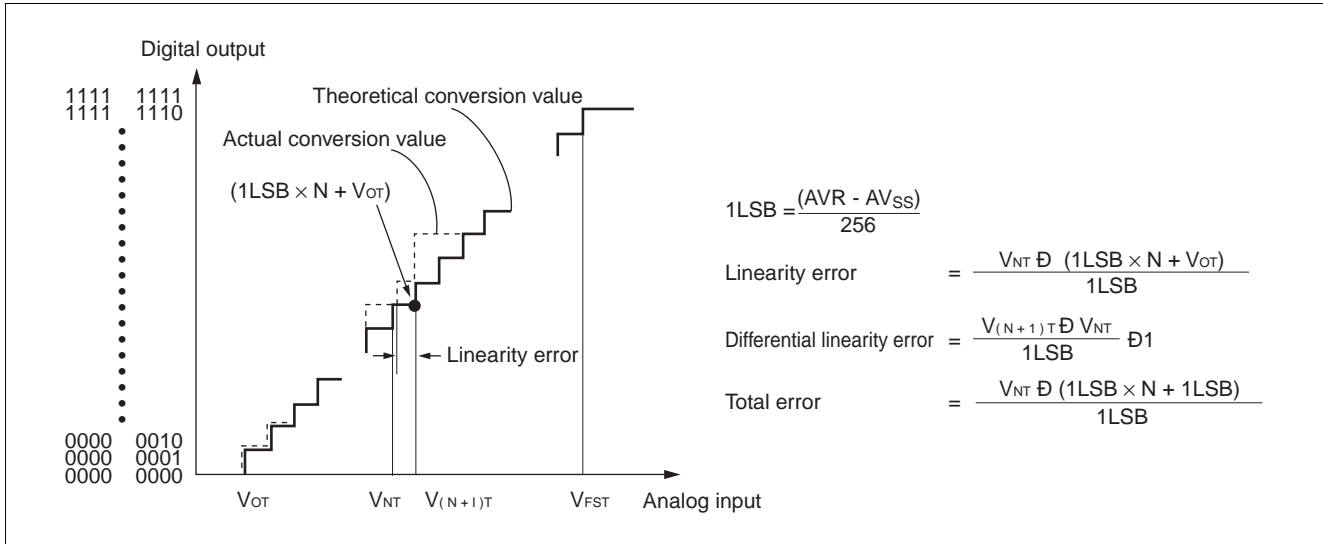| Parameter	Symbol	Pin	Condition	Value Min.	Value Typ.	Value Max.	Unit	Remarks
Resolution	—	—	—	—	—	8	bit	
Total error			AVR = AV_{CC}	—	—	±1.5	LSB	
Linearity error				—	—	±1.0	LSB	
Differential linearity error				—	—	±0.9	LSB	
Zero transition voltage	V_{OT}			AV_{SS} −1.0 LSB	AV_{SS} +0.5 LSB	AV_{SS} +2.0 LSB	V	
Full-scale transition voltage	V_{FST}			AVR −3.0 LSB	AVR −1.5 LSB	AVR	V	
Inter channel disparity				—	—	0.5	LSB	
A/D mode conversion time	—		—	—	44 t_{inst}*	—	µs	
Sense mode conversion time				—	12 t_{inst}*	—	µs	
Analog port input current	I_{AIN}	AN0 to AN7		—	—	10	µA	
Analog input voltage	—			0	—	AVR	V	
Reference voltage		AVR		0	—	V_{CC}	V	
Reference voltage supply current	I_R		AVR = V_{CC} = 5.0 V when A/D conversion is operating	—	100	300	µA	
	I_{RH}			—	—	1	µA	

*: For information on t_{inst}, see "(4) Instruction Cycles" in "4. AC Characteristics".

6. A/D Converter Glossary

- Resolution
  Analog changes that are identifiable by the A/D converter.
  When the number of bits is 8, analog voltage can be divided into 2^8=256.

- Linearity error (unit: LSB)
  The deviation of the straight line connecting the zero transition point ("0000 0000" ↔ "0000 0001") with the full-scale transition point ("1111 1110" ↔ "1111 1111") from actual conversion characteristics.
- Differential linearity error (unit: LSB)
  The deviation of input voltage needed change the output code by 1 LSB from the theoretical value.
- Total error (unit: LSB)
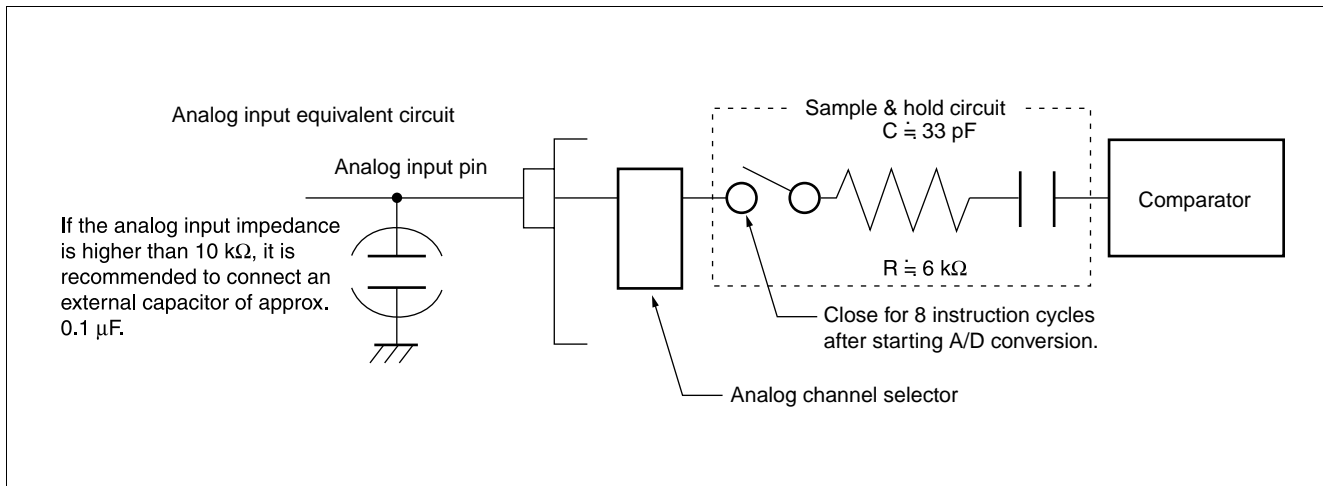 The difference between theoretical and actual conversion values.

The figure shows a graph of Digital output versus Analog input with the following labels:

Digital output axis values:
1111 1111
1111 1110
...
0000 0010
0000 0001
0000 0000

Labels in graph:
- Theoretical conversion value
- Actual conversion value
- $(1\text{LSB} \times N + V_{OT})$
- Linearity error

Analog input axis: V_{OT}, V_{NT}, $V_{(N+I)T}$, V_{FST}

$$1\text{LSB} = \frac{(AVR - AV_{SS})}{256}$$

$$\text{Linearity error} = \frac{V_{NT} - (1\text{LSB} \times N + V_{OT})}{1\text{LSB}}$$

$$\text{Differential linearity error} = \frac{V_{(N+1)T} - V_{NT}}{1\text{LSB}} - 1$$

$$\text{Total error} = \frac{V_{NT} - (1\text{LSB} \times N + 1\text{LSB})}{1\text{LSB}}$$

7. Notes on Using A/D Converter

- **Input impedance of analog input pins**

  The A/D converter used for the MB89190A series contains a sample & hold circuit as illustrated below to fetch analog input voltage into the sample hold capacitor for eight instruction cycles after starting A/D conversion.

  For this reason, if the output impedance of the external circuit for the analog input is high, analog input voltage might not stabilize within the analog input sampling period. Therefore, it is recommended to keep the output impedance of the external circuit low (below 10 kΩ).

  Note that if the impedance cannot be kept low, it is recommended to connect an external capacitor of approx. 0.1 μF for the analog input pin.
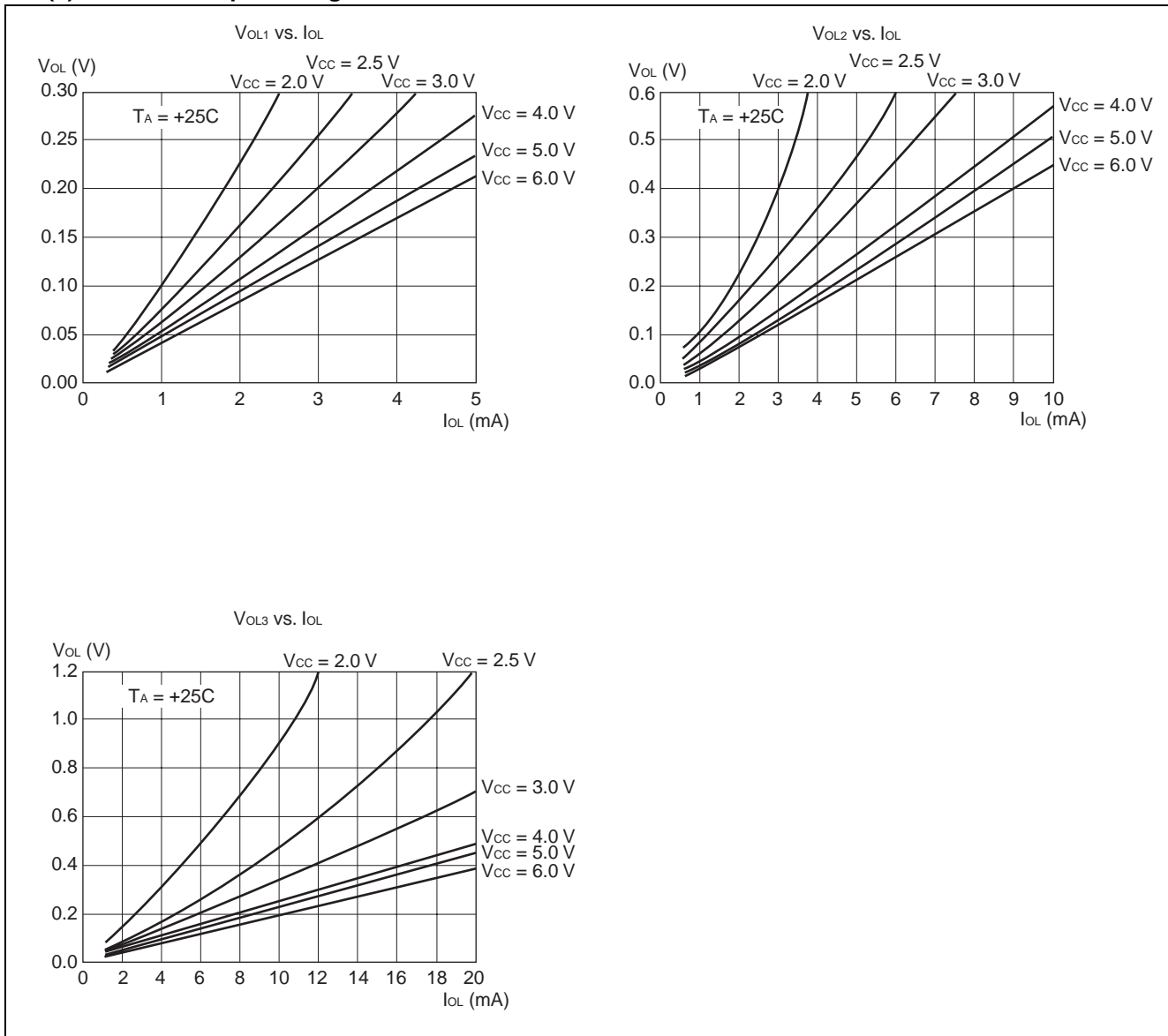

Analog input equivalent circuit diagram with labels:
- Analog input equivalent circuit
- Analog input pin
- If the analog input impedance is higher than 10 kΩ, it is recommended to connect an external capacitor of approx. 0.1 μF.
- Analog channel selector
- Sample & hold circuit
- $C \fallingdotseq 33\ \text{pF}$
- $R \fallingdotseq 6\ \text{k}\Omega$
- Close for 8 instruction cycles after starting A/D conversion.
- Comparator

- **Error**

  The smaller the │AVR-AV_{SS}│, the greater the error would become relatively.
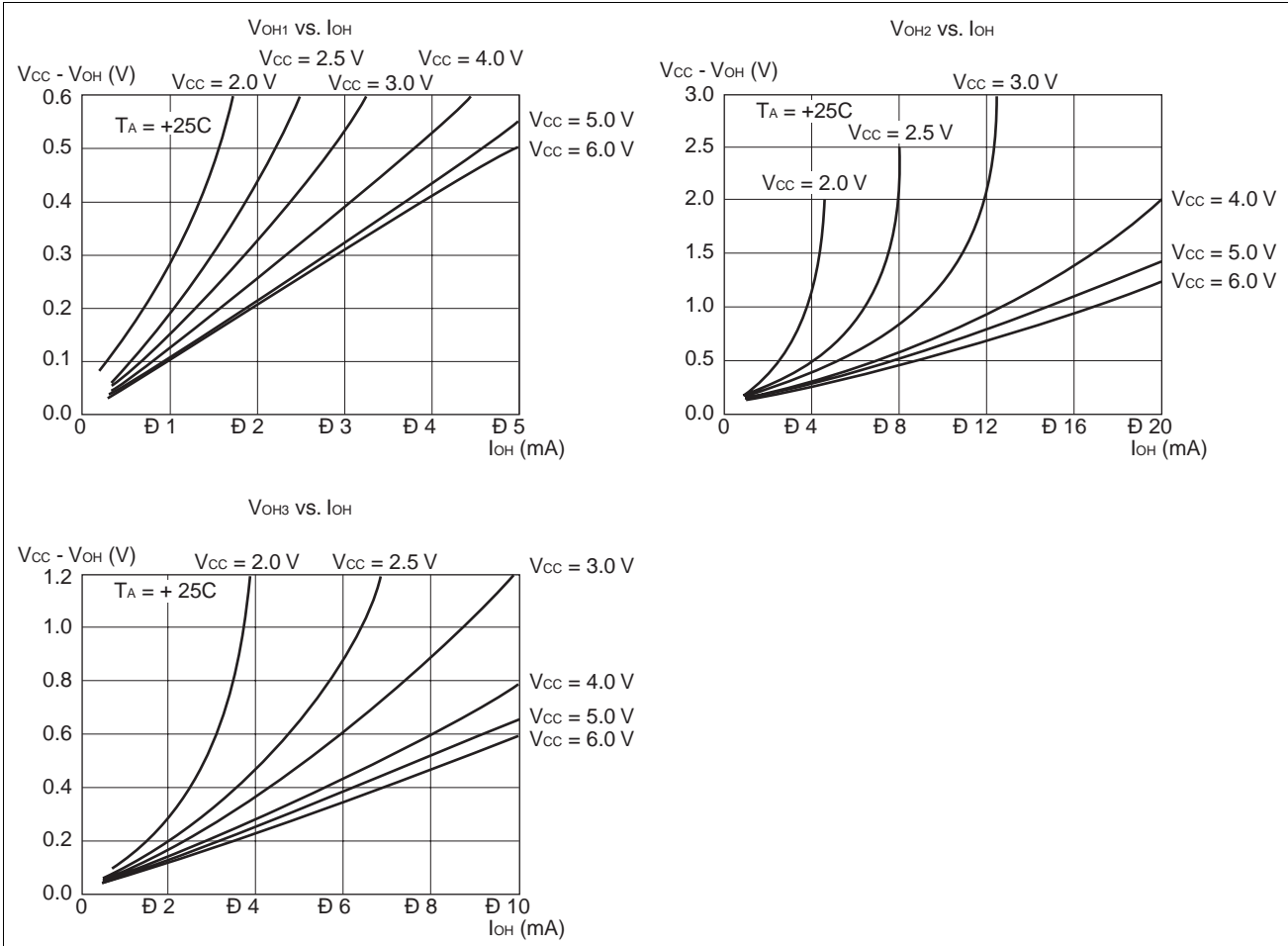
■ EXAMPLE CHARACTERISTICS

(1) "L" Level Output Voltage

(2) "H" Level Output Voltage
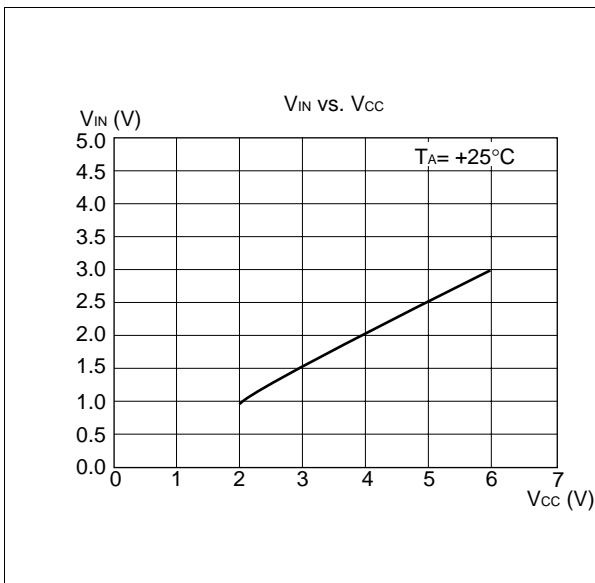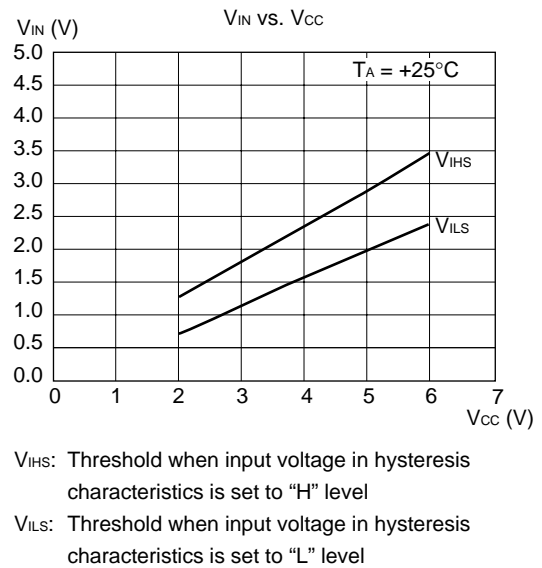

V_{OH1} vs. I_{OH}

V_{CC} - V_{OH} (V)

V_{CC} = 2.5 V V_{CC} = 4.0 V
V_{CC} = 2.0 V V_{CC} = 3.0 V

T_A = +25C

V_{CC} = 5.0 V
V_{CC} = 6.0 V

I_{OH} (mA)

V_{OH2} vs. I_{OH}

V_{CC} - V_{OH} (V)

V_{CC} = 3.0 V

T_A = +25C
V_{CC} = 2.5 V

V_{CC} = 2.0 V

V_{CC} = 4.0 V

V_{CC} = 5.0 V
V_{CC} = 6.0 V

I_{OH} (mA)

V_{OH3} vs. I_{OH}

V_{CC} - V_{OH} (V)

V_{CC} = 2.0 V V_{CC} = 2.5 V V_{CC} = 3.0 V

T_A = + 25C

V_{CC} = 4.0 V
V_{CC} = 5.0 V
V_{CC} = 6.0 V

I_{OH} (mA)

(3) "H" Level Input Voltage/"L" Level Input Voltage (CMOS Input)

V_{IN} (V)

V_{IN} vs. V_{CC}

T_A= +25°C

V_{CC} (V)

(4) "H" Level Input Voltage/"L" Level Input Voltage (Hysteresis Input)

V_{IN} (V) V_{IN} vs. V_{CC}

T_A = +25°C

V_{IHS}

V_{ILS}

V_{CC} (V)

V_{IHS}: Threshold when input voltage in hysteresis characteristics is set to "H" level

V_{ILS}: Threshold when input voltage in hysteresis characteristics is set to "L" level

(5) Power Supply Current (External Clock)

Icc vs. Vcc

Icc (mA)

TA = +25°C

Fc = 4.2 MHz
Fc = 3.0 MHz
Fc = 1.0 MHz

Vcc (V)

Iccs vs. Vcc

Iccs (mA)

TA = +25°C

Fc = 4.2 MHz
Fc = 3.0 MHz
Fc = 1.0 MHz

Vcc (V)

Icch vs. Vcc

Icch (µA)

TA = +25°C

Vcc (V)

IR vs. AVR

IR (µA)

TA = +25°C

AVR (V)
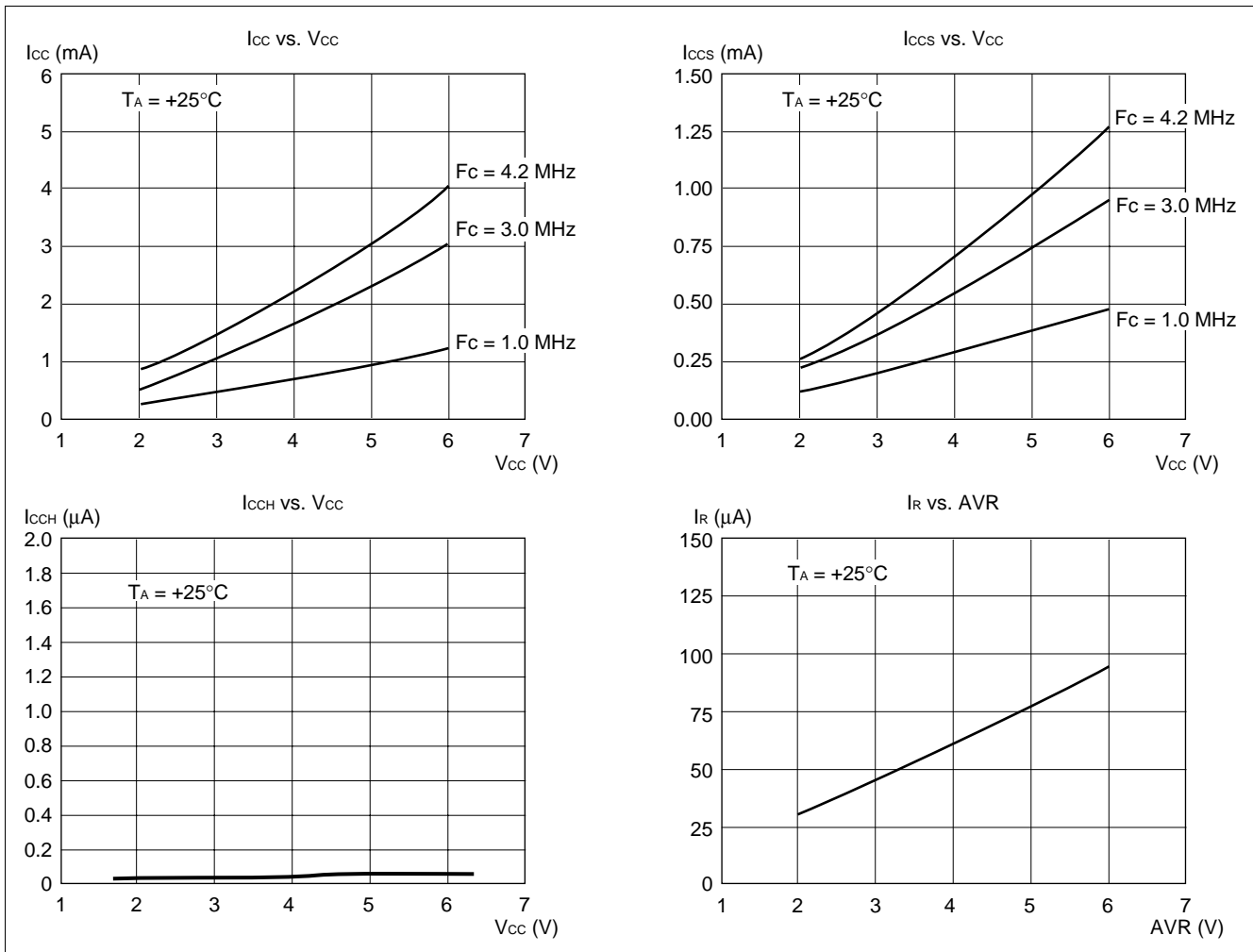
(6) Pull-up Resistance

Rpull vs. Vcc

Rpull (kΩ)

TA = +25°C

Vcc (V)

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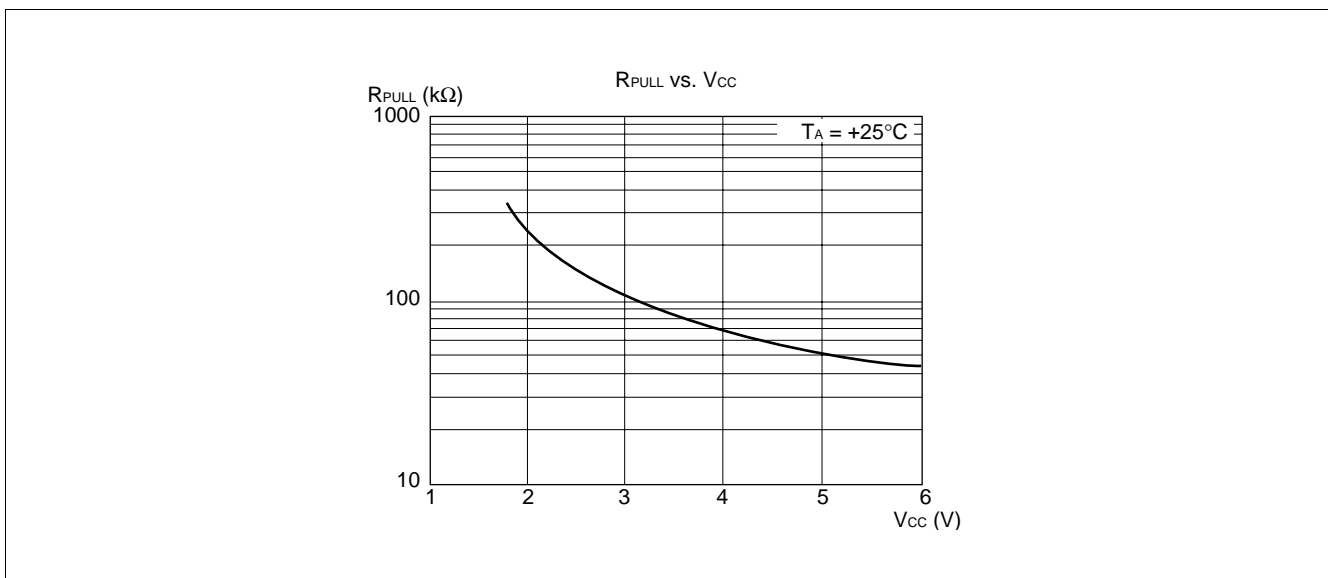
■ **MASK OPTION LIST**

No.	Part number		MB89191 MB89193 MB89195	MB89191A MB89191AH MB89193A MB89193AH MB89195A	MB89P195		MB89P195A		MB89PV190 MB89PV190A
	Specifying procedure		Specify when ordering masking		-101^{*2}	Specify when ordering masking		-201^{*2}	Fixed
1	Port pull-up resistors	P00 to P07 P30 to P37	Selectable by pin		None	Selectable by pin		None	Not available
		P00 to P03 P40 to P45	Selectable by pin	Not available	None	Selectable by pin	Not available	None	Not available
2	Power-on reset • Power-on reset provided • No power-on reset		Selectable		Provided	Provided		Provided	Provided
3	Selection of oscillation stabilization wait time (at 4.2 MHz)[*1] • $2^{18}/F_C$ (approx. 62.4 ms) • $2^{16}/F_C$ (approx. 15.6 ms) • $2^{12}/F_C$ (approx. 0.98 ms) • $2^2/F_C$ (approx. 0 ms)		Selectable		Fixed to $2^{16}/F_C$	Selectable		Fixed to $2^{16}/F_C$	Fixed to $2^{16}/F_C$
4	Reset pin output • Reset output provided • No reset output		Selectable		Provided	Selectable		Provided	Provided
5	Oscillation type of clock 1 Crystal and ceramic oscillators 2 CR		Selectable		"1" only	Selectable		"1" only	"1" only

*1: The oscillation stabilization time is generated by dividing the original clock oscillation. The time described in this item should be used as a rough guideline since the oscillation cycle is unstable immediately after oscillation starts. "F_C" indicates the original oscillation frequency.

*2: −101 and −201 are provided respectively for the MB89P195 and MB89P195A OTP versions as the standard products.

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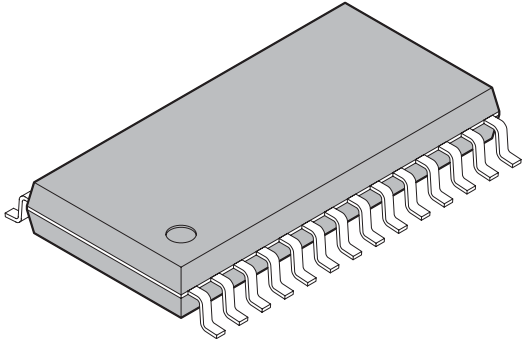
■ ORDERING INFORMATION

Part number	Package	Remarks
MB89191PF MB89193PF MB89195PF MB89P195-101PF MB89191APF MB89191AHPF MB89193APF MB89193AHPF MB89195APF MB89P195A-201PF	28-pin Plastic SOP (FPT-28P-M17)	
MB89191P-SH MB89193P-SH MB89195P-SH MB89191AP-SH MB89191AHP-SH MB89193AP-SH MB89193AHP-SH MB89195AP-SH	28-pin Plastic SH-DIP (DIP-28C-M03)	
MB89191P MB89193P MB89195P MB89P195-101P MB89191AP MB89191AHP MB89193AP MB89193AHP MB89195AP MB89P195A-201P	28-pin Plastic DIP (DIP-28P-M05)	
MB89PV190-101CF MB89PV190A-201CF	48-pin Ceramic MQFP (MQP-48C-P01)	

■ PACKAGE DIMENSION

28-pin plastic SOP	Lead pitch	1.27 mm
	Package width × package length	8.6 × 17.75 mm
	Lead shape	Gullwing
	Sealing method	Plastic mold
	Mounting height	2.80 mm MAX
	Weight	0.82 g
(FPT-28P-M17)	Code (Reference)	P-SOP28-8.6×17.75-1.27

28-pin plastic SOP
(FPT-28P-M17)

Note 1) *1 : These dimensions include resin protrusion.
Note 2) *2 : These dimensions do not include resin protrusion.
Note 3) Pins width and pins thickness include plating thickness.
Note 4) Pins width do not include tie bar cutting remainder.

$*1\ 17.75^{+0.25}_{-0.20}\ (.699^{+.010}_{-.008})$

$0.17^{+0.03}_{-0.04}$
$(.007^{+.001}_{-.002})$

28   15

11.80±0.30
(.465±.012)

INDEX

$*2\ 8.60±0.20$
(.339±.008)

1   14

1.27(.050)

0.47±0.08
(.019±.003)   ⊕ 0.13(.005) Ⓜ

0.10(.004)

"A"

Details of "A" part

2.65±0.15
(.104±.006) (Mounting height)

0.25(.010)

0~8°

0.80±0.20
(.031±.008)
0.88±0.15
(.035±.006)

0.20±0.15
(.008±.006)
(Stand off)

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Dimensions in mm (inches).
Note: The values in parentheses are reference values.

Please confirm the latest Package dimension by following URL.
http://edevice.fujitsu.com/package/en-search/

(Continued)

EIAJ code : SDIP028-P-0400-1

28-pin plastic SH-DIP	Lead pitch	1.778mm(70mil)
	Row spacing	10.16mm(400mil)
	Sealing method	Plastic mold
(DIP-28P-M03)		
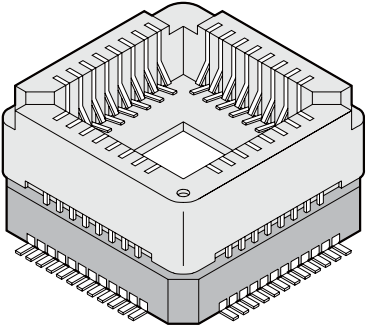
28-pin plastic SH-DIP
(DIP-28P-M03)

$26.00^{+0.20}_{-0.30}$
$\left(1.024^{+.008}_{-.012}\right)$

INDEX-1

INDEX-2

9.10±0.25
(.358±.010)

4.85(.191)MAX

3.00(.118)MIN

0.51(.020)MIN

0.25±0.05
(.010±.002)

0.45±0.10
(.018±.004)

$1.00^{+0.50}_{-0}$
$\left(.039^{+.020}_{-0}\right)$

1.778±0.18
(.070±.007)

15°MAX

10.16(.400)
TYP

1.778(.070)
MAX

23.114(.910)REF

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Dimensions in mm (inches).
Note: The values in parentheses are reference values.

Please confirm the latest Package dimension by following URL.
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(Continued)

48-pin ceramic MQFP	Lead pitch	0.8 mm
	Lead shape	Straight
	Motherboard material	Ceramic
	Mounted package material	Plastic
(MQP-48C-P01)		

48-pin ceramic MQFP
(MQP-48C-P01)

17.20(.677)TYP

15.00±0.25
(.591±.010)

14.82±0.35
(.583±.014)

PIN No.1 INDEX

1.02±0.13
(.040±.005)

10.92 $^{+0.13}_{-0.0}$
$\left(.430\ ^{+.005}_{-0}\right)$

7.14(.281)
TYP

8.71(.343)
TYP

PAD No.1 INDEX

0.30(.012)TYP

4.50(.177)TYP

1.50(.059)TYP

8.80(.346)REF

1.00(.040)TYP

0.80±0.22
(.0315±.0087)

PIN No.1 INDEX

1.10 $^{+0.45}_{-0.25}$
$\left(.043\ ^{+.018}_{-.010}\right)$

0.40±0.08
(.016±.003)

0.60(.024)TYP

8.50(.335)MAX

0.15±0.05
(.006±.002)

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Dimensions in mm (inches).
Note: The values in parentheses are reference values.

Please confirm the latest Package dimension by following URL.
http://edevice.fujitsu.com/package/en-search/

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(Continued)

EIAJ code :∗DIP028-P-0600-2

28-pin plastic DIP	Lead pitch	2.54mm(100mil)
	Row spacing	15.24mm(600mil)
	Sealing method	Plastic mold
(DIP-28P-M05)		

28-pin plastic DIP
(DIP-28P-M05)

$35.73^{+0.20}_{-0.30}$
$\left(1.407^{+.008}_{-.012}\right)$

INDEX-1

INDEX-2

13.80 ± 0.25
$(.543\pm.010)$

$0.99^{+0.50}_{-0}$
$\left(.039^{+.020}_{-0}\right)$

$1.52^{+0.50}_{-0}$
$\left(.060^{+.020}_{-0}\right)$

4.96(.195)MAX

3.00(.118)MIN

0.51(.020)MIN

0.25 ± 0.05
$(.010\pm.002)$

1.58(.062)
MAX

2.54(.100)
TYP

0.46 ± 0.08
$(.018\pm.003)$

15°MAX

15.24(.600)
TYP

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Dimensions in mm (inches).
Note: The values in parentheses are reference values.

Please confirm the latest Package dimension by following URL.
http://edevice.fujitsu.com/package/en-search/

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■ MAIN CHANGES IN THIS EDITION

Page	Section	Change Results
10	■ I/O CIRCUIT TYPE	Added "CMOS input" at the column of "Remarks" in Type B.
14	■ PROGRAMMING TO PROM ON THE MB89P195/P195A	Deleted the "4. EPROM Programmer Socket Adapter".
15	■ PROGRAMMING TO THE EPROM WITH PIGGYBACK/EVALUATION DEVICE	Deleted the "2. Programming Socket Adapter".
27	■ ELECTRICAL CHARACTERISTICS 3. DC Characteristics	Deleted the "(5) Recommended Resonator Manufacturers".
31	■ ELECTRICAL CHARACTERISTICS 4. A/D Converter Electrical Characteristics (MB89190A Series Only)	Changed the unit of "Zero transition voltage" and "Full-scale transition voltage". mV → V
37	■ ORDERING INFORMATION	Changed the ordering information. MB89P195PF-101PF → MB89P195-101PF MB89P195APF-201PF → MB89P195A-201PF MB89P195P-101P → MB89P195-101P MB89P195AP-201P → MB89P195A-201P MB89PV190CF → MB89PV190-101CF MB89PV190ACF→ MB89PV190A-201CF

The vertical lines marked in the left side of the page show the changes.

MEMO

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