

4.1 THERMAL SHOCK: MIL-STD-202, METHOD 107, CONDITION B,

4.3 CORROSION: MIL-STD-202, METHOD 101, CONDITION B
4.4 SHOCK: MIL-STD-202, METHOD 213, CONDITION I
4.5 VIBERATION: MIL-STD-202, METHOD 204, CONDITION D
4.6 MOISTURE RESISTANCE: MIL-STD-202, METHOD 106

5. ALL HOLES PLATED THRU ENTIRE CIRCUIT BOARD STACKUP.
6. HOLE PATTERNS SYMMETRICAL ABOUT CENTER CPW TRACE.

7. FOR OPTIMUM CIRCUIT BOARD HIGH FREQUENCY PERFORMANCE: 7.1 MAINTAIN SOLID GROUND PLANE BELOW HF SUBSTRATE.

7.2 CONTROL PULLBACK OF TRACE AND GROUND FROM BOARD EDGE.

WAVEGUIDE LINE AT 50 MIL INTERVALS ALONG ENTIRE LENGTH.
7.5 IMMERSION GOLD PLATE (ENIG) ALL CONDUCTORS PER IPC-4552.

ROGERS R04003, 8 MIL HIGH FREQUENCY CIRCUIT BOARD SUBSTRATE:

7.3 CONTINUE GROUNDED COPLANAR LINE BEYOND GROUND PADS.7.4 PLACE 16 MIL DIA GROUND VIAS ON BOTH SIDE OF COPLANAR

/8\ REFERENCE DIMENSIONS FOR 50 OHM GROUNDED CPW LINE, USING

CONDUCTOR THICKNESS: 1.4 MIL (INCLUDES PLATING)

4.2 OPERATING TEMPERATURE: -65°C TO 165°C

TRACE WIDTH: 15.5 MILS

GROUND GAPS: 10 MILS

EXCEPT 115°C HIGH TEMP

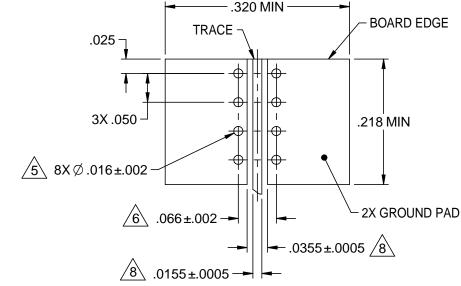
NOTES: UNLESS OTHERWISE SPECIFIED.

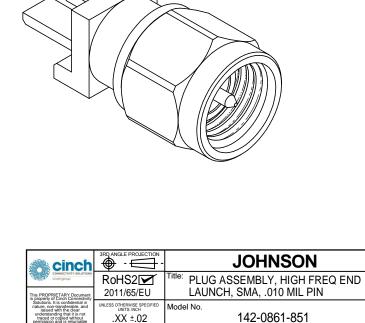
1.3 INSULATOR: PTFE (TEFLON)

1.1 BODY & COUPLING NUT: GOLD PLATED BRASS
1.2 CONTACT: GOLD PLATED BERYLLIUM COPPER

1.4 COUPLING SPRING: BERYLLIUM COPPER UNPLATED

1. MATERIAL AND FINISH:





.XXX ±.005

ANGLES ±2°

DO NOT SCALE

Date: 11/3/2014

Sheet 1 of 1

.499±.020

MOUNTING FOOTPRINT (TOP VIEW, INCLUDING TRACE DIMENSIONS)