



# DESCRIPTION:

The CENTRAL SEMICONDUCTOR CQ92B Series are epoxy molded silicon Triacs designed for full wave AC control applications featuring gate triggering in all four (4) quadrants.

#### MARKING CODE: FULL PART NUMBER

**MAXIMUM RATINGS:** (T<sub>C</sub>=25°C unless otherwise noted)

	SYMBOL	<u>CQ92B</u>	<u>CQ92D</u>	<u>CQ92M</u>	<u>CQ92N</u>	<u>UNITS</u>
Peak Repetitive Off-State Voltage	V <sub>DRM</sub>	200	400	600	800	V
RMS On-State Current (T <sub>C</sub> =50°C)	I <sub>T(RMS)</sub>		1.	C		А
Peak One Cycle Surge (t=10ms)	ITSM		20	)		А
l <sup>2</sup> t Value for Fusing (t=10ms)	l <sup>2</sup> t		2.	C		A <sup>2</sup> s
Peak Gate Power (tp=10µs)	P <sub>GM</sub>		3.	0		W
Average Gate Power Dissipation	P <sub>G</sub> (AV)		0.	2		W
Peak Gate Current (tp=10µs)	IGM		1.	2		А
Peak Gate Voltage (tp=10µs)	V <sub>GM</sub>		8.	C		V
Storage Temperature	T <sub>stg</sub>		-40 to	+150		°C
Junction Temperature	ТЈ		-40 to	+125		°C
Thermal Resistance	$\Theta_{JA}$		18	0		°C/W
Thermal Resistance	$\Theta^{JC}$		9	0		°C/W

# **ELECTRICAL CHARACTERISTICS:** (T<sub>C</sub>=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I <sub>DRM</sub>	Rated V <sub>DRM,</sub> R <sub>GK</sub> =1KΩ		5.0	μA
IDRM	Rated V <sub>DRM,</sub> R <sub>GK</sub> =1KΩ, T <sub>C</sub> =125°C		200	μA
I <sub>GT</sub>	V <sub>D</sub> =12V, QUAD I, II, III, IV		5.0	mA
IН	I <sub>T</sub> =100mA, R <sub>GK</sub> =1K $\Omega$		5.0	mA
V <sub>GT</sub>	V <sub>D</sub> =12V, QUAD I, II, III, IV		2.0	V
$v_{TM}$	I <sub>TM</sub> =1.2A, tp=380µs		1.26	V
dv/dt	V <sub>D</sub> = <sup>2</sup> / <sub>3</sub> V <sub>DRM,</sub> T <sub>C</sub> =125°C	30		V/µs

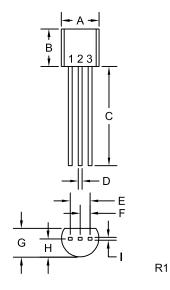
R1 (03-June 2004)



CQ92B CQ92D CQ92M CQ92N

TRIAC 1.0 AMP, 200 THRU 800 VOLTS

TO-92 CASE - MECHANICAL OUTLINE



#### LEAD CODE:

1)	MT1
2)	GATE
3)	MT2

# MARKING CODE: FULL PART NUMBER

DIMENSIONS					
	INCHES		MILLIMETERS		
SYMBOL	MIN	MAX	MIN	MAX	
A (DIA)	0.175	0.205	4.45	5.21	
В	0.170	0.210	4.32	5.33	
С	0.500	-	12.70	-	
D	0.016	0.022	0.41	0.56	
E	0.100		2.54		
F	0.050		1.27		
G	0.125	0.165	3.18	4.19	
Н	0.080	0.105	2.03	2.67	
	0.015		0.38		
TO-92 (REV: R1			EV/· P1)		

TO-92 (REV: R1)

R1 (03-June 2004)

# **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**



#### **PRODUCT SUPPORT**

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options

- Custom bar coding for shipments
- Custom product packing

#### DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

## CONTACT US

#### **Corporate Headquarters & Customer Support Team**

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA Main Tel: (631) 435-1110 Main Fax: (631) 435-1824 Support Team Fax: (631) 435-3388 www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

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PDN01053 8/24/16 2/24/17 8/24/17

Summary: The CQ92 series of TRIACS in the TO-92 case is discontinued and now classified as End of Life (EOL).

Although Central Semiconductor Corp. makes every effort to continue to produce devices that have been proclaimed EOL (End of Life) by various manufacturers, it is an accepted industry practice to discontinue certain devices when customer demand falls below a minimum level of sustainability. Accordingly, the following product(s) have been transitioned to End of Life status as part of Central's Product Management Process. Any replacement product will be noted below. The effective date for placing the last purchase order will be six(6) months from the date of this notice and twelve(12) months from the notice date for final shipments; this may be extended if inventory is available.

Replacement
N/A

Central would be happy to assist you by providing additional information or technical data to help locate an alternate source if we have no replacement available. Please email your requests to engineering@centralsemi.com.