Data Sheet





Description

The ADNS-5000 is a one-chip USB optical mouse sensor for implementing a non-mechanical tracking engine for computer mice.

It is based on optical navigation technology that measures changes in position by optically acquiring sequential surface images (frames) and mathematically determining the direction and magnitude of movement.

The sensor is in a 18-pin optical package that is designed to be used with the ADNS-5100 Round Lens or ADNS-5100-001 Trim Lens, the ADNS-5200 Clip, and the HLMP-ED80-XX000 LED. These parts provide a complete and compact mouse sensor. There are no moving parts, and precision optical alignment is not required, facilitating high volume assembly.

The output format is USB. This device meets USB revision 1.1 specifications and is compatible with USB Revision 2.0 specification.

Default resolution is specified as 500 counts per inch, with rates of motion up to 16 inches per second and 2g acceleration. Resolution can also be programmed to 1000 cpi. Frame rate is varied internally by the sensor to achieve tracking and speed performance, eliminating the need for the use of many registers.

A complete mouse can be built with the addition of a PC board, switches, mechanical Z-wheel, plastic case and cable. A 1% pull up resistor is needed for the USB port to signify a low speed HID device.

Features

- Optical navigation technology
- No mechanical moving parts
- High reliability
- Complete 2-D motion sensor
- High speed motion detection
- Accurate navigation over a wide variety of surfaces
- No precision optical alignment needed
- Wave Solderable
- IEC 60825-1 eye safe under single fault conditions
- Single 5.0 volt power supply
- Meets USB Revision 1.1 Specification and compatible with USB Revision 2.0 specification
- Meets HID Revision 1.1
- On Chip LED Drive with regulated current

Applications

- Mice for desktop PC's, Workstations, and portable PC's
- Trackballs
- Integrated input devices



Theory of Operation

The ADNS-5000 is based on Optical Navigation Technology. It contains an Image Acquisition System (IAS), a Digital Signal Processor (DSP) and USB stream output.

The IAS acquires microscopic surface images via the lens and illumination system provided by the ADNS-5100 Round Lens or ADNS-5100-001 Trim Lens, ADNS-5200, and HLMP-ED80-XX000. These images are processed by the DSP to determine the direction and distance of motion. The DSP generates the Δx and Δy relative displacement values which are converted to USB motion data.

Pinout

Pin	Pin	Description
1	D +	USB D+ line
2	D -	USB D- line
3	ZA	Scroll wheel quadrature input
4	ZB	Scroll wheel quadrature input
5	LGND	LED ground
6	XYLED	XYLED Input
7	VDD5	5 Volt Power (USB VBUS)
8	GND	System ground
9	REG0	3 Volt Power
10	VDD3	3 Volt Power
11	OPT 0	Descriptor Select 1 or B4
12	OPT 1	Descriptor Select 2 or B5
13	GND	System ground
14	OSC_IN	Ceramic resonator input
15	OSC_OUT	Ceramic resonator output
16	B3	Button 3 input (switch to ground)
17	B2	Button 2 input (switch to ground)
18	B1	Button 1 input (switch to ground)

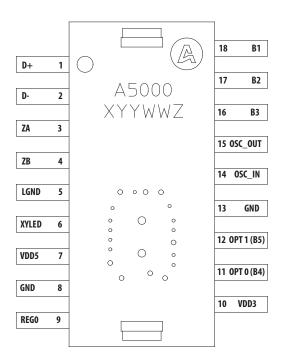


Figure 1. Package outline drawing (top view)

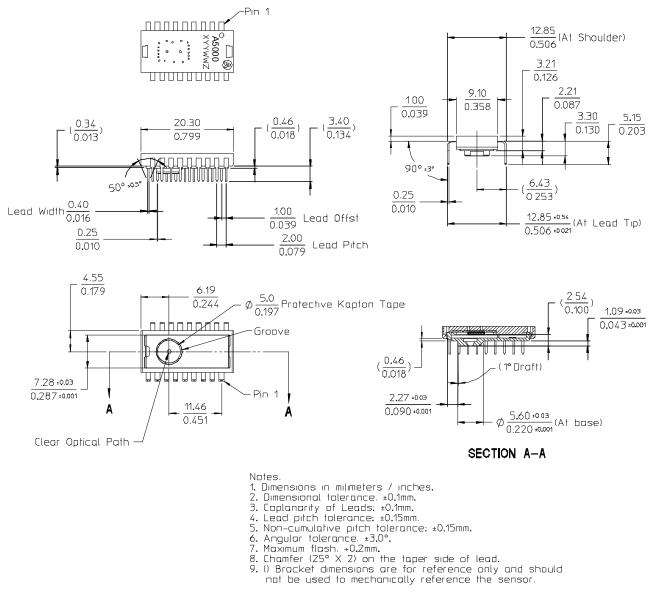


Figure 2. Package outline drawing

CAUTION: It is advised that normal static precautions be taken in handling and assembly of this component to prevent damage and/or degradation which may be induced by ESD.

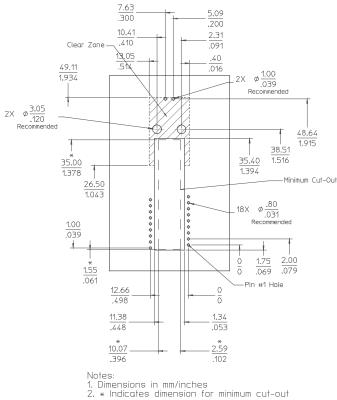


Figure 3. Recommended PCB mechanical cutouts and spacing (Top view)

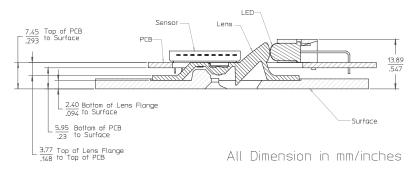


Figure 4. 2D assembly drawing of ADNS-5000

Note: The recommended pin hole dimension of the sensor is 0.7 mm.

Shown with ADNS-5100, ADNS-5200 and HLMP-ED80-XX000

Avago Technologies provides an IGES file drawing describing the base plate molding features for lens and PCB alignment. The components interlock as they are mounted onto defined features on the base plate.

The ADNS-5000 sensor is designed for mounting on a through hole PCB, looking down. The aperture stop and features on the package align it to the lens (See figure 3).

The ADNS-5100 Round lens provides optics for the imaging of the surface as well as illumination of the surface at the optimum angle. Lens features align it to the sensor, base plate, and clip with the LED. The lens also has a large round flange to provide a long creepage path for any ESD events that occur at the opening of the base plate (See figure 4).

The ADNS-5200 clip holds the LED in relation to the lens. The LED must be inserted into the clip and the LED's leads formed prior to loading on the PCB.

The HLMP-ED80-XX000 LED is recommended for illumination. If used with the bin table, sufficient illumination can be guaranteed.

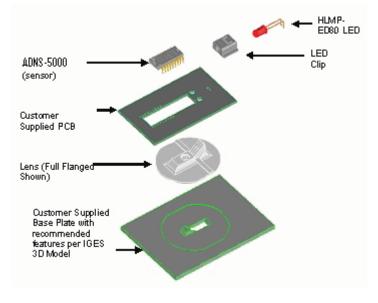


Figure 5. Exploded view drawing

Block Diagram

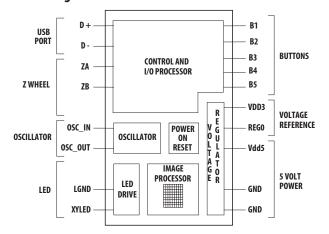


Figure 6. Block Diagram

PCB Assembly Considerations

- 1. Insert the sensor and all other electrical components into PCB.
- 2. Bend the LED leads 90 degrees and then insert the Led into the assembly clip until the snap feature locks the Led base.
- 3. Insert the LED/clip assembly into PCB.
- 4. Wave solder the entire assembly in a no-wash solder process utilizing solder fixture. The solder fixture is needed to protect the sensor during the solder process. The fixture should be designed to expose the sensor leads to solder while shielding the optical aperture from direct solder contact.
- 5. Place the lens onto the base plate.
- 6. Remove the protective Kapton tape from optical aperture of the sensor. Care must be taken to keep contaminants from entering the aperture. Recommend not placing the PCB facing up during the entire mouse assembly process. Recommend to hold the PCB first vertically for the Kapton removal process.
- 7. Insert PCB assembly over the lens onto base plate aligning post to retain PCB assembly. The sensor aperture ring should self-align to the lens.
- 8. The optical position reference for the PCB is set by the base plate and lens. Note that the PCB motion due to button presses must be minimized to maintain optical alignment.
- 9. Install mouse top case.

Lens/Light Pipe

Surface

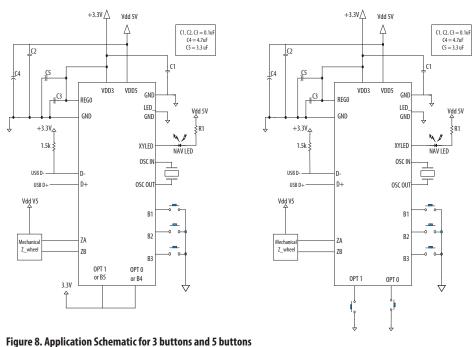
Figure 7. Typical Application

Design considerations for improving ESD Performance

The table below shows typical values assuming base plate construction per the Avago Technologies supplied IGES file and ADNS-5100 Round lens.

Typical distance	A5100	A5100-001
Creepage	40.5mm	17.9mm
Clearance	32.6mm	9.2mm

Typical Application



R1 value (ohm)	LED Bin
59.0	K
59.0	L
59.0	М
59.0	Ν
59.0 to 66.5	Р
59.0 to 78.7	Q
59.0 to 93.1	R
59.0 to 110	S
59.0 to 143	Т

Notes on bypass capacitors:

- All caps (except C4) MUST be as close to the sensor pins as possible.
- Caps should be ceramic.
- Caps should have less than 5 nH of self inductance
- Caps connected to VDD3 MUST have less than 0.2 Ω ESR
- $1.5k\Omega$ resistor should be $\pm 1\%$ tolerance.
- Z-wheel connections are detailed in Figure 20
- Buttons B1-B5 can be used as button or VID/PID straps (see strap table on page 14). For VID/PID connections, parts must be connected to Vdd3 on 'high' connection, preferably near pin 10

Surface mount parts are recommended

Regulatory Requirements

- Passes FCC B and worldwide analogous emission limits when assembled into a mouse with unshielded cable and following Avago Technologies recommendations.
- Passes EN61000-4-4/IEC801-4 EFT tests when assembled into a mouse with shielded cable and following Avago Technologies recommendations.
- UL flammability level UL94 V-0.
- Provides sufficient ESD creepage/clearance distance to avoid discharge up to 15kV when assembled into a mouse according to usage instructions above.

Absolute Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Units	Notes
Storage Temperature	Ts	-40	85	°C	
Operating Temperature	T _A	-15	55	°C	
Lead Solder Temp			260	°C	For 10 seconds, 1.6mm below seating plane.
Supply Voltage	V _{DD}	-0.5	5.5	V	
ESD			2	kV	All pins, human body model MIL 883 Method 3015
Input Voltage	V _{IN}	-0.5	V _{DD} +0.5	V	All I/O pins except OSC_IN and OSC_OUT, D+, D-
Input Voltage	V _{IN}	-1.0	4.6	V	D+, D-, AC waveform, see USB specification (7.1.1)
Input Voltage	V _{IN}	-0.5	3.6	V	OSC_IN and OSC_OUT
Input Short Circuit Voltage	V _{SC}	0	V _{DD}	V	D+, D-, see USB specification (7.1.1)

Recommended Operating Conditions

Parameter	Symbol	Minimum	Typical	Maximum	Units	Notes
Operating Temperature	T _A	0		40	°C	
Power supply voltage	V _{DD}	4.0	5.0	5.25	Volts	For accurate navigation and proper USB operation
Power supply voltage	V _{dd}	3.8	5.0	5.25	Volts	Maintains communication to USB host and internal register contents.
Power supply rise time	V _{RT}	0.1		100	ms	
Supply noise	V _N			100	mV	Peak to peak within 0-100 MHz bandwidth
Velocity	Vel			16	ips	
Acceleration	Acc			2	G	
Clock Frequency	f _{clk}	23.64	24	24.36	MHz	Due to USB timing constraints
Resonator Impedance	X _{RES}			55	Ω	
Distance from lens refer- ence plane to surface	Z	2.3	2.4	2.5	mm	See Figure 9
Light Level onto IC	IRR _{INC}	80 100		25,000 30,000	mW/m ²	=639nm =875nm

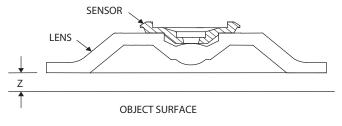


Figure 9. Distance from lens reference plane to object surface

AC Electrical Specifications

Parameter	Symbol	Min.	Тур.	Max.	Units	Notes
Power up delay	T _{PUP}			50	ms	
Debounce delay on button inputs	T _{DBB}	5	9	17	ms	"Maximum" specified at 8ms polling rate.
Mechanical Z-Wheel						Internally pulled down with 20k resistors and debounced
Transient Supply Current	I _{DDT}			60	mA	Max. supply current during a VDD ramp from 0 to 5.0 V with > 500 s rise time. Does not include charging currents for bypass capacitors.
Input Capacitance (OSC Pins)	C _{OSC_IN}		50		pF	OCS_IN, OSC_OUT to GND

Electrical Characteristics over recommended operating conditions. Typical values at 25 °C, V_{DD}=5.0 V, 24MHz

USB Electrical Specifications

Electrical Characteristics over recommended operating conditions.

Parameter	Symbol	Min.	Max.	Units	Notes
Output Signal Crossover Voltage	V _{CRS}	1.3	2.0	V	C _L = 200 to 600 pF (see Figure 10)
Input Signal Crossover Voltage	VICRS	1.2	2.1	V	C _L = 200 to 600 pF (see Figure 10)
Output High	V _{OH}	2.8	3.6	V	with 15 kohm to Ground and 7.5 k to Vbus on D- (see Figure 11)
Output Low	V _{OL}	0.0	0.3	V	with 15 kohm to Ground and 7.5 k to Vbus on D- (see Figure 11)
Single Ended Output	V _{SE0}		0.8	V	
Input High (Driven)	VI _H	2.0		V	
Input High (Floating)	V _{IHZ}	2.7	3.6	V	
Input Low	V _{IL}		0.8	V	7.5k to Vdd5
Differential Input Sensitivity	V _{DI}	0.2		V	(D+)-(D-) See Figure 12
Differential Input Common Mode Range	V _{CM}	0.8	2.5	V	Includes V _{DI} , See Figure 12
Single Ended Receiver Threshold	V _{SE}	0.8	2.0	V	
Transceiver Input Capacitance	C _{IN}		12	pF	D+ to V _{BUS} , D- to V _{BUS}

USB Timing Specifications

Timing Specifications over recommended operating conditions.

Parameter	Symbol	Min.	Max.	Units	Notes
D+/D-Transition rise time	T _{LR}	75		ns	C _L = 200 pF (10% to 90%), see Figure 10
D+/D-Transition rise time	T _{LR}		300	ns	$C_L = 600 \text{ pF}$ (10% to 90%), see Figure 10
D+/D-Transition fall time	T _{LF}	75		ns	C _L = 200 pF (90% to 10%), see Figure 10
D+/D-Transition fall time	T _{LF}		300	ns	$C_L = 600 \text{ pF}$ (90% to 10%), see Figure 10
Rise and Fall time matching	T _{LRFM}	80	125	%	T_R/T_F ; $C_L = 200 \text{ pF}$; Excluding the first transition from the Idle State
Wakeup delay from USB suspend mode due to buttons push	T _{WUPB}		17	ms	Delay from button push to USB operation Only required if remote wakeup enabled
Wakeup delay from USB suspend mode due to buttons push until accurate navigation	T _{WUPN}		50	ms	Delay from button push to navigation operation Only required if remote wakeup enabled
USB reset time	T _{reset}	18.7		S	
Data Rate	t _{LDRATE}	1.4775	1.5225	Mb/s	Average bit rate, 1.5 Mb/s +/- 1.5%
Receiver Jitter Tolerance	t _{DJR1}	-75	75	ns	To next transition, see Figure 13
Receiver Jitter Tolerance	t _{DJR2}	-45	45	ns	For paired transitions, see Figure 13
Differential to EOP Transition Skew	t _{LDEOP}	-40	100	ns	See Figure 14
EOP Width at Receiver	t _{LEOPR}	670		ns	Accepts EOP, see Figure 14
Source EOP Width	t _{LEOPT}	1.25	1.50	S	
Width of SE0 interval during Differential Transition	t _{LST}		210	ns	See Figure 11.
Differential Output Jitter	t _{UDJ1}	-95	95	ns	To next transition, see Figure 15
Differential Output Jitter	tudiz	-150	150	ns	For paired transitions, see Figure 15

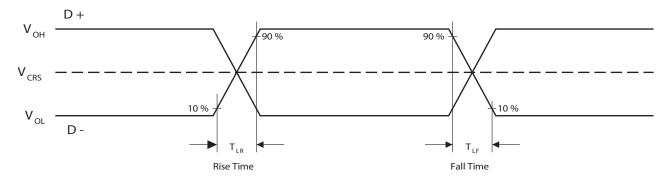


Figure 10. Data Signal Rise and Fall Times

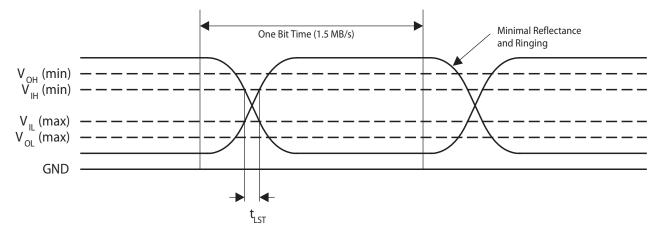
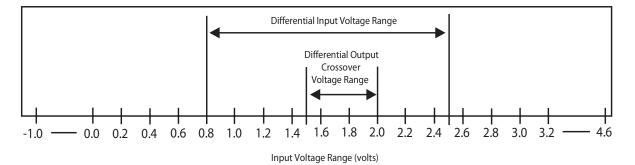
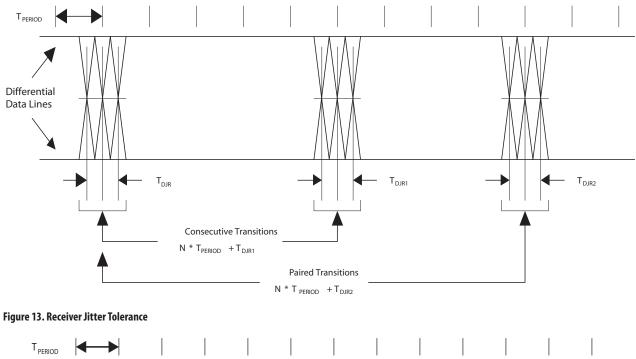


Figure 11. Data Signal Voltage Levels







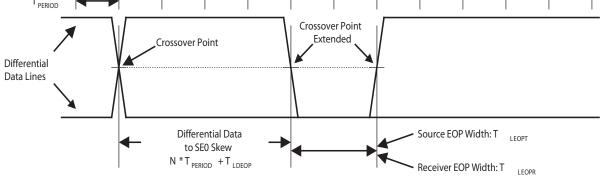


Figure 14. Differential to EOP Transition Skew and EOP Width

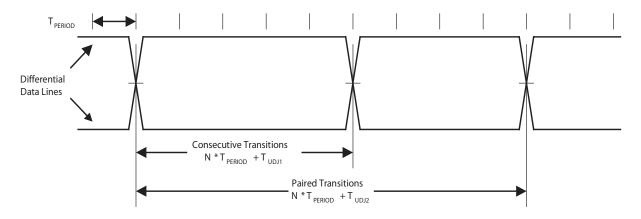


Figure 15. Differential Output Jitter

DC Electrical Specifications

Parameter	Symbol	Minimum	Typical	Maximum	Units	Notes
Supply current (Sensor only), mouse moving	I _{DDS}		7.2		mA	No load on B1-B3, Z-LED, XYLED ZA, ZB, D+, D-
Supply current (Sensor only), mouse not moving	I _{DDSN}		6.2		mA	No load on B1-B3, Z-LED, XYLED ZA, ZB, D+, D-
Supply current, USB suspend mode	I _{DDSS}			250	μΑ	No load on B1-B3, Z-LED, XYLED ZA, ZB, D+, D-
XYLED current	I _{LED}			30	mA	
XYLED Output Low Voltage	V _{OL}			1.1	V	Refer to Figure 16
Input Low Voltage	V _{IL}			0.5	V	Pins: ZA, ZB, B1, B2, B3, V_{IL} max of 0.5V _{DC} is at V _{DD} min of $4V_{DC}$, with a typical of 0.8V _{DC} at V _{DD} of 5V _{DC}
Input High Voltage	V _{IH}	0.6*V _{DD}			V	Pins: ZA, ZB, B1, B2, B3
Input Hysteresis	V _{HYST}		285		mV	Pins: ZA, B1, B2,
Input Hysteresis	V _{HYST}		200		mV	Pins: ZB
Button Pull Up Current	BIOUT	125	275	500	А	Pins: B1, B2, B3

Electrical Characteristics over recommended operating conditions. Typical values at 25 °C, V_{DD}=5.0 V, 24MHz

Typical Performance Characteristics

Performance Characteristics over recommended operating conditions. Typical values at 25 °C, V_{DD}=5.0 V, 24MHz

Parameter	Symbol	Minimum	Typical	Maximum	Units	Notes
Path Error (Deviation)	P _{Error}		0.5		%	Average path error as percent of total 2.5" travel on various standard surfaces

Typical Performance Characteristics

Performance Characteristics over recommended operating conditions. Typical values at 25 °C, V_{DD} =5.0 V, 24MHz

White Paper

-Black Formica

0.2

Z-Height(mm)

0.4

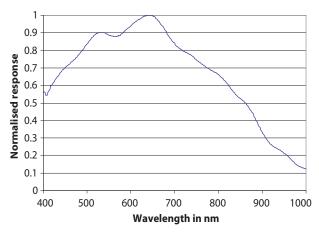
0.6

0.8 1

Manila Black Copy

-0.2 0

The following graphs are the typical performance of the ADNS-5000 sensor, assembled as shown in the 2D assembly drawing with the ADNS-5100 Round Lens/Prism, the ADNS-5200 clip, and the HLMP-ED80-XX000 LED.





-0.4

Figure 17. Wavelength responsivity.^[1] (Comparative Surfaces)

Notes:

1200

1000

800

600

400

200

0

-0.8 -0.6

Resolution(DPI)

- 1. The ADNS-5000 is designed for optimal performance when used with the HLMP-ED80-XX000 (Red LED 639nm).
- 2. Z = distance from Lens Reference Plane to Surface.
- 3. DOF = Depth of Field

Configuration after Power up (Data Values)

Signal Function	State from Figure 9-1 of USB spec: Powered or Default Address or Configured	State from Figure 9-1 of USB spec: Suspended from any other state
B1	Hi-Z if tied to VDD3 else pullup active	Hi-Z if tied to VDD3 else pullup active
B2	Hi-Z if tied to VDD3 else pullup active	Hi-Z if tied to VDD3 else pullup active
B3	Hi-Z if tied to VDD3 else pullup active	Hi-Z if tied to VDD3 else pullup active
B4	Hi-Z if tied to VDD3 else pullup active	Hi-Z if tied to VDD3 else pullup active
B5	Hi-Z if tied to VDD3 else pullup active	Hi-Z if tied to VDD3 else pullup active
D-	USB I/O	Hi-Z input
D+	USB I/O	Hi-Z input
OSC_IN	24MHz	pulled low
OSC_OUT	24MHz	
XYLED	low (on) or pulsing	Pulled high (off)
ZB/Z_LED	Hi-Z input	Hi-Z input
ZA	Hi-Z if ZA tied to GND	Hi-Z input

Strap (Jumper) Table

The PID/string strap matrix is the following:

Mouse type	VID	PID	Manuf str.	Product string	B1	B2	B3	OPT 0	OPT 1	ZA	ZB
3-button mse	0x192F	0x0116		"USB Optical Mouse"	sw1	sw2	sw3	Vdd3	Vdd3	mech Z-wheel	mech Z-wheel
5-button mse	0x192F	0x0216		"USB Optical Mouse"	sw1	sw2	sw3	sw4	sw5	mech Z-wheel	mech Z-wheel

X & Y Directions

(Looking through an ADNS-5100 Lens)

The positive and negative X and Y directions with respect to the mouse case are shown in the diagram below.

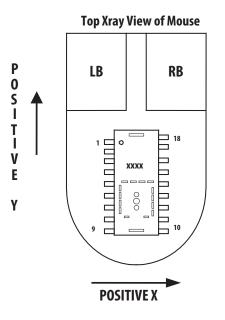


Figure 18. Directions are for a complete mouse, with the ADNS-5100 lens

XY LED

• The peak current values are 30 mA if R1 590hm and the part meets the IEC 825-1 eye safety regulations.

Buttons

The minimum time between button presses is T_{DBB} . Buttons B1 through B3 are connected to a Schmidt trigger input with 100 uA current sources pulling up to +5 volts during normal, sleep and USB suspend modes.

Z-Wheel

The mechanical Z-Wheel connections (A,B) are determined below.

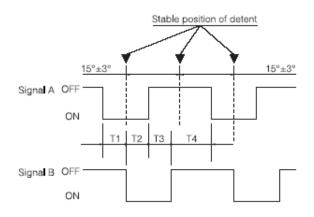


Figure 19. Z-Wheel A and B connections

Notes:

For mechanical Z-wheels the following must be implemented:

- Use a rotary switch equivalent to the Panasonic part EVQVX at http://industrial.panasonic.com/www-data/pdf/ATC0000/ATC0000CE20.pdf (The key point is stable "A" switch state in all detent positions).
- Solder the rotary switch into the PCB such that the common pin is closest to the cable end of the mouse. (Metal plate faces to left)
- Connect the "A" terminal of the rotary switch to "ZA" and the "B" terminal to "ZB". ZA MUST be connected to "Signal A" in Figure 19 where the z-wheel detents are mechanically stable.

USB Commands

Mnemonic	Command	Notes
USB_RESET	D+/D- low > 18.6 us	Device Resets; Address=0
USB_SUSPEND	Idle state > 3mS	Device enters USB low-power mode
USB_RESUME	Non-idle state	Device exits USB low-power mode
Get_Status_Device	80 00 00 00 00 02 00 Normally returns 00 00, Self powere Remote wakeup 02 00	
Get_Status_Interface	81 00 00 00 00 00 02 00	Normally returns 00 00
Get_Status_Endpt0	82 00 00 00 xx 00 02 00	OUT: xx=00, IN: xx=80Normally returns 00 00
Get_Status_Endpt1	82 00 00 00 81 00 02 00	Normally returns 00 00, Halt 00 01
Get_Configuration	80 08 00 00 00 00 01 00	Return: 00=not config., 01=configured
Get_Interface	81 0A 00 00 00 00 01 00	Normally returns 00
Get_Protocol	A1 03 00 00 00 00 01 00	Normally returns 01, Boot protocol 00
Get_Desc_Device	80 06 00 01 00 00 nn 00	See USB command details
Get_Desc_Config	80 06 00 02 00 00 nn 00	See USB command details
Get_Desc_String	80 06 xx 03 00 00 nn 00	See USB command details
Get_Desc_HID	81 06 00 21 00 00 09 00	See USB command details
Get_Desc_HID_Report	81 06 00 22 00 00 nn 00	See USB command details
Get_HID_Input	A1 01 00 01 00 00 nn 00	Return depends on motion & config
Get_ldle	A1 02 00 00 00 00 01 00	Returns rate in multiples of 4ms
Get_Vendor_Test	C0 01 00 00 xx 00 01 00	Read register xx
Set_Address	00 05 xx 00 00 00 00 00	xx = address
Set_Configuration	00 09 xx 00 00 00 00 00	Not configured: xx=00Configured: xx=01
Set_Interface	01 0B 00 00 00 00 00 00	Only one interface supported
Set_Protocol	21 0B xx 00 00 00 00 00	Boot: xx=00, Report: xx=01
Set_Feature_Device	00 03 01 00 00 00 00 00	Enable remote wakeup
Set_Feature_Endpt0	02 03 00 00 xx 00 00 00	Halt. OUT: xx=00, IN: xx=80
Set_Feature_Endpt1	02 03 00 00 81 00 00 00	Halt
Clear_Feature_Device	00 01 01 00 00 00 00 00	Disable Remote wakeup
Clear_Feature_Endpt0	02 01 00 00 xx 00 00 00	Clear Halt; OUT: xx=00, IN: xx=80
Clear_Feature_Endpt1	02 01 00 00 81 00 00 00	Clear Halt
Set_Idle	21 0A 00 rr 00 00 00 00	rr = report rate in multiples of 4ms
Set_Vendor_Test	40 01 00 00 xx yy 00 00	Write yy to address xx
Poll_Endpt1		Read buttons, motion, & Z-wheel
Poll_Endpt1		Read buttons, motion, & Z-wheel

Note:

The last two bytes in a command shown as "nn 00" specify the 16-bit data size in the order of "LowByte HighByte." For example a two-byte data size would be specifed as "02 00." ADNS-5000 will not provide more bytes than the number requested in the command, but it will only supply up to a maximum of 8 bytes at a time. The ADNS-5000 will re-send the last packet if the transfer is not acknowledged properly.

USB COMMAND DETAILS

USB_RESET		D+/D- low for	r an extended period
	USB Spec:		A device may reset after seeing an SEO for more than 18.6 uS, and
	Notor		definitely after 10mS.
	Notes:		After power up and prior to Reset, the device will not respond to any USB commands. After the device has been given a USB
			Reset, the device's address will be reset to zero and the
			device will be in the Default state. The chip will default
			to Report protocol and any pending output will be flushed.
USB_SUSPEN	D USB Spec:		Idle state for an extended period
	USD Spec.		A device may suspend after seeing an idle for more than 3mS, and definitely after 10mS.
	Notes:		The chip will take a minimum of 5mS to start Suspend, though
			will definitely start after 6mS. The chip may finish the current
			frame if necessary before stopping the clock. Thus, an additional
			frame time may be used to reach Suspend mode.
		New Sille state	
USB_RESUM	LUSB Spec:	Non-idle state	Remote Resume signalling from a device must be between 1mS and 15mS.
			The host is required to send Resume signaling for 20mS plus 10mS
			of resume recovery time in which it does not access any devices.
			This allows devices enough time to wake back up.
	Notes:		The chip can cause a Resume if
			Remote Wakeup is enabled and a button has been pressed.
			Remote resume signalling from the chip will last 11.45mS to 12.45mS.
Get_Status_	Device		
			80 00 00 00 00 02 00
	Returns:		хх уу
			xx[0] = Self Powered
			xx[1] = Remote Wakeup
			xx[7:2] = 0 yy = 00 (Reserved)
	Default:		Accept (undefined in USB Spec)
	Addressed:		Accept
	Configured:	Accept	
	Notes:		Use Set_Feature_Device/Clear_Feature_Device to set/clear remote wakeup.
	Interface		
			81 00 00 00 00 02 00
	Returns:		00 00
	Default:		Stall (undefined in USB Spec)
	Addressed:	A	Stall
	Configured:	Accept	Path raturn butes are recorded and surrantly 00
	Notes:		Both return bytes are reserved and currently 00.
Get_Status_	Endpt0		82 00 00 00 xx 00 02 00
_			82 00 00 00 00 02 00
			82 00 00 00 80 00 02 00
			xx = 00 = Endpt0 0UT
	Dete		xx = 80 = Endpt0 IN
	Returns:		XX yy
			xx[0] = Halt $xx[7:1] = 0$
			xx[7:1] = 0 yy = 00 (Reserved)
	Default:		Accept (undefined in USB Spec)
	Addressed:		Accept
	Configured:	Accept	
	Notes:		Use Set_Feature_Endpt0/Clear_Feature_Endpt0 to (try to) set/clear
			Halt bit. According to USB, "It is neither required or recommended
			that the Halt feature be implemented for the Default Control Pipe."
			Since a new SETUP command will clear any Endpt0 halt bit, it is
17			impossible to tell if there really is a halt bit.
· •			

Get_Status_	Endpt1		82 00 00 00 81 00 02 00
	Returns:		xx yy
	neturis.		xx[0] = Halt
			xx[7:1] = 0
			yy = 00 (Reserved)
	Default:		Stall (undefined in USB Spec)
	Addressed:		Stall
	Configured:	Accept	
	Notes:		Use Set_Feature_Endpt1/Clear_Feature_Endpt1 to set/clear Halt bit.
Get_Configu	ration		
			80 08 00 00 00 01 00
	Returns:		XX
			xx = config value
	Default:		Accept (undefined in USB Spec) — returns 00
	Addressed:		Accept — returns 00
		Accont rot	
	Configured:	Accept — ret	
	Notes:		Use Set_Configuration to change.
	<u>е</u>		
set_interiat	~		81 0A 00 00 00 00 01 00
	Returns:		00
	Default:		Undefined in USB Spec
	Addressed:		Stall
	Configured:	Accept — ret	
	Notes:		Command has no alternate interfaces, so only valid value is 00
C.4. D.4.			
Get_Protocol	I		A1 02 00 00 00 01 00
			A1 03 00 00 00 01 00
	Returns:		XX
			xx = 00 = Boot protocol
			xx = 01 = Report protocol
	Default:		Accept
	Addressed:		Accept
	Configured:	Accept	1.
	Notes:	. iccept	Defaults to Report protocol after USB Reset. Use Set_Protocol to change.
	ווטוכז.		שלוממונג נס הבייסור ויוסנסנסו מונכו סכם הככבו. סכב בכו_רוסנסנסו נס נוומוועב.
Get_Desc_D	evice		80 06 00 01 00 00 nn00
			80 06 00 01 00 00 12 00
	Determ		12 01 00 02 00 00 00
	Returns:		12 01 00 02 00 00 08
			vv vv pp pp dd dd mm PP
			ss 01
			vv vv = vendor id
			pp pp = product id (vendor specified)
			dd dd = device id (vendor specified) (bcd rev_id byte)
			mm = iManufacturer
			PP = iProduct
			ss = iSerialNumber (00 - no string)
	Default:		Accept
	Addressed:		Accept
	Configured:	Accept	

Get_Desc_Config

Returns:

Without Z-Wheel:

80 06 00 02 00 00 22 00 09 02 22 00 01 01 00 A0 32 09 04 00 00 01 03 01 02 00 09 21 10 01 00 01 22 rr 00 07 05 81 03 04 00 0A

80 06 00 02 00 00 nn 00

rr = HID Report descriptor length

These values are determined by jumper configuration see strap table.

09 02 22 00 01 01 00 A0 32 09 04 00 00 01 03 01 02 00 09 21 10 01 00 01 22 32 00 07 05 81 03 04 00 0A // Config Descriptor 09 // bLength bDescriptorType 02 // wTotalLength (34 decimal) 22 // 00 // high byte of WTotalLength 01 // bNumInterfaces 01 bConfigurationValue // 00 // iConfiguration bmAttributes (bus powered/remote wakeup) A0 // 32 // MaxPower (in 100mA in 2mA units) // Interface Descriptor 09 // bLength 04 // bDescriptorType 00 bInterfaceNumber // 00 // bAlternateSetting İ01 // **bNumEndpoints** bInterfaceClass (HID Class) 03 // 01 // bInterfaceSubClass 02 // bInterfaceProtocol 00 $\parallel \mid$ iInterface // HID Descriptor 09 // bLength 21 // bDescriptorType 11 // bcdHID (HID Release ##.##; HID 1.1 compliant) 01 // 00 // bCountry 01 // bAvailable 22 // bType // wLength (Length of HID Report below) 32 00 // // Endpoint Descriptor 07 // bLength 05 // bDescriptorType bEndpointAddress (IN & #=1) 81 // 03 // bmAttributes (Interrupt) wMaxPacketSize 04 // 00 // | OA // bInterval (10mS)

02 00 09	21 10 01 00 01	
22 34 00	07 05 81 03 04	
00 0A		
	//	Config Descriptor
09	//	bLength
02	//	bDescriptorType
22	//	wTotalLength (34 decimal)
00	//	high byte of WTotalLength
01	//	bNumInterfaces
01	//	bConfigurationValue
00	//	iConfiguration
A0	//	bmAttributes (bus powered/remote wakeup
32	//	MaxPower (in 100mA in 2mA units)
	//	Interface Descriptor
09	//	bLength
04	//	bDescriptorType
00	//	bInterfaceNumber
00	//	bAlternateSetting
01	//	bNumEndpoints
03	//	bInterfaceClass (HID Class)
01	//	bInterfaceSubClass
02	//	bInterfaceProtocol
00	//	iInterface
	//	HID Descriptor
09	//	bLength
21	//	bDescriptorType
11	//	bcdHID (HID Release ##.##; HID 1.1 complian
01	//	
00	//	bCountry
01	//	bAvailable
22	//	bType
34	//	wLength (Length of HID Report below)
00	//	
	//	Endpoint Descriptor
07	//	bLength
05	//	bDescriptorType
81	//	bEndpointAddress (IN & #=1)
03	//	bmAttributes (Interrupt)
04	//	wMaxPacketSize
00	//	

Default: Addressed: Configured: Accept Notes:

This is the concatenation of 4 descriptors: Configuration Interface HID Endpt

Accept

Accept

Get_Desc_String

80 06 xx 03 00 00 nn 00 xx= 00 Language String 02 Product String

Returns: ss 03 "unicode string" ss = String descriptor length

These values are determined by jumper configuration on page 14:

For xx=00: 04 03 09 04 // Language ID For xx=02: 20 00 55 00 53 00 42 00 // " USB" 20 00 4f 00 70 00 74 00 // "0pt" 69 00 63 00 61 00 6c 00 // "ical" 20 00 4d 00 6f 00 75 00 // "Mou" 73 00 65 00 // "se

Get_Desc_HID

81 06 00 21 00 00 09 00

Returns:

09 21 10 01 00 01 22 rr 00 rr = HID Report descriptor length

These values are determined by jumper configuration see table on page 14:

Without Z-wheel:

09 21 10 01 00 01 22 32 00 // HID Descriptor 09 // bLength 21 bDescriptorType // İ10 // bcdHID (HID Release ##.##; HID 1.1 compliant) 01 // 00 // bCountry 01 // bAvailable 22 // bType wLength (Length of HID Report below) 32 // 00 //

With Z-wheel:

Default: Addressed: Configured:

09 21 10 01 00 01 22 34 00

		//	HID Descriptor
	09	//	bLength
	21	//	bDescriptorType
	10	//	bcdHID (HID Release ##.##; HID 1.1 compliant)
	01	//	· · · ·
	00	//	bCountry
	01	//	bAvailable
	22	//	bType
	34	//	wLength (Length of HID Report below)
	00	//	
	Accept		
	Accept		
: Accept			

Get_Desc_HID_Report 81 06 00 22 00 00 nn 00

Returns: This returns a report descriptor that describes how many buttons and x, y, z data.

These values are determined by jumper configuration see table on page 14:

Without Z-wheel:

05 01 09 02 A1 01 09 01 A1 00 05 09 19 01 29 xx 15 00 25 01 75 01 95 xx 81 02 75 yy 95 01 81 01 05 01 09 30 09 31 15 81 25 7F 75 08 95 02 81 06 C0 C0	// xx = # buttons // xx = # buttons // yy = 8 - # buttons
//	HID Report
05 //	USAGE_PAGE (Generic Desktop)
01 //	
09 //	USAGE (Mouse)
02 //	
A1 //	COLLECTION (Application)
01 //	
09 //	USAGE (Pointer)
01 //	
A1 // 00 //	COLLECTION (Physical)
05 //	USAGE_PAGE (Button)
09 //	
19 //	USAGE_MINIMUM (Button 1)
01 //	
29 //	USAGE_MAXIMUM (Button #)
xx //	
15 //	LOGICAL_MINIMUM (0)
00 //	
25 //	LOGICAL_MAXIMUM (1)
01 //	
75 //	REPORT_SIZE (1)
01 //	
95 // xx //	REPORT_COUNT (Button #)
xx // 81 //	INPUT (Data,Var,Abs)
02 //	
75 //	REPORT_SIZE (8 - Button #)
yy //	
95 //	REPORT_COUNT (1)
01 //	
81 //	INPUT (Cnst,Ary,Abs)
01 //	
05 //	USAGE_PAGE (Generic Desktop)
01 //	
09 //	USAGE (X)
30 // 09 //	
31 //	USAGE (Y)
15 //	LOGICAL_MINIMUM (-127)
81 //	
25 //	LOGICAL_MAXIMUM (127)
7F //	
75 //	REPORT_SIZE (8)
08 //	
95 //	REPORT_COUNT (2)
02 //	
81 //	INPUT (Data,Var,Rel)
06 //	
C0 // C0 //	END_COLLECTION END COLLECTION

05 01 09 02 A1 01 09 01 A1 00 05 09 19 01 29 xx 15 00 25 01 75 01 95 xx 81 02 75 yy 95 01 81 01 05 01 09 30 09 31 09 38 15 81 25 7F 75 08 95 03 81 06 00	// xx = # buttons // xx = # buttons // yy = 8 - # buttons
15 00 25 01 75 01 95 xx 81 02 75 yy 95 01 81 01 05 01 09 30 09 31 09 38	// xx = # buttons
05 // 01 // 09 // 30 //	USAGE_PAGE (Generic Desktop) USAGE (X)
09 // 31 // 09 // 38 // 15 // 81 // 25 //	USAGE (Y) USAGE (Wheel) LOGICAL_MINIMUM (-127) LOGICAL_MAXIMUM (127)
7F // 75 // 08 // 95 // 03 // 81 // 06 //	REPORT_SIZE (8) REPORT_COUNT (3) INPUT (Data, Var, Rel)
C0 // C0 // Accept Accept	END_COLLECTION END_COLLECTION

Addressed: Configured: Accept Notes:

Default:

The length of this report is needed in the HID descriptor.

Get_HID_Inp	out		A1 01 00 01 00 00 mm 00
			A1 01 00 01 00 00 nn 00
			A1 01 00 01 00 00 04 00 0R
			A1 01 00 01 00 00 03 00 (if no Z-wheel present)
	Returns:		bb xx yy zz OR
			bb xx yy (if no Z-wheel present)
			bb = button byte
			xx = X motion byte
			yy = Y motion byte
			zz = Z motion byte
	Default:		Stall
	Addressed:		Stall
	Configured:	Accept	Ster
	Notes:	лесерс	If the device is configured, it will
	notes.		always respond with a report for this command, even if no motion
			or button changes have occurred. In this case, it would report 00
			for motion and simply report the current button state. If a report
			is pending on endpt1, the data there will be reported and the
			report on endpt1 cleared.
			The mouse will only create new button/motion packets when it is
			in the Configured state.
C.4. 1.11.			
Get_Idle			A1 02 00 00 00 00 01 00
	Returns:		rr
			rr = rate in multiples of 4mS
	Default:		Accept
	Addressed:		Accept
	Configured:	Accept	Acch
	Notes:	Ассерс	The third buts of the command is to calest the Denast ID. There is
	notes.		The third byte of the command is to select the Report ID. There is
			only one for the mouse — so, using 00 or 01 will work. See also Set_Idle.
	T .		
Get_Vendor_	_Test		C0 01 00 00 xx 00 01 00
Get_Vendor_	_Test		C0 01 00 00 xx 00 01 00 ii = ignore
Get_Vendor_	_Test		C0 01 00 00 xx 00 01 00
Get_Vendor_			C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read
Get_Vendor_	_Test Returns:		C0 01 00 00 xx 00 01 00 ii = ignore
Get_Vendor_			C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read
Get_Vendor_	Returns:		C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept
Get_Vendor_	Returns: Default: Addressed:	Accent	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read)
Get_Vendor_	Returns: Default: Addressed: Configured:	Accept	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept
Get_Vendor_	Returns: Default: Addressed:	Accept	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept Address range (xx) should be x00 to x3F. Behavior for addresses
Get_Vendor_	Returns: Default: Addressed: Configured: Notes:	Accept	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future.
	Returns: Default: Addressed: Configured: Notes:	for description	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers.
	Returns: Default: Addressed: Configured: Notes:		C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers. 0 00 00 00
	Returns: Default: Addressed: Configured: Notes: See page 28	for description	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers. 000 00 00 xx = new device address, from 00 to 7F
	Returns: Default: Addressed: Configured: Notes: See page 28 : Default:	for description	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers. 0 00 00 00 xx = new device address, from 00 to 7F Accept
	Returns: Default: Addressed: Configured: Notes: See page 28 Default: Addressed:	for description 00 05 xx 00 0	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers. 0 00 00 00 xx = new device address, from 00 to 7F Accept Accept
	Returns: Default: Addressed: Configured: Notes: See page 28 : Default:	for description	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers. 0 00 00 00 xx = new device address, from 00 to 7F Accept Accept
Set_Address	Returns: Default: Addressed: Configured: Notes: See page 28 Default: Addressed: Configured:	for description 00 05 xx 00 0	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers. 0 00 00 00 xx = new device address, from 00 to 7F Accept Accept
Set_Address	Returns: Default: Addressed: Configured: Notes: See page 28 Default: Addressed: Configured:	for description 00 05 xx 00 0	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers. 0 00 00 00 xx = new device address, from 00 to 7F Accept Accept Accept USB Spec 00 09 xx 00 00 00 00
Set_Address	Returns: Default: Addressed: Configured: Notes: See page 28 Default: Addressed: Configured:	for description 00 05 xx 00 0	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers. 0 00 00 00 xx = new device address, from 00 to 7F Accept Accept USB Spec 00 09 xx 00 00 000 00 xx = 00 = not configured
Set_Address	Returns: Default: Addressed: Configured: Notes: See page 28 1 Default: Addressed: Configured: ation	for description 00 05 xx 00 0	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers. 0 00 00 00 xx = new device address, from 00 to 7F Accept Accept USB Spec 00 09 xx 00 00 000 00 xx = 00 = not configured xx = 01 = configured
Set_Address	Returns: Default: Addressed: Configured: Notes: See page 28 Default: Addressed: Configured: ation Default:	for description 00 05 xx 00 0	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers. 0 00 00 00 xx = new device address, from 00 to 7F Accept Accept USB Spec 00 09 xx 00 00 000 00 xx = 00 = not configured xx = 01 = configured Undefined in USB Spec
Get_Vendor_ Set_Address	Returns: Default: Addressed: Configured: Notes: See page 28 1 Default: Addressed: Configured: ation	for description 00 05 xx 00 0	C0 01 00 00 xx 00 01 00 ii = ignore xx = address of register to read rr (depends on register read) Accept Accept Address range (xx) should be x00 to x3F. Behavior for addresses above 0x3F is not guaranteed and may be modified in the future. of registers. 0 00 00 00 xx = new device address, from 00 to 7F Accept Accept USB Spec 00 09 xx 00 00 000 00 xx = 00 = not configured xx = 01 = configured

	Addressed: Configured:	Accept	Accept			
	Default:		Accept (undefined in USB Spec)			
			xx = 00 = Endpt0 OUT xx = 80 = Endpt0 IN			
			02 01 00 00 80 00 00 00			
Clear_Featur	e_Endpt0	02 01 00 00	xx 00 00 00 02 01 00 00 00 00 00 00 00			
(l		02.04.00.00	·			
	Configured: Notes:	Accept	This clears the remote wakeup bit.			
	Addressed:		Accept			
	Default:		Accept (undefined in USB Spec)			
 Clear_Featur	e_Device		00 01 01 00 00 00 00 00			
	Notes:		Sets the halt bit for Endpt1.			
	Addressed: Configured:	Accept	Stall			
	Default:		Stall (undefined in USB Spec)			
			02 03 00 00 81 00 00 00			
			the halt bit set for Endpt0 with the Get_Status_Endpt0 command, as any new SETUP command will clear Endpt0 stall.			
			status stage for this command. The chip never reports			
	Notes:		This (tries to) sets the halt bit. The chip always stalls the			
	Addressed: Configured:	Stall	Stall			
	Default:		Stall (undefined in USB Spec)			
			xx = 80 = Endpt0 IN			
			xx = 00 = Endpt0 OUT			
			02 03 00 00 80 00 00 00			
Set_Feature_	_Endpt0		02 03 00 00 xx 00 00 00 02 03 00 00 00 00 00 00			
	Notes:		This sets the remote wakeup bit.			
	Configured:	Accept				
	Default: Addressed:		Accept (undefined in USB Spec) Accept			
Set_Feature_			00 03 01 00 00 00 00 00 Accent (undefined in USB Spec)			
	Notes:		3 byte data packets will be reported in boot mode.			
	Configured:	Accept				
	Addressed:		Accept (Not in USB Spec)			
	Default:		Accept (Not in USB Spec)			
			xx = 00 = Boot protocol xx = 01 = Report protocol			
Set_Protocol		21 OB xx 00	00 00 00 00			
			if invalid values are given and command was stalled.			
			interface state after executing this command in configured mode even			
	notes:		Mouse has only one valid interface (00) and alternate setting (00). Invalid values will cause stall. Chip retains previous (valid)			
	Configured: Notes:	Accept	Mauca has anly ana valid interface (00) and alternate setting (00)			
	Addressed:		Stall			
	Default:		Stall (undefined in USB Spec)			
set_interiace	2	01 08 00 00				
Set_Interface		01 0B 00 00 00 00 00 00				

Clear_Feature	e_Endpt1 Default: Addressed: Configured: Notes:	02 01 00 00 8 Accept	1 00 00 00 Stall (undefined in USB Spec) Stall See Set_Feature_Endpt1.
Set_Idle	Default: Addressed: Configured: Notes:	Accept	21 0A 00 rr 00 00 00 00 rr = rate in multiples of 4mS Accept Accept The third byte of the command is to select the Report ID. There is only one for the mouse — so, using either 00 or 01 will work. The fourth byte of the command sets the rate in multiples of 4mS. The initial value for mice will be x00 which means "infinite" — that is packets only come out when there is a change in data. Data will only be allowed to come out when the device is configured. However, the chip will accept the command in Default or Addressed mode and use that value when the device is later configured.
Get_Vendor_	Test Default: Addressed: Configured: Notes:	Accept	40 01 00 00 xx yy 00 00 xx = address yy = data Accept Accept Address range for "xx" should be 0x00 to 0x3F. Addresses above this are reserved for possible future use. See also Get_Vendor_Test.
Poll_Endpt1	Returns:		bb xx yy zz bb = button byte xx = X motion byte yy = Y motion byte zz = Z motion byte (if Z-Wheel)
	Default: Addressed: Configured: Notes:	Accept (NAK i	Ignore request Ignore request Ignore request ino data; Send packet if available) See also Get_HID_Input. Endpt will only stall if halt bit is set by Set_Feature_Endpt1. Details of data packet are below. Endpt1 should be polled at least every 10 frames (mS). It is typically polled every 8 frames on Windows machines. For internal testing, Endpt1 can be continuously polled if desired. The chip will not generate any report packets unless in the Configured state. If Endpt1 is currently empty, any motion or button change will be loaded into the Endpt1 buffers. Once the Endpt1 buffers are full, any further motion events will get accumulated. When the Endpt1 buffers are later polled and emptied, the current accumulated X/Y/Z values will be loaded into the Endpt1 buffers. After transferring their data, the accumulation registers are reset so they are ready to start accumulating new motion events.

Button information is handled a bit differently. If the Endpt1 buffers are empty, and a button change event occurs, the new button state is put into the Endpt1 buffers. At the same time, the button state that is put in Endpt1 is copied for later use. While Endpt1 is full, changes in button state are essentially ignored. When Endpt1 is emptied, if the current button state is different than that which was last loaded into Endpt1, then the new state will be loaded and a new copy saved. Basically, the button state at that point in time. It should also be noted that there is hardware on the chip to help de-bounce the buttons.

USB Data Packet Fo	rmat							
Bit	7	6	5	4	3	2	1	0
Byte 1	0	0	0	B5	B4	B3(MB)	B2(RB)	B1(LB)
Byte 2	Х7	Х6	X5	X4	X3	X2	X1	X0
Byte 3	Y7	Y6	Y5	Y4	Y3	Y2	Y1	Y0
Byte 4	Z7	Z6	Z5	Z4	Z3	Z2	Z1	Z0

Special note on wLength:

The wLength paramater in commands specifies the maximum number of bytes a device should send back. The commands listed below are not able to handle a wLength of 0 correctly.

Get_Status_Device Get_Status_Interface Get_Status_Endpt0 Get_Status_Endpt1 Get_Configuration Get_Interface

 $This chip will send one byte of data rather than none when wLength {=} 0 is requested for the above commands.$

USB Data Packet Format, With Z wheel

	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Byte 1	0	0	0	Button 5	Button 4	Button 3	Button 2	Button 1
Byte 2	X[7]	X[6]	X[5]	X[4]	X[3]	X[2]	X[1]	X[0]
Byte 3	Y[7]	Y[6]	Y[5]	Y[4]	Y[3]	Y[2]	Y[1]	Y[0]
Byte 4	Z[7]	Z[6]	Z[5]	Z[4]	Z[3]	Z[2]	Z[1]	Z[0]

USB Data Packet Format, Without Z wheel

	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Byte 1	0	0	0	Button 5	Button 4	Button 3	Button 2	Button 1
Byte 2	X[7]	X[6]	X[5]	X[4]	X[3]	X[2]	X[1]	X[0]
Byte 3	Y[7]	Y[6]	Y[5]	Y[4]	Y[3]	Y[2]	Y[1]	Y[0]

Registers

The sensor can be programmed through registers, via the USB port, and configuration and motion data can be read from these registers. Certain registers must be "enabled" after power up but before first read or write to that register. The registers will be "disabled" by VDD going low or sending a USB reset command.

Address	Register
0x00	Product_ID
0x01	Revision_ID
0x02	Motion
0x03	DeltaX
0x04	DeltaY
0x05	SQUAL
0x06	Shutter_Upper
0x07	Shutter_Lower
0x08	Maximum_Pixel
0x09	Average_Pixel
0x0a	Minimum_Pixel
0x0b	Pix_Grab
0x0c	Dz
0x0d	Configuration_bits
0x3f	InvRevID

Product_ID		Address:	0x00					
Access: Read		Reset Val	ue: 0x05					
Bit	7	6	5	4	3	2	1	0
Field	PID ₇	PID ₆	PID ₅	PID ₄	PID ₃	PID ₂	PID ₁	PID0
Data Type:		Eight bit	number with	n the produ	ct identifier.			
USAGE:			e in this regis ons link is Ol		t change; it o	can be used	to verify tha	at the sensor com
Revision_ID		Address:	0x01					
Revision_ID Access: Read			0x01 ue: 0xNN					
	7			4	3	2	1	0
Access: Read	7 RID ₇	Reset Val	ue: 0xNN	4 RID ₄	3 RID3	2 RID ₂	1 RID ₁	0 RID ₀
Access: Read Bit	-	Reset Val 6 RID ₆	ue: 0xNN 5	RID ₄	RID ₃	RID ₂	•	•
Access: Read Bit Field	-	Reset Val	ue: 0xNN 5 RID ₅ number with	RID ₄	RID ₃	RID ₂	RID ₁	•

Motion		Address: 0x	Address: 0x02								
Access: Read		Reset Value	: Undefined								
Bit	7	6	5	4	3	2	1	0			
Field	MOT	Reserved	Reserved	BUT ₅	BUT ₄	BUT ₃	BUT ₂	BUT ₁			
Data Type:		Bit field									
		Field Name	Descript								
		МОТ	0 = no m	since last ro otion tion has occ							
		BUT ₅	When u reports 0 = pin 1 = pin	, utton pin							
		BUT ₄	reports 0 = pin	sed as a 5 k the status o at logic 1 (\ at logic 0 (C							
		BUT ₃	0 = pin	the status at logic 1 (\ at logic 0 (0							
		BUT ₂	0 = pin	the status at logic 1 (\ at logic 0 (0							
		BUT ₁	Reports 0 = pin 1 = pin								
		Reserved	Reserve Reset Va l	ed for future ue = 0	e use						

USAGE:

A "1" in the motion bit indicates that the USB endpoint has valid data. This register is included for test purposes only. For navigation use, use the USB HID defined commands. The button status bits reported are for the debounce signals.

DeltaX		Address	: 0x03							
Access: Read		Reset Value: 0x00								
Bit	7	6	5	4	3	2	1	0		
Field	X ₇	X ₆	X ₅	X4	X ₃	X ₂	X ₁	X ₀		
Data Type:		Bit field								
USAGE:		output. defined	This register	is included Data is 8 bit	for test purp	poses only. F	or navigatio	or data queued fo on use, use the HIE determined by the		

Register 0x03 must be read before register 0x04 (Delta Y)

DeltaY		Address:	0x04					
Access: Read		Reset Va	ue: 0x00					
Bit	7	6	5	4	3	2	1	0
Field	Y ₇	Y ₆	Y ₅	Y4	Y ₃	Y ₂	Y ₁	Y ₀
Data Type:		Bit field						
USAGE:		The valu output.	e in this reg	gister reflects	s the last US	B delta Y da	ata output o	or data queued f
		Register	0x03 should	l be read bef	ore register	0x04 (Delta	Y), else Delta	a Y will return 0.
			ds. Data is 8					se the HID define ed by the current
SQUAL		Address:	0x05					
		Address: Reset Va						
	7			4	3	2	1	0
Access: Read	7 SQ ₇	Reset Val	ue: 0x00	4 SQ ₄	3 SQ ₃	2 SQ ₂	1 SQ1	0 SQ ₀
		Reset Val	ue: 0x00 5				•	-

Shutter_Upper		Address:	Address: 0x06									
Access: Read		Reset Val	ue: 0x01									
Bit	7	6	5	4	3	2	1	0				
Field	SH ₇	SH ₆	SH ₅	SH ₄	SH₃	SH ₂	SH1	SH ₀				

USAGE:

The combination of Shutter_Upper and Shutter_Lower is a 16-bit number. This is the number of clocks the shutter was open for the last image taken. The units are in main clocks ticks (nominally 24MHz). To avoid split read issues, read Shutter_Upper first.

Shutter_Lower		Address:	0x07							
Access: Read		Reset Value: 0x64								
Bit	7	6	5	4	3	2	1	0		
Field	SL ₇	SL ₆	SL ₅	SL ₄	SL ₃	SL ₂	SL ₁	SL ₀		
Data Type:		Eight bit	number.							
USAGE: The combination of Shutter_Upper and Shutter_Lower is a 16-bit number. This is number of clocks the shutter was open for the last image taken. The units are in m clocks ticks (nominally 24MHz). To avoid split read issues, read Shutter_Upper first.										
 Maximum_Pixel		Address:	0x08							
Maximum_Pixel Access: Read		Address: Reset Va								
	7			4	3	2	1	0		
Access: Read	7 MX ₇	Reset Va	lue: 0x00	4 MX ₄	3 MX ₃	2 MX ₂	1 MX ₁	0 MX ₀		
Access: Read Bit	-	Reset Val	lue: 0x00		-		•	-		

Average_Pixel		Address: 0x09									
Access: Read		Reset Va	Reset Value: 0x00								
Bit	7	6	5	4	3	2	1	0			
Field	AC ₇	AC ₆	AC ₅	AC ₄	AC ₃	AC ₂	AC ₁	AC ₀			
Data Type:		Eight bit	number.								
USAGE:		only the	This is the accumulated pixel value from the last image taken. For the 15X15 raw image, only the 8 most interesting bits are reported ([14:7]). To get the true average pixel value, multiply this register value by 1.75.								

Minimum_Pixel		Address:	Address: 0x0a								
Access: Read		Reset Val	ue: 0x00								
Bit	7	6	5	4	3	2	1	0			
Field	MN ₇	MN ₆	MN_5	MN ₄	MN ₃	MN_2	MN ₁	MN ₀			

Data Type:

USAGE:

Eight bit number.

This is the minimum pixel value from the last image taken.

Pix_Grab		Address:	0x0b							
Access: Read/W	/rite	Reset Valu	e: 0x00							
Bit	7	6	5	4	3	2	1	0		
Field	PG ₇	PG ₆	PG ₅	PG ₄	PG ₃	PG ₂	PG ₁	PG ₀		

USAGE:

The pixel grabber captures 1 pixel per frame. If there is a valid pixel in the grabber when this is read, the MSB will be set, an internal counter will incremented to captured the next pixel and the grabber will be armed to capture the next pixel. It will take 225 reads to upload the completed image. Any write to this register will reset and arm the grabber to grab pixel 0,0 on the next image.

Pixel Address Map (Looking through the aperture of the sensor)

0	15	30	45	60	75	90	105	120	135	150	165	180	195	210
1	16	31	46	61	76	91	106	121	136	151	166	181	196	211
2	17	32	47	62	77	92	107	122	137	152	167	182	197	212
3	18	33	48	63	78	93	108	123	138	153	168	183	198	213
4	19	34	49	64	79	94	109	124	139	154	169	184	199	214
5	20	35	50	65	80	95	110	125	140	155	170	185	200	215
6	21	36	51	66	81	96	111	126	141	156	171	186	201	216
7	22	37	52	67	82	97	112	127	142	157	172	187	202	217
8	23	38	53	68	83	98	113	128	143	158	173	188	203	218
9	24	39	54	69	84	99	114	129	144	159	174	189	204	219
10	25	40	55	70	85	100	115	130	145	160	175	190	205	220
11	26	41	56	71	86	101	116	131	146	161	176	191	206	221
12	27	42	57	72	87	102	117	132	147	162	177	192	207	222
13	28	43	58	73	88	103	118	133	148	163	178	193	208	223
14	29	44	59	74	89	104	119	134	149	164	179	194	209	224

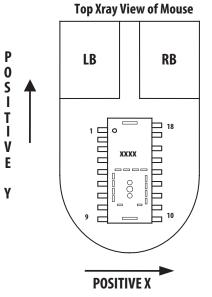
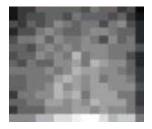


Figure 20. Pixel Map.

The following images are the output of the pixel dump command. The data ranges from zero for complete black, to 63 for complete white. An internal AGC circuit adjusts the shutter value to keep the brightest feature (max pixel) in the mid 50's.





White Paper Figure 21. Pixel Dump Pictures

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dump command. The data rang justs the shutter value to keep

4 Z ₄ d to contain 127 decimal		Z ₂	1 Z ₁	0 Z ₀ Z-wheel cou						
Z ₄ d to contain o 127 decimal	Z ₃ a Z-wheel, th	Z ₂	Z ₁	Z ₀						
d to contain 127 decimal	a Z-wheel, th									
127 decimal		nis register o	contains the	Z-wheel cou						
127 decimal		nis register o	contains the	Z-wheel cou						
		Reset Value: 0x00								
4	3	2	1	0						
Reserved	Reserved	Reserved	Reserved	RES						
Description										
Resolution in counts per inch 0 = 500 1 = 1000										
2 3 1	Description Resolution in cc D = 500 I = 1000	Description Resolution in counts per inch D = 500	Description Resolution in counts per inch D = 500 I = 1000	Description Resolution in counts per inch D = 500 I = 1000						

InvRevID		Address: 0x03f								
Access: Read		Reset Value: 0xf0								
Bit	7	6	5	4	3	2	1	0		
Field	RRID ₇	RRID ₆	RRID ₅	RRID ₄	RRID ₃	RRID ₂	RRID ₁	RRID ₀		
Data Type:		Eight bit r	number with	current rev	ision of the I	C.				
USAGE:		Contains	Contains the inverse of the revision ID which is located in register 0x01.							
		IC Dogisto	IC Degister state ofter Deset (newer un)							

IC Register state after Reset (power up)

Ordering Information

Specify part number as follows: ADNS-5000 = Sensor IC in a 18-pin staggered DIP, 22 per tube. ADNS-5100 = Lens ADNS-5100-001 = Trim Lens ADNS-5200 = LED clip

For product information and a complete list of distributors, please go to our web site:

www.avagotech.com

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