Specification

Date : Mar-22-2012

TO: Digi-Key

| Approved by | Selling agency KYOCERA Corporation (Electronic Components Sales Division) 〒612-8501 6 Takeda Tobadono-cho, Fushimi-ku Kyoto 612-8501 TEL 075-604-3500, FAX 075-604-3501 |
|-------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| | Manufacturer KYOCERA KINSEKI Corporation (Crystal Oscillator Department) ∓201-8648 1-8-1 Izumi –Honcho Komae-shi, Tokyo 201-8648 TEL 03-5497-3111, FAX 03-5497-3209 |

Let us Submit <u>1</u> Copies of the approved Specification on the below items.

| Product | SAW Oscillator |
|--------------------|---------------------------------------------------------|
| Model | KC7050Yxxx.xxxP20EZU (x is frequency.) |
| Frequency | 75.0000, 125.000, 156.250, 200.000, 250.000, 312.500MHz |
| Customer Model | - |
| Customer Parts No. | - |

This product is Pb - Free and RoHS compliant.

| Engineering | Issued by | Approved by | Drawing No. |
|-----------------------------------------|--------------|-------------|-----------------|
| KYOCERA KINSEKI Yamagata Corporation | Y. Yamagishi | M.Takeno | K1101-12003-SF2 |

*Recycled paper is being used for the conservation of nature

HISTORY

| No | Date | Change matter | Charge | Check | Approval |
|----|-----------|-------------------------------------------------------------------------------------------------------------|--------------|-------------|-----------|
| 1 | 2012/1/6 | First edition | Y. Yamagishi | T.Kabayashi | N. Takeno |
| 2 | 2012/3/22 | Model KC7050Yxxx.xxxP20E00 → KC7050Yxxx.xxxP20EZU 7-1. Taping Quantities maximum 1000 pcs → maximum 500 pcs | | T.Kebayashi | M.Takeno |
| | | | | | |

| KYOCERA KINSEKI Yamagata Corporation | Dwg No | K1101-12003-SF2 2/8 |
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1. Application

This specification delivers Digi-Key.

SAW Oscillator, KC7050Yxxx.xxxP20EZU applies to 75.0000, 125.000, 156.250, 200.000, 250.000, 312.500MHz.

2. Function

2-1. Absolute Maximum Rating

| Item | Symbol | Rating | Unit |
|---------------------------|------------------|------------------------------|------|
| Power Supply Voltage | Vcc | -0.3 to +5.0 | ٧ |
| Input Voltage | V _{IN} | -0.3 to V _{CC} +0.3 | V |
| Storage Temperature Range | T _{STG} | -55 to +125 | Ç |

Note: If KC7050Y is used beyond absolute maximum ratings, it may cause internal destruction.

KC7050Y should be used under the recommended operating conditions. KC7050Y reliability may be damaged if those conditions are exceeded.

2-2. Recommended Operating Condition

| Item | Symbol | Min | Тур | Max | Unit | Remarks |
|-----------------------------|------------------|-------|-----|-----------------|------|---------|
| Power Supply Voltage | V _{CC} | 2.375 | 2.5 | 2.625 | ٧ | |
| Input Voltage | V _{IN} | 0 | | V _{CC} | ٧ | |
| Operating Temperature Range | T _{OPR} | 0 | +25 | +70 | တ | |

2-3. Electrical Characteristic Specifications

| Item | Symbol | Min | Тур | Max | Unit | Remarks |
|----------------------------------------|------------------|------------------------|-------|------------------------|------|----------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Frequency Stability | F _{SBY} | -50 | | +50 | ppm | *Over all conditions: Initial tolerance, operating temperature range, rated power supply voltage change, load change, aging (1year @25°C), shock and vibration |
| Current Consumption | I _{CC} | | 74 | 100 | mA | |
| Standby Current | I _{ST} | | | 30 | μΑ | |
| Duty ratio (crossing point) | SYM | 45 | 50 | 55 | % | 50ohm, @ crossing point |
| Rise Time (20% to 80% Output Level) | Tr | | 0.25 | 0.4 | nS | 50ohm |
| Fall Time (20% to 80% Output Level) | Tf | | 0.25 | 0.4 | IIS | 5001111 |
| Output Voltage -"L" | V _{OL} | V _{CC} -1.810 | 1.600 | V _{CC} -1.620 | v | DC characteristic. |
| Output Voltage -"H" | V _{OH} | V _{CC} -1.025 | 2.350 | V _{CC} -0.880 | V | DC characteristic. |
| Output Load | | | 50 | | ohm | LV-PECL Output |
| Input Voltage -"L" | V _{IL} | | | 30% V _{CC} | v | OE termination |
| Input Voltage -"H" | V _{IH} | 70% V _{CC} | | | - | OL terrimation |
| Output Disable Time | | | | 200 | nS | |
| Output Enable Time | | | 2 | 10 | mS | |
| Start up time | ST | | 2 | 10 | mS | @Minimum operating voltage to be 0sec |
| Deterministic Jitter* | DJ | | 0.2 | 2 | | DJ pk-pk |
| 1sigma Jitter* | 1sigma | | 2 | 4 | pS | |
| Peak to Peak Jitter* | Pk-Pk | | 20 | 30 | | |

Note: All Electrical characteristics define Maximum Loaded and operating temperature range.

Table 1

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|-----------------------------------------|--------|---------------------|

^{*}The Time Interval Analyzer "Wavecrest DTS-2079" with VISI 6.3.1 shall measure jitter.

2-4. Measurement Condition

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests are as follows:

Ambient temperature : 15℃ to 35 ℃
Relative Humidity : 25% to 85%
Air pressure : 86kPa to 106kPa

If there is any doubt about the results, measurements shall be made within the following limits:

• Ambient temperature : 25℃

Relative Humidity : 60% to 70%Air pressure : 86kPa to 106kPa

Unless otherwise specified for each item, it should be specified according to JIS (Japanese industrial Standard).

2-5. Measurement Circuit

The test circuit as shown in "Fig. 1" (*Jitter Test Circuits "Fig. 3") shall measure electric characteristics.

ECL Termination 10086A (Agilent)

2-6. Clock Timing Chart

The clock timing chart as shown in "Fig. 2".

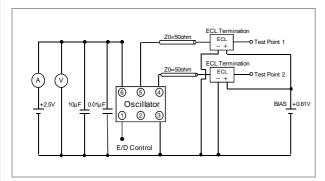


Fig.1 Test Circuits

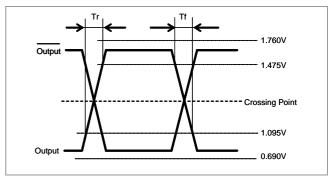


Fig.2 Clock Timing Chart (PECL Output)

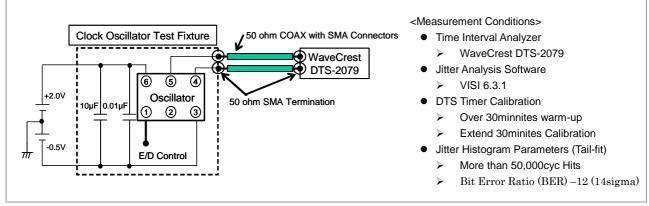
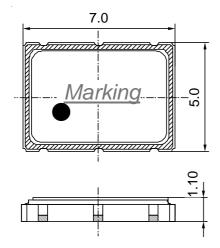
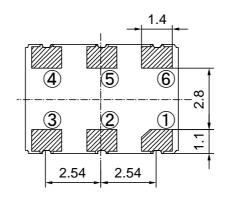


Fig.3 Jitter Test Circuits

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|-----------------------------------------|--------|---------------------|--|
|-----------------------------------------|--------|---------------------|--|

3. Dimensions and Marking





Tolerance; +/-0.2 Unit; (mm)

| Pad | Pad arrangement | | | | |
|-----|----------------------|--|--|--|--|
| 1 | ① Enable / Disable | | | | |
| 2 | NC | | | | |
| 3 | GND | | | | |
| 4 | Output | | | | |
| (5) | Complementary Output | | | | |
| 6 | V _{cc} | | | | |

| Enable / Disable Function | | | |
|---------------------------|----------------|--|--|
| *Pad1 | *Pad4 / Pad5 | | |
| OPEN | Active | | |
| "H" Level | Active | | |
| "L" Level | No-Oscillation | | |

| Marking | | | | | | | | | | |
|---------|----------------------------------------|------|-------|---------------|-------------|----------------|-------|---------------|----|----|
| Α | Frequency (3digits) | | | (Exan | nple) | | | | | |
| В | Frequency idntification code | | | | ٨ | B (| DE | : = | | |
| С | Output Waveform | | | | 7 | ט ט | , U L | - ' | | |
| | ex; 1[LV-PECL] | | | 156 A 1 B B 1 | | | | | | |
| D | Supply Voltage | | | | | | | | | |
| | ex; B[2.5V] | | | | | | | | | |
| Е | Multiplying function | | | | | KC | 20 | 1 | | |
| | ex; A[No multiplying], B[1/2],C[1/4] | | | | → [. | : | | ' | | |
| F | Management No. | | | | | 1 — | 1 | $\overline{}$ | | |
| G | Pin No.1 indication | | | | G | H | ď | | | |
| Н | Making Company Abbreviation | | | | | | | | | |
| I | Lot code | | | | | | | | | |
| | Manufacturing year and week are shown. | | | | | | | | | |
| | code | year | month | | | | Week | | | |
| | 201 | | | | | 1 | 2 | 3 | 4 | 5 |
| | 202 | 2012 | 1 | 6 | 7 | 8 | 9 | 10 | 11 | 12 |
| | 203 | |] | 13 | 14 | 15 | 16 | 17 | 18 | 19 |
| | ex; 2012,The first week : 201 | | | | | | | | | |

4. Parts Numbering Guide

KC7050Y xxx.xxx P 2 0 E ZU

Α В CDEF G

- A: Series (6pad SMD SAW OSC)
- B: Oscillating frequency
- C: Output

P [LV-PECL]

D: Supply voltage

<u>2</u> [2.5V]

E: Frequency stability (*Over all condition)

*Over all conditions:

<u>0</u> [±50ppm]

E [Duty 45% to 50% with stanby function] G: Customer special model suffix **ZU** [Custom specification]

F: Duty ratio and Enable/Disable function

Initial tolerance, operating temperature range, rated power supply voltage change, load change, aging(1year @25°C), shock and vibration.

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5. Environmental Characterist

| Items | Conditions | Criteria of Acceptance | | |
|--------------------------------------------------------|------------------------------------------------------------|-----------------------------------------------|--|--|
| F 1 Coldorability | Soaking: | Dipped potion: | | |
| 5-1. Solderability | 245±5℃, 5.0±0.5sec | Minimum 95% coverage | | |
| | Reflow Soldering: | | | |
| 5-2. Soldering Heat | Peak 260℃ max, 10sec, Twice max | Without looseness or crack et | | |
| Resistance | Soldering iron: | | | |
| Nesisiance | 380±5℃, 3+1/-0sec, | | | |
| | Twice as one time for four Pads | | | |
| 5-3. Temperature Cycle | 10Cycles: | | | |
| 5-5. Temperature Cycle | -55℃ to +125℃ (30minuts each)/cycle | _ | | |
| 5-4. Mechanical | 5 times | | | |
| Shock (Pulse) | 14750m/sec ² (1500G), Duration of pulse 0.5msec | | | |
| SHOCK (Fulse) | (MIL-STD-883D-2002.3 Condition B) | | | |
| | 4 times each axis X, Y, Z: | | | |
| 5-5. Vibration | 20 to 2000Hz and 2000Hz to 20Hz/cycle | Clause 5-10 shall be satisfied. | | |
| 5-5. VIDIALION | Peak acceleration 196m/sec ² (20G) | _ | | |
| | (MIL-STD-883D-2007.2 Condition A) | | | |
| 5-6. High Temperature | 1000 hours: | | | |
| 5-6. High remperature | Temperature: 85+5/-3℃ | | | |
| F. 7. Law Tamparatura | 1000 hours: | | | |
| 5-7. Low Temperature | Temperature: -40+5/-3℃ | | | |
| | 10 cycles: | | | |
| 5-8. Humidity Cycle | Based on 1004 specifications | Clause 5-1 shall be satisfied. | | |
| | (MIL-STD-883D-1004.7) | | | |
| 5-9. Hermeticity 1 | Soaking: | No hubbles appeared | | |
| (Gross leak) | 110±5℃, 5minutes | No bubbles appeared | | |
| 5-10. Hermeticity 2 Measured by Helium Detector Device | | 5x10 ⁻⁹ Pa m ³ /sec max | | |
| (Fine leak) | (MIL-STD-883D-1014.10 Condition A1) | JATO FAIII/SECIIIAX | | |

Note:After above Test, it shall be subjected to standard atmospheric conditions for 2 hours, after which measurement shall be made. And result of the test shall satisfy **Table 1**

Table2

6. Recommended Land pattern and soldering Guide

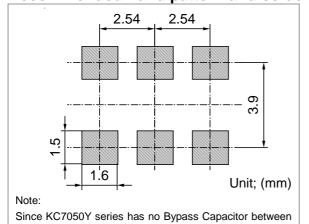


Fig.4 Land pattern

V_{CC} and GND, Please mount high frequency type capacitor

 $0.01\mu F$ and $10\mu F$ to the nearest position of oscillator.

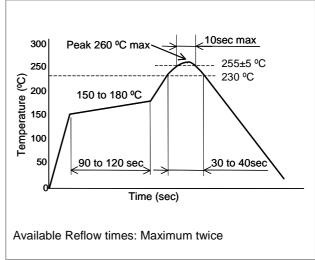


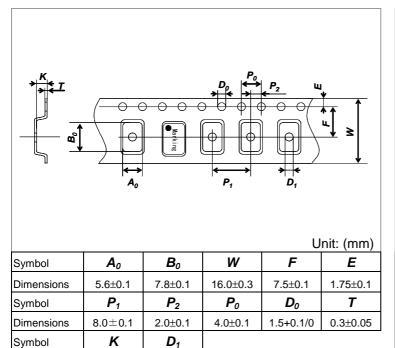
Fig.5 Reflow profile (Lead Free Available)

- <Reflow Condition>
- · Solder melting point 183℃
- <Solder Heat Resistance>
- Maximum 260℃ / Maximum 10sec or Maximum 230℃ / Ma ximum 60sec.

7. Taping Specifications

7-1. Taping Quantities:

- The tape of one reel shall pack with maximum 500 pcs.
- KC7050Y shall be contained continuously in pocket.



D O O A W

| | | · | ınıt: (mm) |
|------------|----------|---------------------|------------|
| Symbol | Α | В | С |
| Dimensions | φ 330±2 | φ 100±1 | φ 13±2 |
| Symbol | D | E | F |
| Dimensions | 2.0±0.5 | ϕ 21 \pm 0.8 | 120° |
| Symbol | W | T | |
| Dimensions | 17.5±0.5 | 2±0.5 | |

Fig.6 Emboss Carrier

1.55±0.05

Fig.7 Reel

7-2. Leader and Blank Pocket

1.9±0.1

Dimensions

- Package shall consist of leader, blank pocket and loaded pocket as follows. "Fig.8"
- The power peeling top tape from carrier one shall be 0.1N {10gf} to 0.7N {70gf}. "Fig.9"

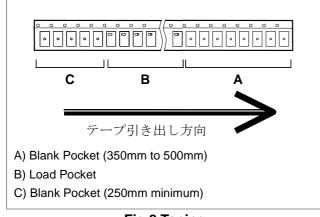


Fig.8 Taping

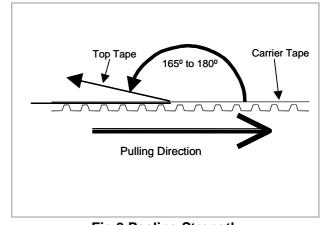


Fig.9 Peeling Strength

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8. Package

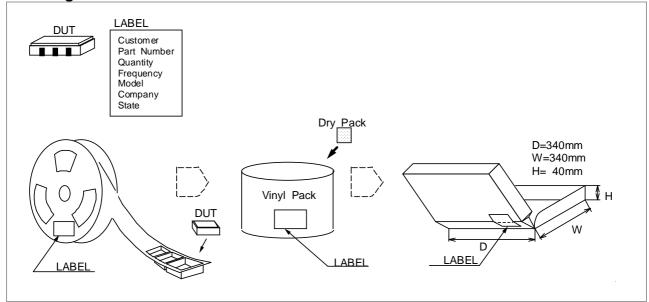


Fig.10 Package

9. The agreement of this specifications

If you find further points in this specifications, contact us within 45 days after the date of issue.

10. Remarks on Usage

10-1. Storage Condition

Parts should be stored in temperature range of -5 to +40℃, humidity 40 to 60% RH, and avoid direct sunlight. Then use within 6 months.

10-2. Handling Condition

Although KC7050Y has protection circuit against static electricity, when excess static electricity is applied, the inside IC may get damaged.

When mounting on PCB, please make sure the direction of KC7050Y is correct, otherwise KC7050Y will increase in temperature and may damaged.

Please do not use KC7050Y under unfavorable condition such as beyond specified range in catalogue or specification sheet.

When using an auto-mounting machine, select the one which give silent impulse as little as possible to the relevant components and operate it with much attentive confirmation so that it may not cause damaged.

After making the KC7050Y mounted on a printed circuit board, if it is required to divide the printed circuit board into another one, use it with attentive confirmation so that a warp cased by this division might not affect any damage. When designing a printed circuit board as well as handling the mounting location, the printed circuit board has to be being stress free area as much as possible.

Please do not use KC7050Y under condition in the water or salt water will drop on KC7050Y and under environment of dew or harmful gas.

10-3. Soldering

Please use KC7050Y under condition "IR or Vapor phase Reflow "only.

10-4. Washing Condition

If KC7050Y is applied ultrasonic, it may be inferior and destroy.

Please don't use ultrasonic cleaner.

In case of using KC7050Y without above precaution, Kyocera is unable to guarantee the specified characteristics.

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