Specification

Drawing No.	OKY1W-H1-14043-00
Issued Date.	Dec. 15,2014

To: Digi-Key

Note: In case of specification change, KYOCERA Part Number also will be changed.

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Product Name	SAW Oscillator			
Product Model	KC7050T Series			
Frequency	212.500MHz			
Customer Part Number				
Customer Specification Number				
KYOCERA Part Number	KC7050T212.500P3AEYG			
Remarks: Pb-Free,RoHS Compliant.,MSL=1				

Customer Acceptance

Accept Signature	Accept Date	
	Department	
	Person in charge	

KYOCERA Crystal Device Corporation

Head Office Sales Division

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Design Department	Quality	Approved by	Checked by	Issued by
KYOCERA Crystal Device Corporation	Assurance	Approved by	Checked by	issued by
Oscillators Division	a . m	20.1	1	44.0
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Revision History

Dec.15.2014 W. Jakene J. Baite M. Jakikana	Rev.No.	Description of revise	Date	Approved by	Checked by	Issued by
	00	First edition	Dec.15,2014	N. Takeno	T.Saito	II. Ishikawa

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1. Application

This specification delivers Digi-Key SAW Oscillator, KC7050T212.500P3AEYG applies to 212.500MHz.

2. Function

2-1. Absolute Maximum Rating

Item	Symbol	Rating	Unit
Power Supply Voltage	V_{DD}	-0.5 to +5.0	٧
Input Voltage	V _{IN}	-0.5 to V _{CC} +0.5	٧
Storage Temperature Range	T _{STG}	-55 to +125	ç

Note: If KC7050T is used beyond absolute maximum ratings, it may cause internal destruction.

KC7050T should be used under the recommended operating conditions. KC7050T reliability may be damaged if those conditions are exceeded.

2-2. Recommended Operating Condition

Item	Symbol	Min	Тур	Max	Unit	Remarks
Power Supply Voltage	V _{CC}	2.97	3.3	3.63	V	
Input Voltage	V _{IN}	0		V _{CC}	٧	
Operating Temperature Range	T _{OPR}	-5	+25	+85	°C	

2-3. Electrical Characteristic Specifications

Item	Symbol	Min	Тур	Max	Unit	Remarks
Frequency Range	F _{OUT}		212.500		MHz	
Frequency Stability	F _{SBY}	-100		+100	ppm	*Over all conditions: Initial tolerance, operating temperature range, rated power supply voltage change, load change, aging (10year @0~85°C), shock and vibration
Current Consumption	Icc		55	90	mA	
Standby Current	I _{ST}			30	μΑ	
Duty ratio (Symmetry)	SYM	45	50	55	%	50ohm, @ crossing point
Rise Time (20% to 80% Output Level)	Tr		0.4	0.6	nS	50ohm
Fall Time (20% to 80% Output Level)	Tf		0.4	0.6	113	3001111
Output Voltage -"L"	V _{OL}	V _{CC} -1.810 V _{CC} -1.830		V _{CC} -1.620 V _{CC} -1.555	V	0 to +85°C, DC characteristic. -5 to +0°C, DC characteristic.
Output Voltage -"H"	V _{OH}	V _{CC} -1.025 V _{CC} -1.085		V _{CC} -0.880 V _{CC} -0.900	V	0 to +85°C, DC characteristic. -5 to +0°C, DC characteristic.
Output Load			50		ohm	LV-PECL Output
Input Voltage -"L"	V _{IL}			30% V _{CC}	٧	OE termination
Input Voltage -"H"	V_{IH}	70% V _{CC}			V	OE terrimation
Output Disable Time				200	nS	
Output Enable Time			2	10	mS	
Start up time	ST		2	10	mS	@Minimum operating voltage to be 0sec
Deterministic Jitter*	DJ		0.2	2		DJ pk-pk
1sigma Jitter*	1sigma		2	4	pS	
Peak to Peak Jitter*	Pk-Pk		20	30		

Note: All Electrical characteristics define Maximum Loaded and operating temperature range.

Table 1

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^{*}The Time Interval Analyzer "Wavecrest DTS-2079" with VISI 6.3.1 shall measure jitter.

2-4. Measurement Condition

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests are as follows:

Ambient temperature : +15°C to +35 °C
 Relative Humidity : 25% to 85%
 Air pressure : 86kPa to 106kPa

If there is any doubt about the results, measurements shall be made within the following limits:

Ambient temperature : +25°C
 Relative Humidity : 60% to 70%
 Air pressure : 86kPa to 106kPa

Unless otherwise specified for each item, it should be specified according to JIS (Japanese industrial Standard).

2-5. Measurement Circuit

The test circuit as shown in "Fig. 1" (*Jitter Test Circuits "Fig. 3") shall measure electric characteristics.

ECL Termination 10086A (Agilent)

2-6. Clock Timing Chart

The clock timing chart as shown in "Fig. 2".

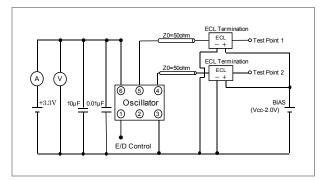


Fig.1 Test Circuits

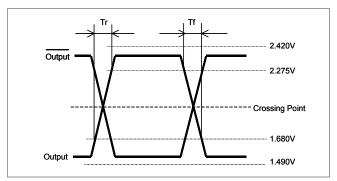


Fig.2 Clock Timing Chart (PECL Output)

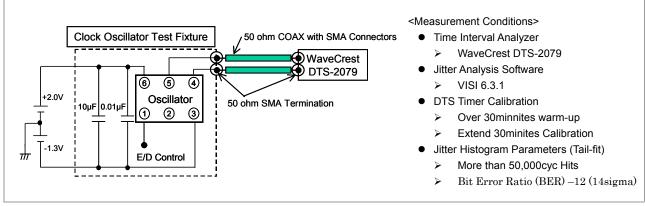
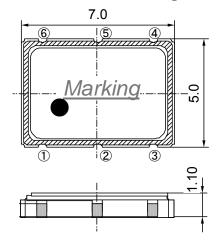
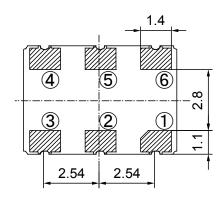


Fig.3 Jitter Test Circuits

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3. Dimensions and Marking





Tolerance; +/-0.2 Unit; (mm)

Pad	Pad arrangement					
1	Enable / Disable					
2	NC					
3	GND					
4	Output					
(5)	Complementary Output					
6	V _{CC}					

Enable / Disable Function				
Pad1	Pad4 / Pad5			
OPEN	Active			
"H" Level	Active			
"L" Level	No-Oscillation			

Marking							
Α	Frequency	/ (3digits	3)	(Exampl	e)		
В	Frequency	/ idntific		Α	ВС	DE	
С	Output Wa	Output Waveform			Î	ĬĬ	ĪĪ
	ex; 1[LV-PECL]				₩	\downarrow	
D	Supply Vo	Supply Voltage				A 1	- AD:
	ex; A[3.3V], B[2.5V]			'			AB
Е	Multiplying	g functio)	KSS	401	
	ex; A[No r	nultiplyir			A	▲	
F	Pin No.1 indication			1 +			+
G	Making Co	Making Company Abbreviation				Ġ	H
Н	Lot code Manufacturing year and week are shown. The 1st digit shows year, 2nd to 3rd digit shows week.						
	Lot code	Year	Week				
	401		1st week				
	402 ≈	402 2014 2nd weel ≈		(

4. Parts Numbering Guide

KC7050T 212.500 P 3 A E YG

A B CDEFG

- A: Series (6pad SMD SAW OSC)
- B: Oscillating frequency
- C: Output

P [LV-PECL]

D: Supply voltage

<u>3</u> [3.3V]

- E: Frequency stability (*Over all condition)

 <u>A</u> [±100ppm]
- F: Duty ratio and Enable/Disable function

 <u>E</u> [Duty 45% to 55% with stanby function]
- G: Customer special model suffix YG [Custom specification]

27th week

*Over all conditions:

Initial tolerance, operating temperature range, rated power supply voltage change, load change, aging(10year @0 \sim +85 $^{\circ}$ C), shock and vibration.

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ex; 2014, The 1st week : 401

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5. Environmental Characterist

Items	Conditions	Criteria of Acceptance	
F 1 Coldorability	Soaking:	Dipped potion:	
5-1. Solderability	+245±5°C, 5.0±0.5sec	Minimum 95% coverage	
	Reflow Soldering:	Without looseness or crack etc.	
5-2. Soldering Heat	Peak 260°C max, 10sec, Twice max		
Resistance	Soldering iron:		
resistance	+380±5°C, 3+1/-0sec,		
	Twice as one time for four Pads		
5-3. Temperature Cycle	10Cycles:		
5-5. Temperature Cycle	-55°C to +125°C (30minuts each)/cycle		
5-4. Mechanical	5 times		
	14750m/sec ² (1500G), Duration of pulse 0.5msec		
Shock (Pulse)	(MIL-STD-883D-2002.3 Condition B)		
	4 times each axis X, Y, Z:		
5-5. Vibration	20 to 2000Hz and 2000Hz to 20Hz/cycle	Clause 5-10 shall be satisfied.	
5-5. VIDIALION	Peak acceleration 196m/sec ² (20G)		
	(MIL-STD-883D-2007.2 Condition A)		
5-6. High Temperature	1000 hours:		
5-6. High remperature	Temperature:+85+5/-3°C		
F. 7. Law Tamparatura	1000 hours:		
5-7. Low Temperature	Temperature: -40+5/-3°C		
	10 cycles:	Clause 5-1 shall be satisfied.	
5-8. Humidity Cycle	Based on 1004 specifications		
	(MIL-STD-883D-1004.7)		
5-9. Hermeticity 1	Soaking:	No bubbles appeared	
(Gross leak)	+110±5°C, 5minutes		
5-10. Hermeticity 2). Hermeticity 2 Measured by Helium Detector Device		
(Fine leak)	(MIL-STD-883D-1014.10 Condition A1)	5x10 ⁻⁹ Pa m ³ /sec max	

Note:After above Test, it shall be subjected to standard atmospheric conditions for 2 hours, after which measurement shall be made. And result of the test shall satisfy **Table 1**

Table2

6. Recommended Land pattern and soldering Guide

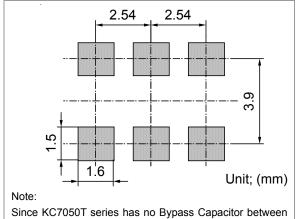


Fig.4 Land pattern

V_{CC} and GND, Please mount high frequency type capacitor

 $0.01\mu F$ and $10\mu F$ to the nearest position of oscillator.

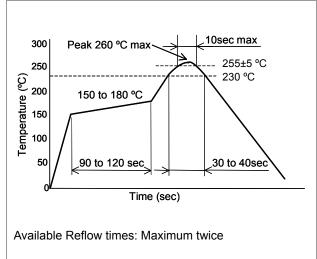


Fig.5 Reflow profile (Lead Free Available)

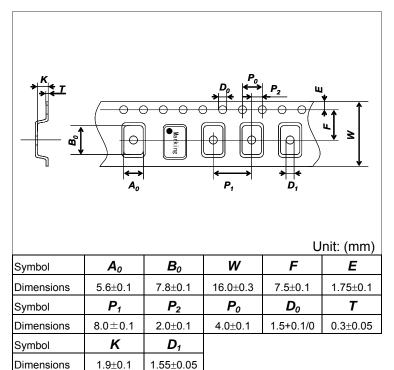
- <Reflow Condition>
- · Solder melting point +183°C
- <Solder Heat Resistance>
- Maximum +260°C / Maximum 10sec or Maximum +230°C / Maximum 60sec.

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7. Taping Specifications

7-1. Taping Quantities:

- The tape of one reel shall pack 1,000 pcs.
- KC7050T shall be contained continuously in pocket.



D	
000	
	a
	w
A	T

Unit: (mm)

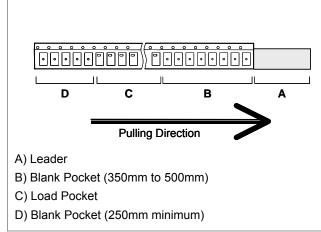
Symbol	Α	В	С	
Dimensions	φ 330±2	φ 100±1	φ 13±0.2	
Symbol	D	E	F	
Dimensions	2.0±0.5	$\phi 21 \pm 0.8$	120°	
Symbol	W	T		
Dimensions	16.4±0.5	2±0.5		

Fig.7 Reel

Fig.6 Emboss Carrier

7-2. Leader and Blank Pocket

- Package shall consist of leader, blank pocket and loaded pocket as follows. "Fig.8"
- The power peeling top tape from carrier one shall be 0.1N {10gf} to 0.7N {70gf}. "Fig.9"



Top Tape 165° to 180°

Pulling Direction

Fig.9 Peeling Strength

Fig.8 Taping

8. Package

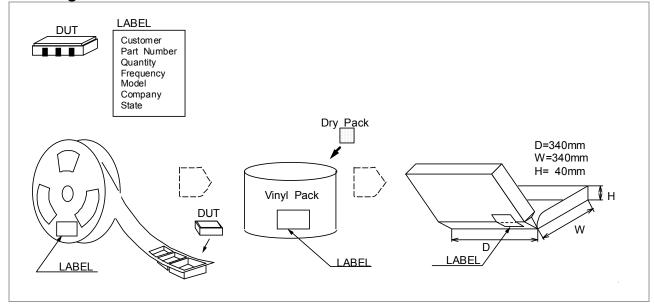


Fig.10 Package

9. The agreement of this specifications

If you find further points in this specifications, contact us within 45 days after the date of issue.

10. Remarks on Usage

10-1. Storage Condition

Parts should be stored in temperature range of -5 to +40°C, humidity 40 to 60% RH, and avoid direct sunlight. Then use within 6 months.

10-2. Handling Condition
Although KC7050T has protection circuit against static electricity, when excess static electricity is applied, the inside IC may get damaged.

When mounting on PCB, please make sure the direction of KC7050T is correct, otherwise KC7050T will increase in temperature and may damaged.

Please do not use KC7050T under unfavorable condition such as beyond specified range in catalogue or specification sheet.

When using an auto-mounting machine, select the one which give silent impulse as little as possible to the relevant components and operate it with much attentive confirmation so that it may not cause damaged.

After making the KC7050T mounted on a printed ciruit board, if it is required to divide the printed circuit board into another one, use it with attentive confirmation so that a warp cased by this division might not affect any damage. When designing a printed circuit board as well as handling the mounting location, the printed circuit board has to be being stress free area as much as possible.

Please do not use KC7050T under condition in the water or salt water will drop on KC7050T and under environment of dew or harmful gas.

10-3. Soldering

Please use KC7050T under condition " IR or Vapor phase Reflow " only.

10-4. Washing Condition

If KC7050T is applied ultrasonic, it may be inferior and destroy.

Please don't use ultrasonic cleaner.

In case of using KC7050T without above precaution, Kyocera is unable to guarantee the specified characteristics.

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