

TSL13FN

Light-to-Voltage Converters

General Description

The TSL13FN is a cost optimized, highly integrated light-to-voltage optical sensor that contains a photodiode and a transimpedance amplifier with a 20 M Ω feedback resistor on a single monolithic integrated circuit. The photodiode active area is 0.5mm x 0.5mm and the sensor responds to visible light in the range of 300 nm to 1100 nm. The output voltage is linear with light intensity (irradiance) incident on the sensor over a wide dynamic range. These devices are supplied in a surface mountable package.

Ordering Information and Content Guide appear at end of datasheet.

Key Benefits & Features

The benefits and features of TSL13FN are listed below:

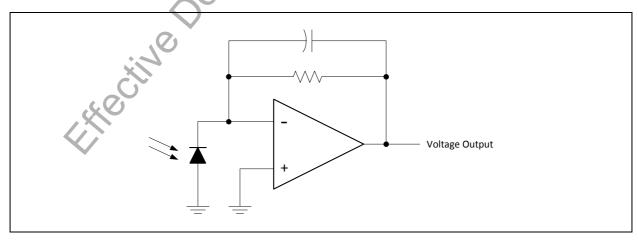
Figure 1: Added Value of Using TSL13FN

Benefits	Features
Approximates Human Eye Response	Single Diode
High Irradiance Responsivity to Green	18.7 mV/(μ W/cm ²) at $\lambda_p = 525$ nm
Low Supply Current	0.8mA Typical

Block Diagram

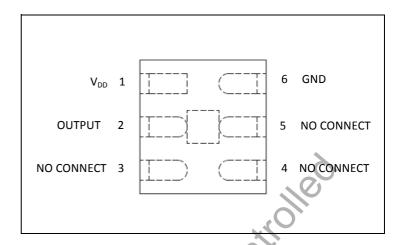
The functional blocks of this device for reference are shown below:

Figure 2: TSL13FN Block Diagram



Pin Assignment

Figure 3: Pin Diagram (Top View)



Pin Description

Figure 4: Pin Description

Pin Number	Pin Name	Description
1	V _{DD}	Supply voltage
2	OUT	Output voltage
3	N/C	No connection
4	N/C	No connection
5	N/C	No connection
6	GND	Power supply ground. All voltages are referenced to GND
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Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated under "Operating Conditions" is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Figure 5:
Absolute Maximum Ratings

Symbol	Parameter	Min	Max	Units	Comments
V _{DD}	Supply voltage		6	V	All voltages are with respect to GND
lo	Output current	-10	10	mA	
T _{stg}	Storage temperature range	-40	85	Ŷ	*{O
	Storage temperature range				

Electrical Characteristics

All limits are guaranteed. The parameters with min and max values are guaranteed with production tests or SQC (Statistical Quality Control) methods.

Figure 6: Recommended Operating Conditions

Symbol	Parameter	Min	Тур	Max	Units
V _{DD}	Supply voltage	2.7		5.5	V
T _A	Operating free-air temperature	-30		70	°C

Figure 7: Operating Characteristics, $V_{DD} = 5 \text{ V}$, $T_A = 25 \,^{\circ}\text{C}$, $\lambda_p = 525 \text{ nm}$ (unless otherwise noted) (1), (2), (3)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
V _{OM}	Maximum output voltage		4.6	4.9		V
Vo	Output voltage	$\lambda_p = 525 \text{ nm}$ $E_e = 107 \mu\text{W/cm}^2$ 1.5 2	2.5	V		
V O	Odipat voltage	$\lambda_p = 850 \text{ nm}$ $E_e = 70.5 \mu\text{W/cm}^2$	1.5 2	2.5	V	
R _e	Irradiance responsivity	$\lambda_{p} = 525 \text{nm}^{(4)}$		18.7		mV/(μW /cm²)
V _{OS}	Extrapolated offset voltage	see note (4)	-0.02	0.03	0.08	V
V_{d}	Dark voltage	$E_e = 0 \mu\text{W/cm}^2$	0		0.08	V
I _D	Supply current	$\lambda_p = 525 \text{ nm}$ $E_e = 107 \mu\text{W/cm}^2$		0.8	1.3	mA

Note(s) and/or Footnote(s):

- 1. Optical measurements are made using small-angle incident radiation from an LED optical source.
- 2. The 525 nm irradiance Ee is supplied by a InGaN light-emitting diode with the following typical characteristics: peak wavelength $\lambda p = 525$ nm and spectral halfwidth $\Delta \lambda \frac{1}{2} = 40$ nm.
- 3. The 850 nm irradiance Ee is supplied by a light-emitting diode with the following typical characteristics: peak wavelength $\lambda p = 850$ nm and spectral halfwidth $\Delta \lambda 2 = 42$ nm.
- 4. Irradiance responsivity is characterized over the range VO = 0.2 to 4 V. The best-fit straight line of output voltage VO versus irradiance Ee over this range may have a positive or negative extrapolated VO value for Ee = 0. For low irradiance values, the output voltage VO versus irradiance Ee characteristic is nonlinear with a deviation toward VO = 0, Ee = 0 origin from the best-fit straight line referenced above.

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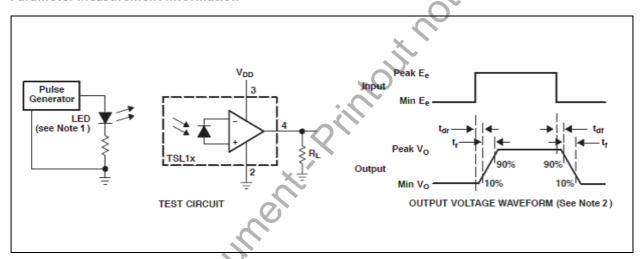


Figure 8: Dynamic Characteristics, V_{DD} = 5 V, T_A = 25°C, λ_p = 525 nm, R_L = 10 k Ω (unless otherwise noted) (see also Figure 9)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t _{dr}	Output pulse delay time for rising edge (0% to 10%)	Min $V_O = 0V$; Peak $V_O = 2V$ Min $V_O = 0.5V$; Peak $V_O = 2V$		1.7 1.2		μs
t _r	Output pulse rise time (10% to 90%)	Min $V_O = 0V$; Peak $V_O = 2V$ Min $V_O = 0.5V$; Peak $V_O = 2V$		7.2 6.5		μs
t _{df}	Output pulse delay time for falling edge (100% to 90%)	Min $V_O = 0V$; Peak $V_O = 2V$ Min $V_O = 0.5V$; Peak $V_O = 2V$		1.2 1.1	8	μs
t _f	Output pulse fall time (90% to 10%)	Min $V_O = 0V$; Peak $V_O = 2V$ Min $V_O = 0.5V$; Peak $V_O = 2V$		6.8 6.4		μs

Timing Diagrams

Figure 9: **Parameter Measurement Information**



Note(s) and/or Footnote(s):

- 1. The input irradiance is supplied by a pulsed InGaN light emitting diode with the following characteristics: $\lambda_p = 525$ nm, $t_r < 1$ μs ,
- 2. The output waveform is monitored on an oscilloscope with the following characteristics: $t_r < 100$ ns, $Z_i \ge 1$ M Ω , $C_i \le 20$ pF.

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Typical Operating Characteristics

Figure 10: Spectral Responsivity

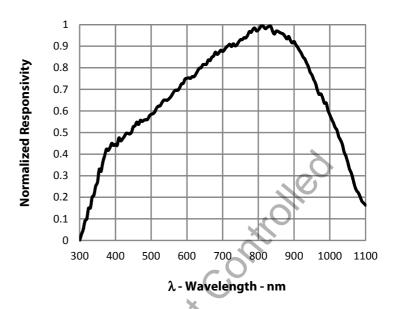
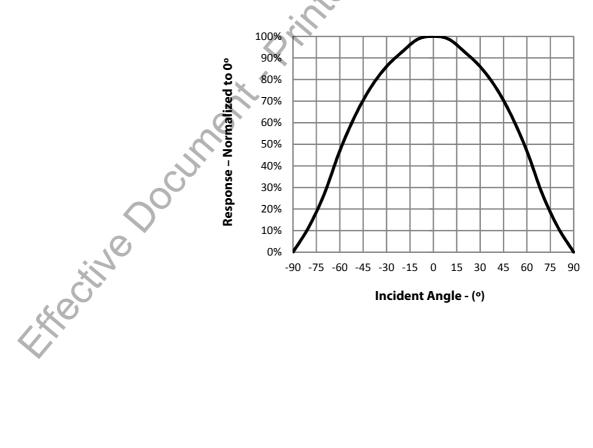


Figure 11:
Normalized Output Voltage vs. Angular Response



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Figure 12: Rising Edge Dynamic Characteristics vs. Peak Output Voltage (Min $V_0 = 0V$)

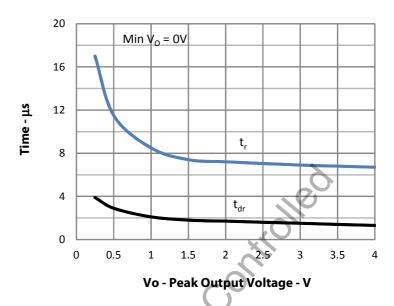


Figure 13:
Rising Edge Dynamic Characteristics vs. Peak Output Voltage (Min V_O 7 0.5V)

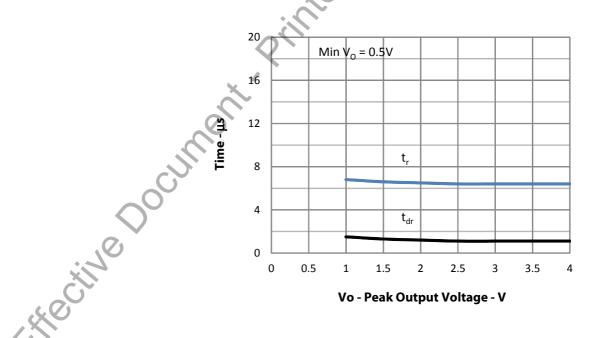


Figure 14: Falling Edge Dynamic Characteristics vs. Peak Output Voltage (Min $V_0 = 0V$)

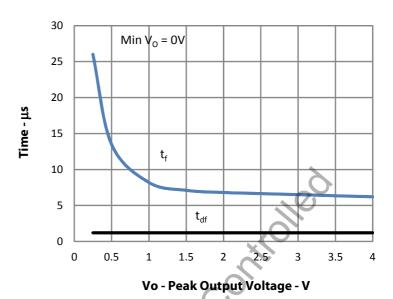
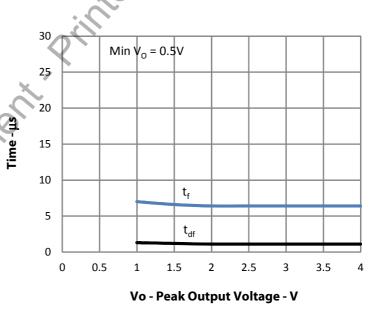


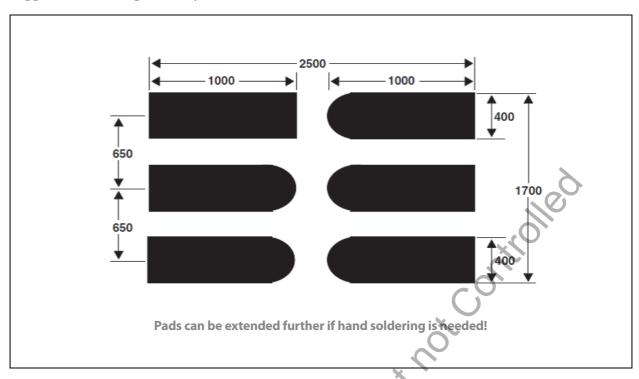
Figure 15:
Falling Edge Dynamic Characteristics vs. Peak Output Voltage (Min V_O = 0.5V)





PCB Pad Layout

Figure 16: Suggested FN Package PCB Layout

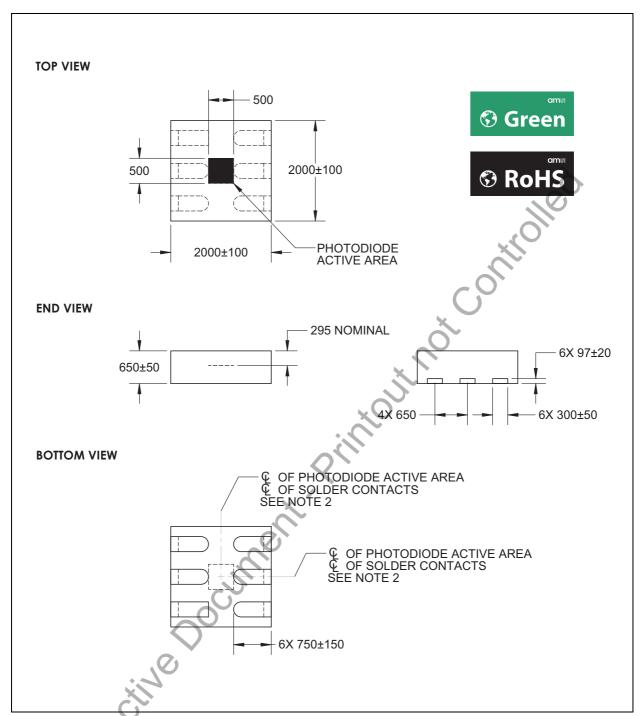


Note(s) and/or Footnote(s):

- 1. All linear dimensions are in millimeters.
- 2. This drawing is subject to change without notice.

Package Drawings & Markings

Figure 17:
Six-lead Surface Mount FN Package Configuration



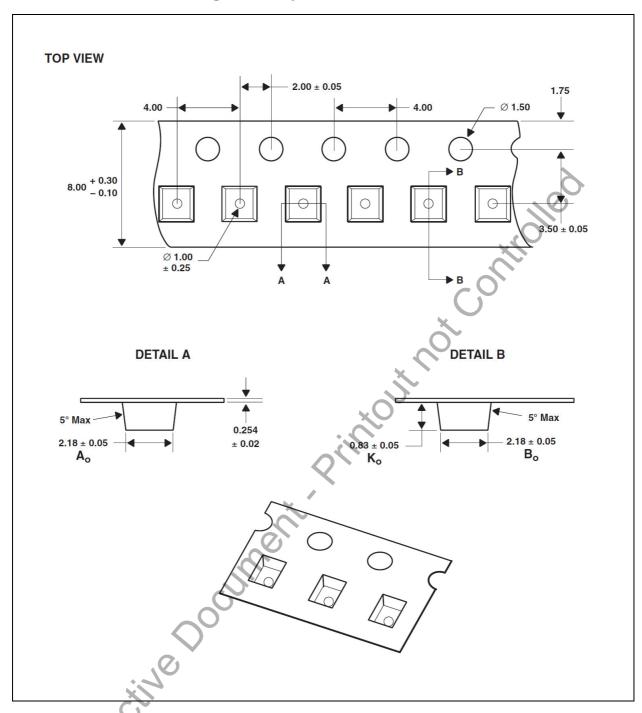
Note(s) and/or Footnote(s):

- 1. All linear dimensions are in micrometers.
- 2. The Photodiode Active Area is centered within the package within a tolerance of ± 75 micrometers.
- 3. Package top surface is molded with an electrically nonconductive clear plastic compound having an index of refraction of 1.55.
- 4. Contact finish is Copper Alloy A194 with pre-plated NiPdAu lead finish.
- 5. This package contains no lead (Pb).
- 6. This drawing is subject to change without notice.



Mechanical Data

Figure 18:
Six-lead Surface Mount FN Package Carrier Tape



Note(s) and/or Footnote(s):

- 1. All linear dimensions are in millimeters.
- 2. The dimensions in this drawing are for illustration purposes only. Dimensions of an actual carrier may vary slightly.
- 3. Symbols on drawing $\rm A_{0}, \, B_{0}$ and $\rm K_{0}$ are defined in ANSI EIA standard 481-B 2001.
- 4. Each reel is 178 millimeters in diameter and contains 1000 parts.
- 5. ams packaging tape and reel conform to the requirements of EIA 481-B.
- 6. In accordance with EIA standard, the device pin1 is located next to the sprocket holes in the tape.
- 7. This drawing is subject to change without notice.

Soldering & Storage Information

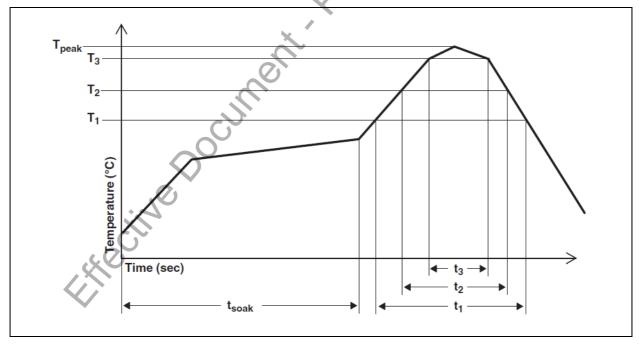
Soldering Information

The reflow profile specified here describes expected maximum heat exposure of devices during the solder reflow process of the device on a PCB. Temperature is measured at the top of the device. Devices should be limited to one pass through solder reflow profile.

Figure 19: Solder Reflow Profile

Symbol	Parameter	TSL13FN
	Average temperature gradient in preheating	2.5°C/sec
t _{soak}	Soak time	2 to 3 minutes
t ₁	Time above T ₁ , 217°C	Max 60 sec
t ₂	Time above T ₂ , 230°C	Max 50 sec
t ₃	Time above T ₃ , (T _{peak} - 10°C)	Max 10 sec
T _{peak}	Peak temperature in reflow	260°C(-0°C/+5°C)
	Temperature gradient in cooling	Max -5°C/sec

Figure 20: Solder Reflow Graph



Note: Not to scale – for reference only.



Storage Information

Moisture Sensitivity

Optical characteristics of the device can be adversely affected during the soldering process by the release and vaporization of moisture that has been previously absorbed into the package. To ensure the package contains the smallest amount of absorbed moisture possible, each device is baked prior to being dry packed for shipping.

Devices are dry packed in a sealed aluminized envelope called a moisture-barrier bag with silica gel to protect them from ambient moisture during shipping, handling, and storage before use.

This package has been assigned a moisture sensitivity level of MSL3 and the devices should be stored under the following conditions

• Temperature Range: 5°C to 50°C

· Relative Humidity: 60% maximum

 Total Time: 12 months from the date code on the aluminized envelope – if opened

· Open Time: 168 hours or less

Re-baking will be required if the devices have been stored unopened for more than 12 months or the aluminized envelop has been open for more than 168hrs. If re-baking is required, it should be done at 50°C for 12 hours.

Ordering & Contact Information

Figure 21: Ordering Information

Device	T _A	Package Description	Package Designator	Ordering Number
TSL13	0 to 70°C	6-lead Surface Mount	FN	TSL13FN

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Document Status

Document Status	Product Status	Definition				
Product Preview	Pre-Development	Information in this datasheet is based on product ideas in the planning phase of development. All specifications are design goals without any warranty and are subject to change without notice				
Preliminary Datasheet	Pre-Production	Information in this datasheet is based on products in the design, validation or qualification phase of development. The performance and parameters shown in this document are preliminary without any warranty and are subject to change without notice				
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Revision Information

Changes from 0-03 (2013-Nov) to current revision 1-00 (2014-Jun-04)	Page
Content was updated to the latest ams design	

Effective Document. Printout not Controlled



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