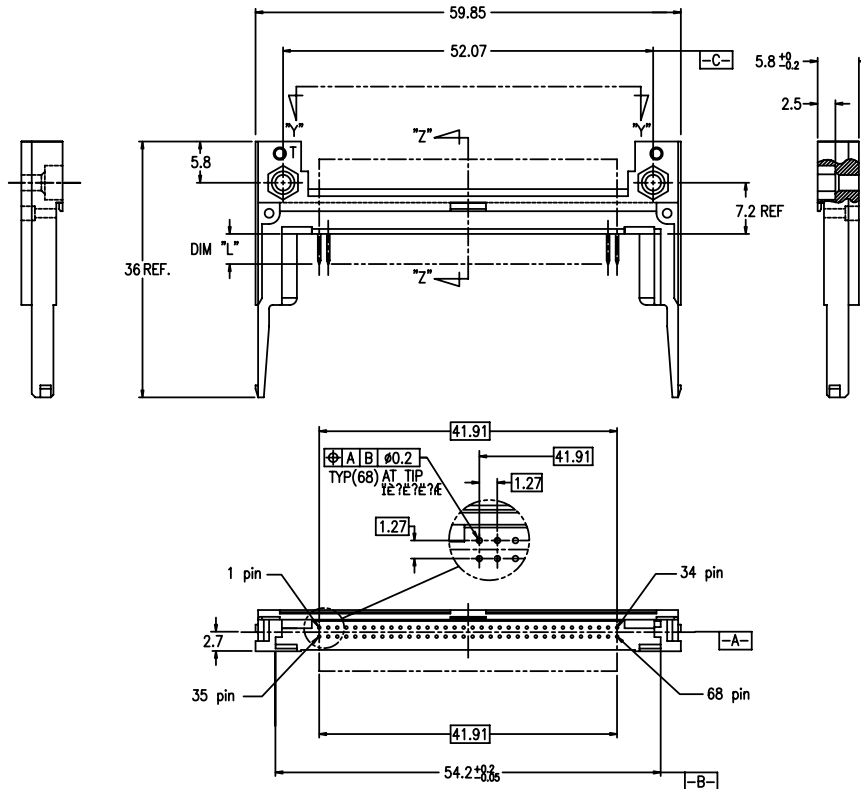


PRODUCT NO.
92140-000
92140-000LF
92140-000H
92140-000HLF
92140-001
92140-001LF
92140-001H
92140-001HLF
92140-002
92140-002LF
92140-002H
92140-002HLF



NOTES

- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
- SEE TA-946 FOR PCB LAYOUT.

- MATERIAL
HOUSING : LCP UL94V-0 BLACK.
PIN : PHOSPHOR BRONZE.
HOLD-DOWN : BRASS

- FINISH
PIN
UNDER PLATING : 0.5μm MIN. Ni.
CONTACT AREA : 0.1μm MIN. Au.
OVER 0.5μm MIN. Pb-Ni.
SOLDER AREA : 2.5μm MIN. Sn-Pb.
OR 2.5μm MIN. PURE Sn.(FOR -XXXLF)
HOLD-DOWN : 2.5μm MIN. PURE Sn.

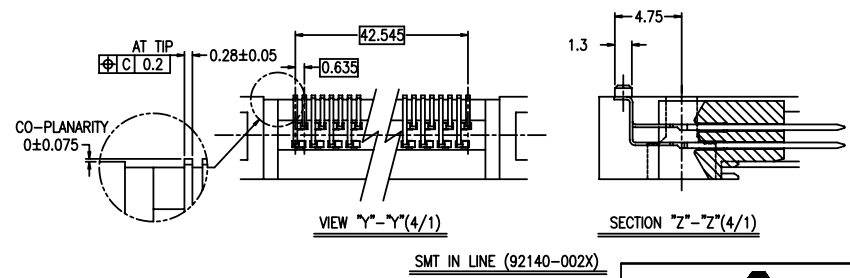
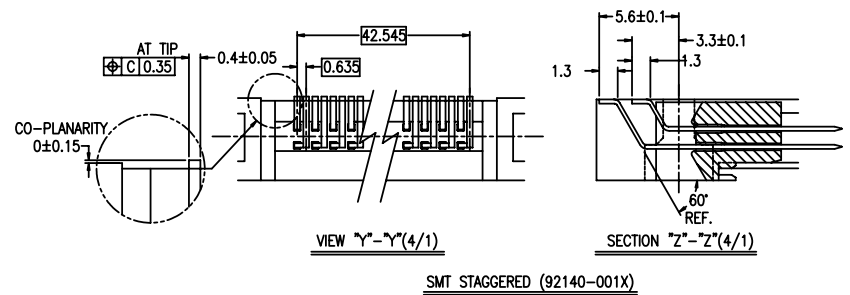
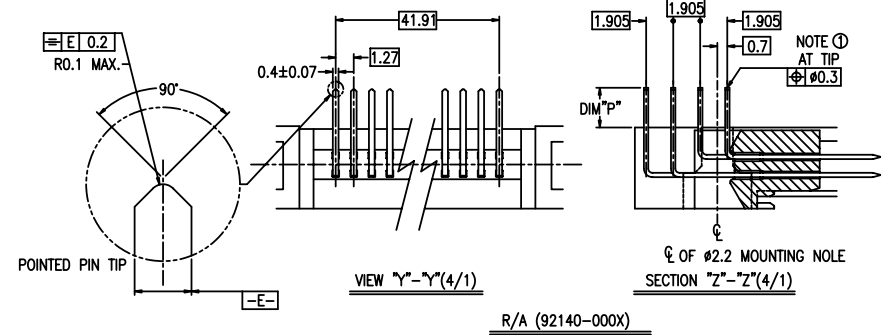
- RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1

- SEQUENCE PIN ASSIGNMENT

	DIM "L"		
PIN No.	OTHERS	36.67	35.51.68

- GENERAL TOLERANCE : ±0.3
- SEE SHEET 1 REGARDING COMPONENTS.

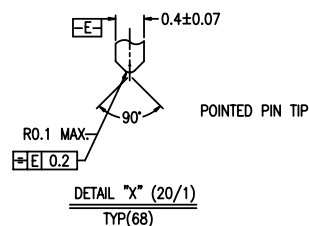
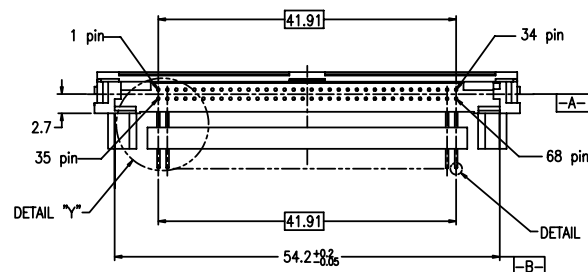
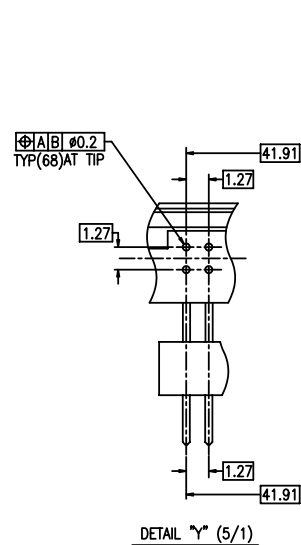
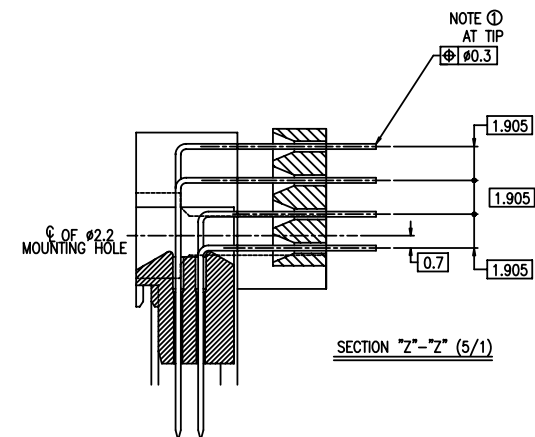
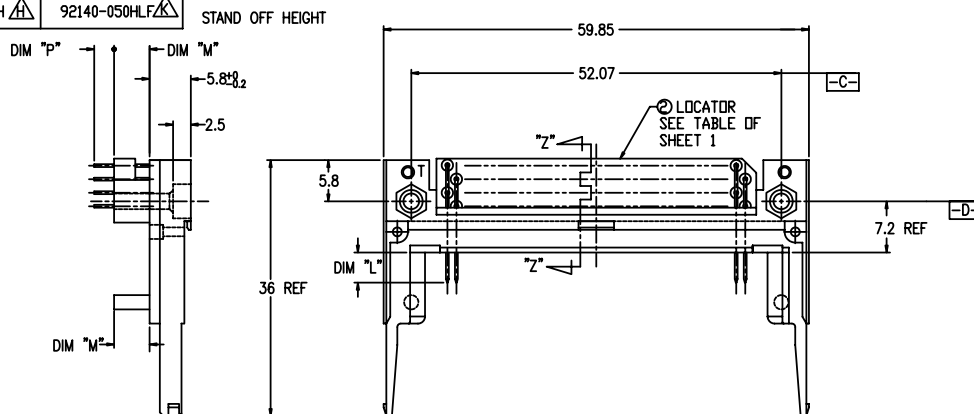
P	REVISED	N05-0046	S.K	2/24/05	REVISIONS			
					REV	DESCRIPTION	ECN	BY DATE
Q	REVISED	N05-0279	M.Z	10/28/05	A	RELEASED	J30043	N.T 2/23/93
					B	REVISED	J30157	Y.Su 5/20/93
					C	REVISED (ADDED -111)	J30135	K.S 8/13/93
					D	REVISED	J40005	K.S 2/ 7/94
					E	REVISED	V32220	M.K 2/16/94
					F	REVISED	V32221	M.K 2/28/94
					G	REVISED	J40068	M.K 3/ 2/94
					H	REVISED	V40645	M.K 6/24/94
					J	REVISED	V40840	M.K 6/24/94
					K	REVISED	J50006	M.K 6/29/94
					L	REVISED	J50039	M.K 3/ 1/95
					M	REVISED	J50097	S.K 8/7/95
					N	REVISED	J80447	S.K 11/5/98



SHEET INDEX	ISSUE	SH NO.	MAY	SEE NOTE ③	DATE	CUSTOMER	TITLE	PRODUCT FAMILY	CODE
LINEAR									
ANGLES									

PRODUCT NO.	PRODUCT NO.
92140-010	92140-010LF
92140-010H	92140-010HLF
92140-020	92140-020LF
92140-020H	92140-020HLF
92140-040	92140-040LF
92140-050	92140-050LF
92140-050H	92140-050HLF

REVISIONS				
REV	DESCRIPTION	ECN	BY	DATE
A	RELEASED	J30043	N.T.	2/18/93
B	REVISED	V32221	M.K.	2/28/94
C	REVISED	J40068	M.K.	3/ 2/94
D	REVISED	V40840	M.K.	6/29/94
E	REVISED	J50008	M.K.	2/9/95
F	REVISED	J50039	M.K.	3/1/95
G	REVISED (ADDED-020H)	V70154	K.S.	8/8/97
H	REVISED (ADDED-020H)	J70134	K.S.	8/8/97
J	REVISED (ADDED-010H)	V81446	S.K.	11/13/98
K	REVISED	N05-0046	M.H.	2/24/05



NOTES

- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
- SEE TA-946 FOR PCB LAYOUT.

③ MATERIAL

HOUSING : LCP UL94V-0 BLACK.
PIN : PHOSPHOR BRONZE.
HOLD-DOWN : BRASS

④ FINISH

PIN
UNDER PLATING : 0.5 μ m MIN. Ni.
CONTACT AREA : 0.1 μ m MIN. Au.
OVER 0.5 μ m MIN. Pb-Ni.
SOLDER AREA : 2.5 μ m MIN. Sn-Pb.
OR 2.5 μ m MIN. PURE Sn.(FOR -XXXLF)
HOLD-DOWN : 2.5 μ m MIN. PURE Sn.

- RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3 $\pm$ 0.1
- SEQUENCE PIN ASSIGNMENT

	DIM "L"
	4.25 $\pm$ 0.1
	3.5 $\pm$ 0.1
	5.0 $\pm$ 0.1
PIN No.	OTHERS
	36.67
	117.94
	35.51.68

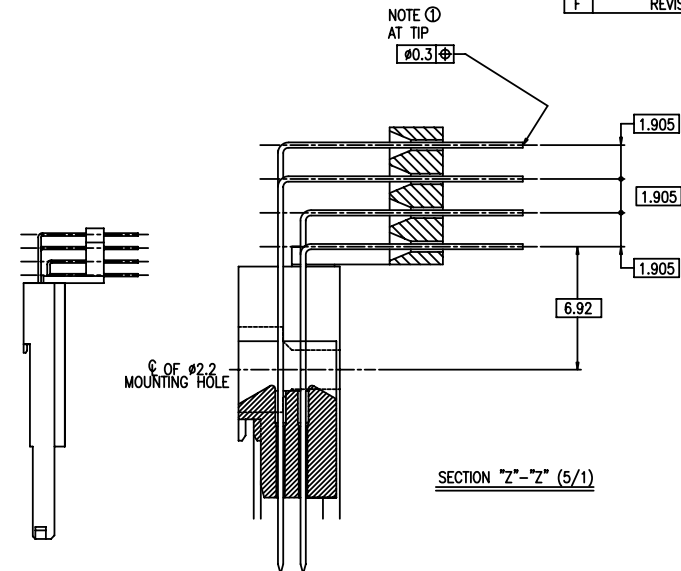
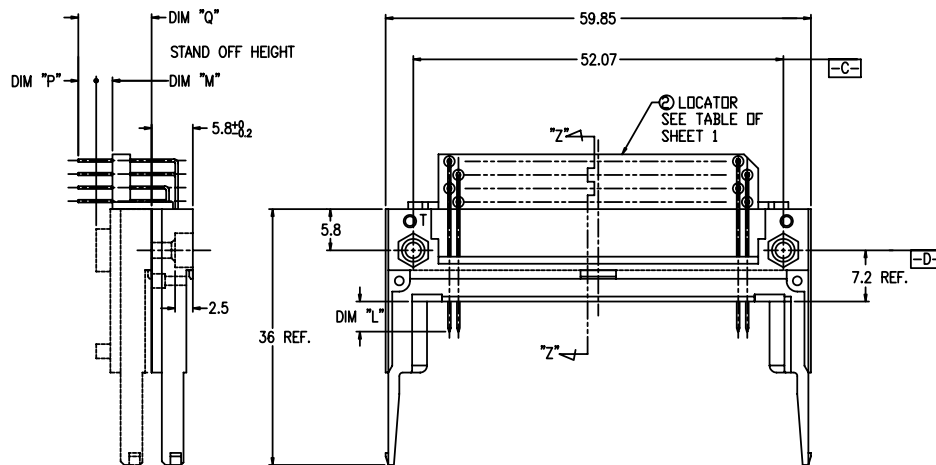
- GENERAL TOLERANCE : $\pm$ 0.3
- SEE SHEET 1 REGARDING COMPONENTS.
- THIS PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008

SHEET INDEX		ISSUE		SH NO.		MAY		DATE		CUSTOMER		TITLE	
TOLERANCES UNLESS OTHERWISE SPECIFIED		SEE NOTE ③		DR		M.Hasegawa		2/24/05		COPY		Header PCMCIA SANF	
LINEAR		FINISH		CHK		M.Hasegawa		2/24/05		THIRD ANGLE PROJ		SCALE	
ANGLES		SEE NOTE ④		APPD						UNIT		2/1	
										SIZE		C	
										DWG. NO.		92140	
										REV		K	
										SHEET		3 of	



PRODUCT NO.
92140-005
92140-005LF 
92140-045
92140-045LF 

REVISIONS				
REV	DESCRIPTION	ECN	BY	DATE
A	RELEASED	J30043	N.T.	2/23/93
B	REVISED	V32221	M.K.	2/28/94
C	REVISED	J40068	M.K.	3/ 2/94
D	REVISED	J50008	M.K.	2/ 9/95
E	REVISED	J50039	M.K.	3/ 1/95
F	REVISED	N05-0048	M.K.	2/24/05

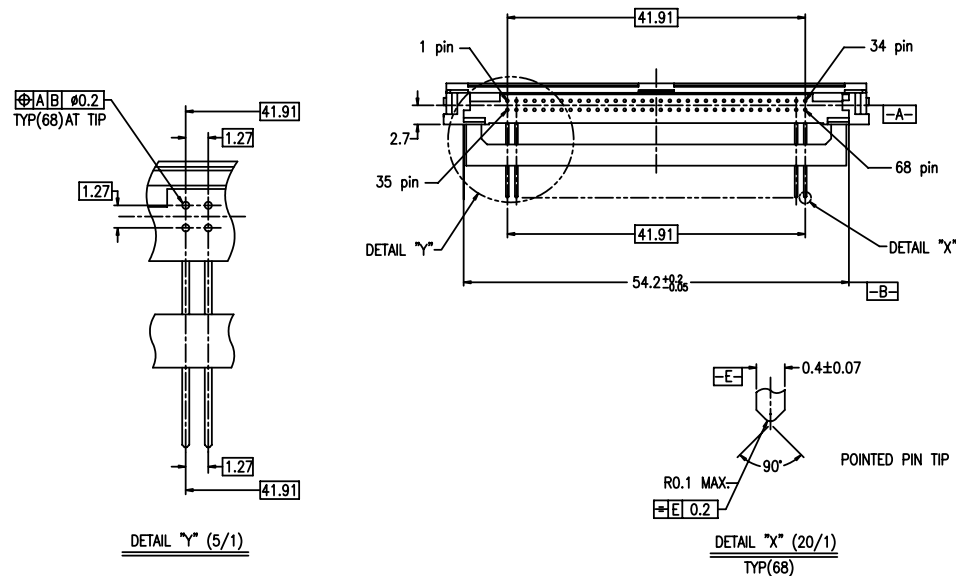


NOTES

- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL
HOUSING : LCP UL94V-0 BLACK.
PIN : PHOSPHOR BRONZE.
HOLD-DOWN : BRASS
- FINISH
PIN
UNDER PLATING : 0.5μm MIN. Ni.
CONTACT AREA : 0.1μm MIN. Au.
OVER 0.5μm MIN. Pb-Ni.
SOLDER AREA : 2.5μm MIN. Sn-Pb.
OR 2.5μm MIN. PURE Sn.(FOR -XXXLF)
HOLD-DOWN : 2.5μm MIN. PURE Sn.
- RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1
- SEQUENCE PIN ASSIGNMENT

PIN No.	DIM "L"		
	4.25±0.1	3.5±0.1	5.0±0.1
OTHERS	36.67	17.34	35.51.68

- GENERAL TOLERANCE : ±0.3
- SEE SHEET 1 REGARDING COMPONENTS.
- THIS PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008



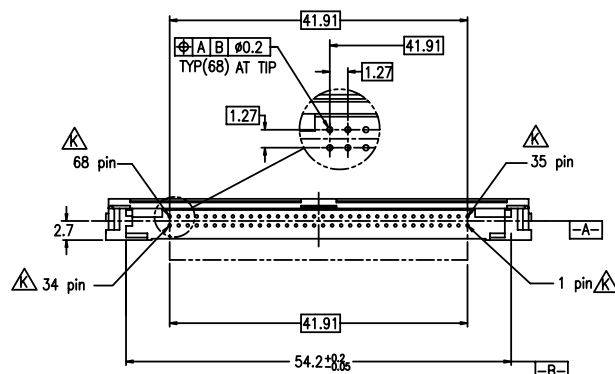
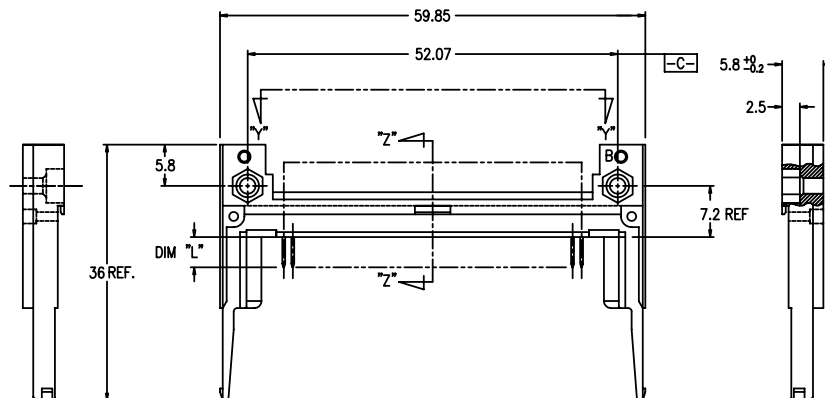
DF-003A REV F

DO NOT SCALE PRINT THIS INFORMATION IS CONFIDENTIAL AND IS DISCLOSED TO YOU ON CONDITION THAT NO FURTHER DISCLOSURE IS MADE BY YOU TO OTHER THAN FCI PERSONNEL WITHOUT WRITTEN AUTHORIZATION FROM FCI JAPAN K.K.

SHEET INDEX		ISSUE		SH NO.		DATE		CUSTOMER		TITLE	
TOLERANCES UNLESS OTHERWISE SPECIFIED		MAY		SEE NOTE 3		M.Hasegawa		2/24/05		68 POS EJECTOR HEADER ASSY	
LINEAR		FINISH		SEE NOTE 4		M.Hasegawa		2/24/05		Header PCMCIA SANF	
ANGLES		APPD								SCALE: 2/1 C	
										DWG. NO. 92140	
										REV F	
										SHEET 4 of 4	



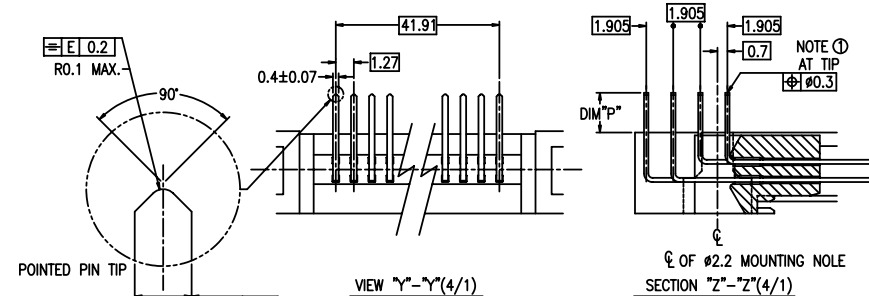
PRODUCT NO.
92140-500
92140-500LF 
92140-500H
92140-500HLF 
92140-501
92140-501LF 
92140-501H
92140-501HLF 
92140-502
92140-502LF 
92140-502H
92140-502HLF 
92140-503
92140-503LF 



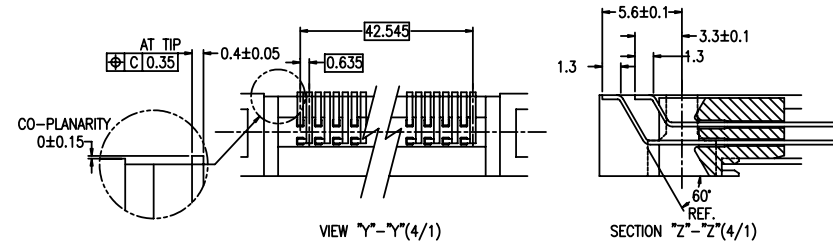
- NOTES
- ① TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
 2. SEE TA-946 FOR PCB LAYOUT.
 - ③ MATERIAL
 - HOUSING : LCP UL94V-0 NATURAL.
 - PIN : PHOSPHOR BRONZE.
 - HOLD-DOWN : BRASS
 - ④ FINISH
PIN
 - UNDER PLATING : 0.5μm MIN. Ni.
 - CONTACT AREA : 0.1μm MIN. Au.
 - OVER 0.5μm MIN. Pb-Ni.
 - SOLDER AREA : 2.5μm MIN. Sn-Pb.
 - OR 2.5μm MIN. PURE Sn.(FOR -XXXLF)
 - HOLD-DOWN : 2.5μm MIN. PURE Sn.
 5. RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3 ± 0.1
 6. SEQUENCE PIN ASSIGNMENT

	DIM "L"		
	4.25±0.1	3.5±0.1	5.0±0.1
PIN No.	OTHERS	36.67	1.17, 34 35.51, 68

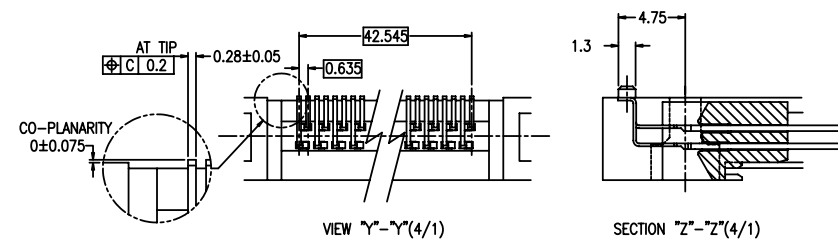
7. GENERAL TOLERANCE : ± 0.3
8. SEE SHEET 1 REGARDING COMPONENTS.
9. THIS PRODUCT MEETS EUROPEAN UNION
DIRECTIVES AND OTHER COUNTRY
REGULATIONS AS DESCRIBED IN GS-22-008



R/A (92140-500X)



SMT STAGGERED (92140-501X)

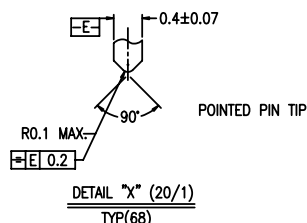
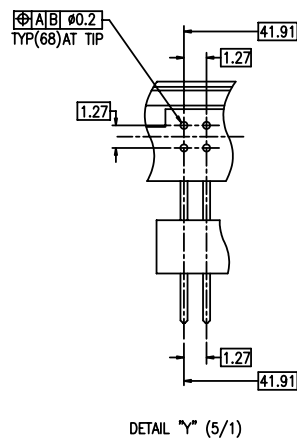
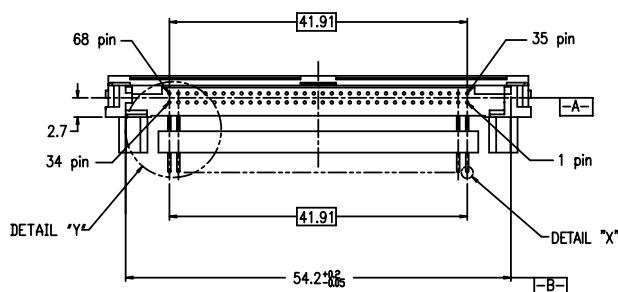
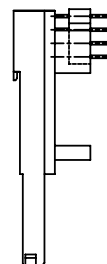
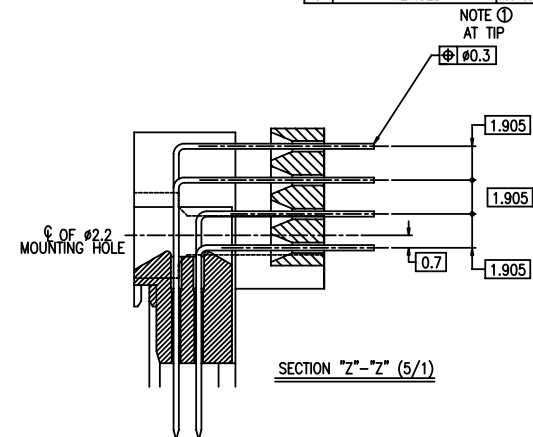
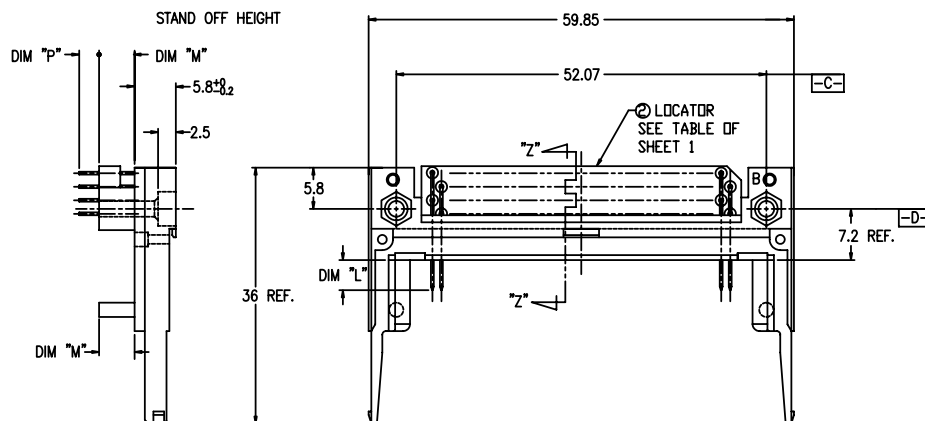


SMT IN LINE (92140-502X)

[illegible]

PRODUCT NO.	
92140-510	92140-510LF Δ
92140-520	92140-520LF Δ
92140-520H Δ	92140-520HLF Δ
92140-530	92140-530LF Δ
92140-540	92140-540LF Δ

REVISIONS				
REV	DESCRIPTION	ECN	BY	DATE
A	RELEASED	J30043	N.T.	2/18/93
B	REVISED	V32220	M.K.	2/16/94
C	REVISED	V32221	M.K.	2/28/94
D	REVISED	J40068	M.K.	3/ 2/94
E	REVISED	V40840	M.K.	7/11/94
F	REVISED	J50006	M.K.	2/ 9/95
G	REVISED	J50039	M.K.	3/ 1/95
H	REVISED	J03-0307	M.H.	7/16/03
J	REVISED	N05-0048	M.H.	2/24/05



- NOTES
- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
 - SEE TA-946 FOR PCB LAYOUT.
 - MATERIAL
 - HOUSING : LCP UL94V-0 NATURAL
 - PIN : PHOSPHOR BRONZE.
 - HOLD-DOWN : BRASS
 - FINISH
 - PIN
 - UNDER PLATING : 0.5μm MIN. Ni.
 - CONTACT AREA : 0.1μm MIN. Au.
 - OVER 0.5μm MIN. Pb-Ni.
 - SOLDER AREA : 2.5μm MIN. Sn-Pb.
 - OR 2.5μm MIN. PURE Sn.(FOR -XXXLF)
 - HOLD-DOWN : 2.5μm MIN. PURE Sn.
 - RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1
 - SEQUENCE PIN ASSIGNMENT

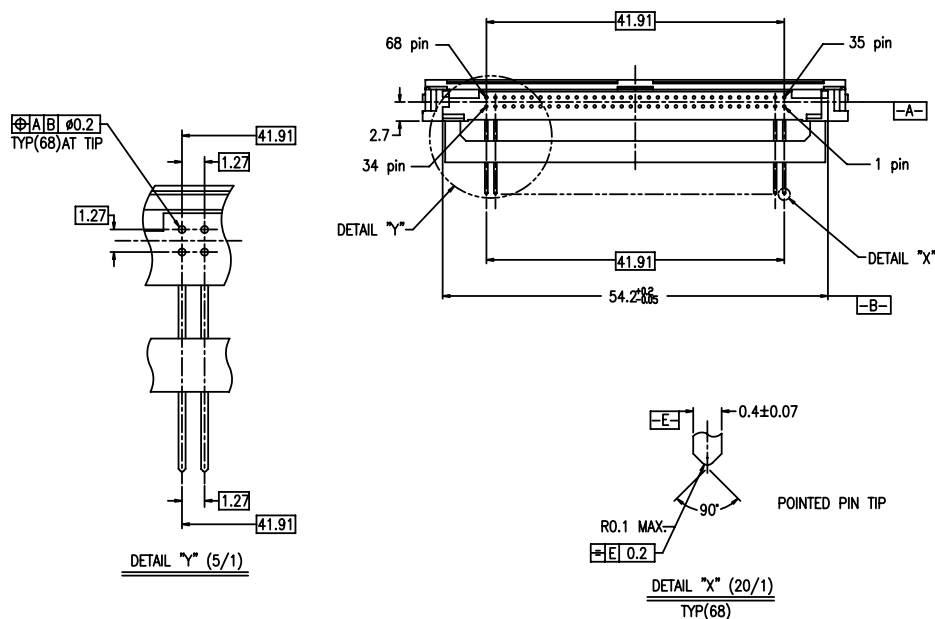
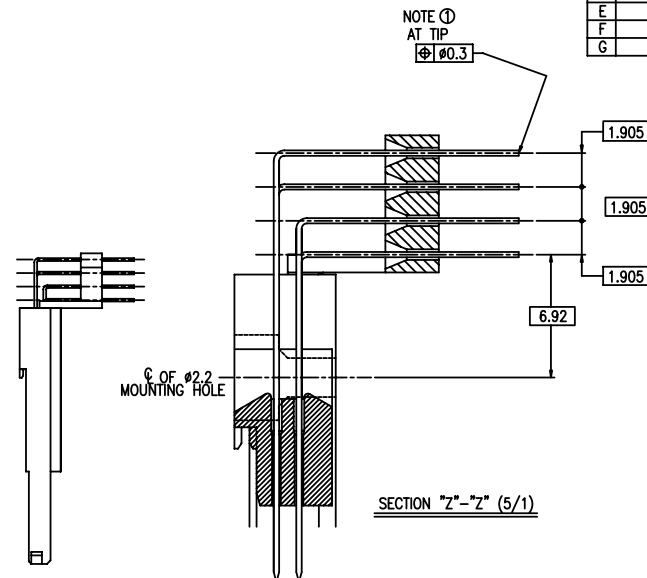
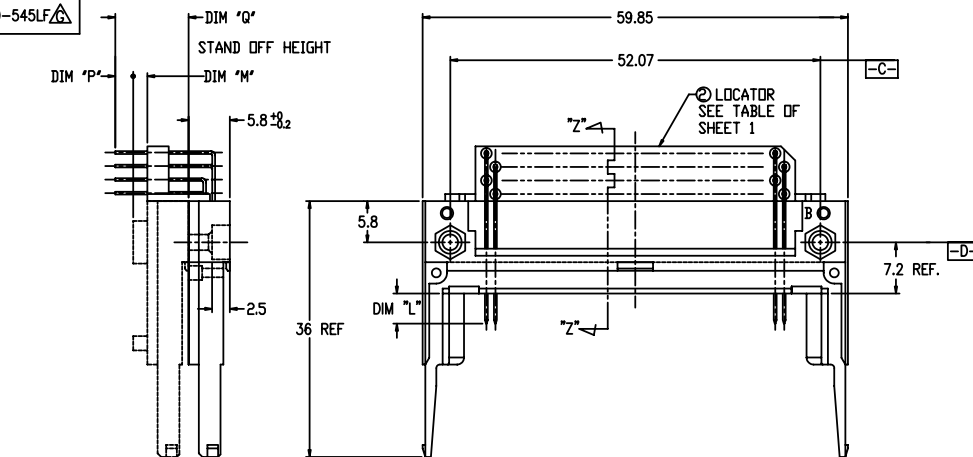
PIN No.	DIM "L"		
	4.25±0.1	3.5±0.1	5.0±0.1
OTHERS	1.734	36.67	35.51.88

- GENERAL TOLERANCE : ±0.3
- SEE SHEET 1 REGARDING COMPONENTS.
- THIS PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008

SHEET INDEX	ISSUE										TITLE	PRODUCT FAMILY	CODE JP
	SH NO.	MAT	DR	DATE	CUSTOMER COPY	THIRD ANGLE PROJ	SCALE	SIZE	DWG. NO.	REV			
TOLERANCES UNLESS OTHERWISE SPECIFIED		SEE NOTE 3	M.Hasegawa	2/24/05							68P EJECTOR HEADER ASSY	Header PCMCIA SANF	
LINEAR		FINISH SEE NOTE 4	CHK										
ANGLES			APPD										
											UNIT	MM	
											2/1	C	
											92140	J	6 of 6

PRODUCT NO.
92140-505
92140-505LF 
92140-535
92140-535LF 
92140-545
92140-545LF 

REVISIONS				
REV	DESCRIPTION	ECN	BY	DATE
A	RELEASED	J30043	N.T.	2/23/93
B	REVISED	J30157	Y.S.	5/20/93
C	REVISED	V32221	M.K.	2/28/94
D	REVISED	J40068	M.K.	3/ 2/94
E	REVISED	J50008	M.K.	2/ 9/95
F	REVISED	J50039	M.K.	3/ 1/95
G	REVISED	N05-0046	M.H.	2/24/05

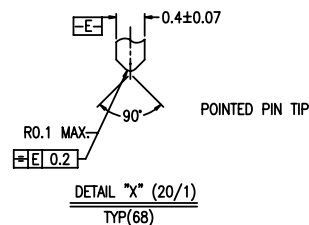


NOTES

- TRUE POSITION IS A FREE GRID TO PIN PATTERN ITSELF.
- SEE TA-946 FOR PCB LAYOUT.
- MATERIAL
 - HOUSING : LCP UL94V-0 NATURAL
 - PIN : PHOSPHOR BRONZE.
 - HOLD-DOWN : BRASS
- FINISH
 - PIN
 - UNDER PLATING : 0.5μm MIN. Ni.
 - CONTACT AREA : 0.1μm MIN. Au.
 - OVER 0.5μm MIN. Pb-Ni.
 - SOLDER AREA : 2.5μm MIN. Sn-Pb.
 - OR 2.5μm MIN. PURE Sn.(FOR -XXXLF)
 - HOLD-DOWN : 2.5μm MIN. PURE Sn.
- RECOMMENDED PCB HOLE (OPTIONAL HOLD-DOWN) : 2.3±0.1
- SEQUENCE PIN ASSIGNMENT

PIN No.	DIM "L"		
	4.25±0.1	3.5±0.1	5.0±0.1
OTHERS	36.67	1.17.34	35.51.68

- GENERAL TOLERANCE : ±0.3
- SEE SHEET 1 REGARDING COMPONENTS.
- THIS PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008



SHEET INDEX		ISSUE										TITLE			
SH NO.												68 POS EJECTOR HEADER ASSY			
TOLERANCES UNLESS OTHERWISE SPECIFIED		MAY SEE NOTE ③ 7/6/01/1100 DR M.Hasegawa/24/05 ENGR M.Hasegawa/24/05 DATE 7/6/01/1100 CUSTOMER COPY THIRD ANGLE PROJ UNIT MM SCALE 2/1 SIZE C DWG. NO. 92140 REV G SHEET 7 of 7										PRODUCT FAMILY Header PCMCIA SANF CODE JP			
LINEAR		FINISH SEE NOTE ④ 7/6/01/1100 APPD													
ANGLES															

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