

AOS Semiconductor Product Reliability Report

AO4726/AO4726L, rev A

Plastic Encapsulated Device

ALPHA & OMEGA Semiconductor, Inc

495 Mercury Drive Sunnyvale, CA 94085 U.S.

Tel: (408) 830-9742 <u>www.aosmd.com</u>

Mar 4, 2008



This AOS product reliability report summarizes the qualification result for AO4726. Accelerated environmental tests are performed on a specific sample size, and then followed by electrical test at end point. Review of final electrical test result confirms that AO4726 passes AOS quality and reliability requirements. The released product will be categorized by the process family and be monitored on a quarterly basis for continuously improving the product quality.

Table of Contents:

- I. Product Description
- II. Package and Die information
- III. Environmental Stress Test Summary and Result
- IV. Reliability Evaluation
- V. Quality Assurance Information

I. Product Description:

The AO4726 uses advanced trench technology with a monolithically integrated Schottky diode to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a low side FET in SMPS, load switching and general purpose applications.

Absolute Maximum Ratings T _A =25°C unless otherwise noted							
Parameter		Symbol	10 sec	Steady state	Units		
Drain-Source Voltage		V_{DS}	30		V		
Gate-Source Voltage		V_{GS}	±12		V		
Continuous Drain	T _A =25°C		18	13			
Current	T _A =70°C	I _{DSM}	14	10	Α		
Pulsed Drain Current		I _{DM}	80				
Avalanche Current		I _{AR}	42		Α		
Repetitive avalanche energy L=0.3mH		E _{AR}	265		mJ		
	T _A =25°C	- P _{DSM}	3.1	1.7	W		
Power Dissipation	T _A =70°C	DSM	2.0	1.1	"		
Junction and Storage Temperature Range		T _J , T _{STG}	-55 to 150		°C		

Thermal Characteristics						
Parameter		Symbol	Тур	Max	Units	
Maximum Junction-to- Ambient	T ≤ 10s	D	32	40	°C/W	
Maximum Junction-to- Ambient	Steady- State	$R_{ heta JA}$	60	75	°C/W	
Maximum Junction-to-Lead	Steady- State	$R_{ heta JL}$	17	24	°C/W	



II. Die / Package Information:

AO4726 AO4726L (Green Compound)

Process Standard sub-micron Standard sub-micron

Low voltage N channel process Low voltage N channel process

Package Type8 leads SOIC8 leads SOICLead FrameCu, S/pad, Ag spotCu, S/pad, Ag spot

Die Attach Ag epoxy Ag epoxy

Bond wire S: Cu 2mils, G: Au 1.3mils S: Cu 2mils, G: Au 1.3mils **Mold Material** Epoxy resin with silica filler Epoxy resin with silica filler

Filler % (Spherical/Flake)90/10100/0Flammability RatingUL-94 V-0UL-94 V-0Backside MetallizationTi / Ni / AgTi / Ni / AgMoisture LevelUp to Level 1 *Up to Level 1*

Note * based on info provided by assembler and mold compound supplier

III. Result of Reliability Stress for AO4726 (Standard) & AO4726L (Green)

Test Item	Test Condition	Time Point	Lot Attribution	Total Sample size	Number of Failures
Solder Reflow Precondition	Standard: 1hr PCT+3 cycle reflow@260°c Green: 168hr 85°c /85%RH +3 cycles reflow@260°c	Ohr	Standard: 83 lots Green: 29 lots	17380 pcs	0
HTGB	Temp = 150°c , Vgs=100% of Vgsmax	168 / 500 hrs 1000 hrs	3 lots (Note A*)	246 pcs 77+5 pcs / lot	0
HTRB	Temp = 150°c , Vds=80% of Vdsmax	168 / 500 hrs 1000 hrs	3 lots (Note A*)	246 pcs 77+5 pcs / lot	0
HAST	130 +/- 2°c , 85%RH, 33.3 psi, Vgs = 80% of Vgs max	100 hrs	Standard: 81 lots Green: 16 lots (Note B**)	5335 pcs 50+5 pcs / lot	0
Pressure Pot	121°c,29.7 psi, 100%RH	96 hrs	Standard: 83 lots Green: 20 lots (Note B**)	5665 pcs 50+5 pcs / lot	0
Temperature Cycle	-65°c to 150°c , air to air	250 / 500 cycles	Standard: 87 lots Green: 29 lots (Note B**)	6380 pcs 50+5 pcs / lot	0



III. Result of Reliability Stress for AO4726 (Standard) & AO4726L (Green) Continues

		5	5	
	NA	5	5	0
°c bake	0hr 250hr 500hr	40 40 40	40 wires 40 wires 40 wires	0
°C	5 sec	15	15 leads	0
	om Temp °c bake °c bake °c	om Temp Ohr °c bake 250hr °c bake 500hr	om Temp	om Temp Ohr 40 40 wires °c bake 250hr 40 40 wires °c bake 500hr 40 40 wires

Note A: The HTGB and HTRB reliability data presents total of available AO4726 and AO4726L burn-in data up to the published date.

Note B: The pressure pot, temperature cycle and HAST reliability data for AO4726 and AO4726L comes from the AOS generic package qualification data.

IV. Reliability Evaluation

FIT rate (per billion): 43 MTTF =2654 years

In general, 500 hrs of HTGB, 150 deg C accelerated stress testing is equivalent to 15 years of lifetime at 55 deg C operating conditions (by applying the Arrhenius equation with an activation energy of 0.7eV and 60% of upper confidence level on the failure rate calculation). AOS reliability group also routinely monitors the product reliability up to 1000 hr at and performs the necessary failure analysis on the units failed for reliability test(s).

The presentation of FIT rate for the individual product reliability is restricted by the actual burn-in sample size of the selected product (AO4726). Failure Rate Determination is based on JEDEC Standard JESD 85. FIT means one failure per billion hours.

Failure Rate = $\text{Chi}^2 \times 10^9 / [2 \text{ (N) (H) (Af)}] = 1.83 \times 10^9 / [2 (3 \times 164) (168) (258)] = 43$ **MTTF** = $10^9 / \text{FIT} = 2.32 \times 10^7 \text{hrs} = 2654 \text{ years}$

Chi² = Chi Squared Distribution, determined by the number of failures and confidence interval **N** = Total Number of units from HTRB and HTGB tests

H = Duration of HTRB/HTGB testing

Af = Acceleration Factor from Test to Use Conditions (Ea = 0.7eV and Tuse = 55°C)

Acceleration Factor [Af] = Exp [Ea / k (1/Tj u - 1/Tj s)]

Acceleration Factor ratio list:

	55 deg C	70 deg C	85 deg C	100 deg C	115 deg C	130 deg C	150 deg C
Af	258	87	32	13	5.64	2.59	1

Tj s = Stressed junction temperature in degree (Kelvin), K = C+273.16

Tj u =The use junction temperature in degree (Kelvin), K = C+273.16

k = Boltzmann's constant, 8.617164 X 10⁻⁵eV / K



V. Quality Assurance Information

Acceptable Quality Level for outgoing inspection: **0.1%** for electrical and visual. Guaranteed Outgoing Defect Rate: **< 25 ppm** Quality Sample Plan: conform to **Mil-Std-105D**