



Integrated Device Technology, Inc.

November/December 1996

Dear *Innovations* Readers:

Thank you for your interest in Integrated Device Technology. Please find the following information you requested in response to the November/December *Innovations* article on IDT's agreement with MoSys.

Response Kit Includes:

- IDT's MoSys/MDRAM press release
- IDT's Tom North press release
- IDT's Bob Proebsting press release

If you have any additional questions, please call our toll-free customer service hotline at (800) 345-7015. Again, thank you for reading *Innovations* and for your continued interest in IDT.

Sincerely,

Corporate Marketing

N E W S . R E L E A S E

FOR IMMEDIATE RELEASE

FOR FURTHER INFORMATION:

Diana Lorang, Corporate Communications Specialist

(408) 492-8210

Tom North, Vice President of DRAM Products

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IDT APPOINTS TOM NORTH VICE PRESIDENT OF NEW DRAM PRODUCTS DIVISION

SANTA CLARA, CA, October 29, 1996 —Integrated Device Technology, Inc. (IDT) recently named Tom North vice president of its newly-developed dynamic random access memory (DRAM) products division. In his new role, North executes IDT's Fusion Memory™ strategies and directs the product development of IDT's DRAM product line. He manages the engineering, design, production and marketing of high-performance DRAMs implemented in IDT's Fusion Memory technology for cache, graphics and other innovative memory applications.

North brings to IDT more than 20 years of semiconductor experience. Most recently, he served as the founder, chief executive officer and president of ShaBLAMM! Computer Inc., a WindowsNT™ workstation company that focused on the applications of high-speed main memory. North also gained semiconductor experience at Aspen, Cypress, Xicor, Mostek and Intel.

"Tom was the optimum choice to build IDT's DRAM division," said Len Perham, IDT's president and chief executive officer. "Tom's extensive experience in management, marketing and sales will help IDT develop yet another strong product area."

North received both his bachelor of science in electrical engineering and his master of business administration degrees from Stanford University and holds one patent in memory system design. He is 48 years old and resides in Los Altos, California.

Electronic Media Program

Information about IDT and its products is easily accessible through IDT's comprehensive electronic media program, which includes free CD-Roms by calling (800) 345-7015, a Home Page on the World Wide Web (<http://www.idt.com>), and fax-on-demand services by calling (800) 9-IDT-FAX. For more information about these services, call (800) 345-7015.

About IDT

Integrated Device Technology, Inc. (IDT) designs, manufactures and markets high-performance integrated circuits and modules that are used in products in four rapidly growing market segments: communications equipment, such as routers, hubs, switches, cellular base stations and other devices; distributed computing systems, such as workstations and servers; personal computers (PCs), desktop and notebook; and office automation equipment, such as laser printers and color copiers. IDT enhances the

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opportunity of its customers to optimize the cost and performance of their microprocessor-based systems by providing innovative solutions based on four product areas: high-speed SRAMs and Fusion Memory; communications products, including industry-leading FIFO memories, multi-port memories and Asynchronous Transfer Mode (ATM) products; RISC microprocessors, optimized for both embedded as well as reprogrammable applications; and high-performance logic. Headquartered in Santa Clara, Calif., IDT employs approximately 4,120 people worldwide.

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Fusion Memory is a trademark of Integrated Device Technology, Inc., WindowsNT is a registered trademark of Microsoft Corporation

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IDT NAMES ROBERT PROEBSTING, Ph.D., VICE PRESIDENT OF ADVANCED DESIGN CONCEPTS

SANTA CLARA, CA, October 29, 1996 —Integrated Device Technology, Inc. (IDT) recently appointed Dr. Robert (Bob) Proebsting vice president of advanced design concepts. In this position, Proebsting is responsible for providing technical leadership for the design of all memory products, primarily concentrating on static random access memories (SRAMs) and IDT's new direction into dynamic random access memory (DRAM) devices.

Proebsting's extensive history in DRAM includes defining the operating modes and introducing many of the circuit techniques used throughout the growth of the DRAM. He has spent over 28 years in integrated circuit design and research and development, and holds more than 40 U.S. patents that cover significant advances in metal oxide semiconductor integrated circuits (ICs).

Proebsting most recently served as assistant vice president of research and development for the semiconductor memory division at Hyundai Electronics America and as senior staff scientist for processor and memory products at Fairchild Semiconductor/Intergraph. Prior to that, Proebsting was a fellow and co-founder of Mostek Corporation and a branch manager at Texas Instruments.

Proebsting received his Ph.D. in electrical engineering and his master of science degree in physics from the University of Wisconsin. He received his bachelor of arts in physics and math at Knox College in Illinois.

Proebsting is a U.S. Masters swimmer and is the current defending world champion and world record holder for his age group in the 50, 100 and 200 meter butterfly and 100 meter freestyle events.

Proebsting and his wife, Arlene, live in Los Altos Hills, California. He has two children, two step-children and two grandchildren.

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IDT AND MOSYS TEAM TO DELIVER MDRAM TO MULTIMEDIA PC MANUFACTURERS

SANTA CLARA, Calif., October 29, 1996—Integrated Device Technology Inc. (IDT) and MoSys Inc. have signed an agreement for IDT to market, manufacture, and distribute worldwide Multibank dynamic random access memory (MDRAM) products for personal computer (PC) and workstation graphics and other ultra-high-performance memory applications.

The MDRAM, the world's fastest DRAM, was developed by MoSys. The agreement enables IDT to expand its industry-leading memory product portfolio with derivative MDRAM products for high-end communications and computing systems, including multimedia PCs. It gives MoSys an alternate source to expand the market for the MDRAM architecture and ensures high-volume availability to meet customers' increasing demands.

"This alliance fits in with IDT's increasing focus on the adoption of innovative, high-performance DRAM technology for applications in communications and computing," said Len Perham, IDT's president and chief executive officer. "IDT has the world-class manufacturing capabilities, a tradition of memory leadership and the technical expertise to implement our DRAM plans."

In a related announcement, IDT today introduced Tom North, vice president of its new DRAM products division and Robert Proebsting, vice president of advanced design concepts, to spearhead IDT's execution of the IDT/MoSys MDRAM agreement and develop products for other high-end memory applications as the trend toward high-performance graphics continues.

Ideal Graphics Solution

"The MDRAM has been widely recognized as the best memory architecture for PC graphics and other ultra-high-bandwidth yet cost-sensitive applications," said North. "Since PC graphics migrate to the highest performance of equal-cost solutions, the MDRAM business is now headed to the mainstream, high-volume market."

MDRAM has already demonstrated its power in high-end graphics cards. In combination with Tseng Labs' ET6000 accelerator chip, the pair has been touted by industry experts as a top-ranked graphics solution, offering exceptional video and gaming performance for leading-edge graphics boards.*

The peak bandwidth (greater than 660 Mbytes/second) available from the Multibank architecture exceeds the nearest competition by approximately one-third. More importantly, net bandwidth (bandwidth after accounting for overhead) beats competitive memory by 250 percent. Multibank

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MDRAMs are cost competitive with other mainstream DRAM architectures in the graphics multimedia market and use a simple, easily implemented interface.

IDT is the fourth MDRAM alternate source and also second sources the MCache™, a DRAM designed as a less expensive alternative to conventional pipelined burst cache SRAMs with equal performance and lower power. IDT plans to introduce MDRAM products in mid-1997. Production will be supplied by IDT's 0.35μ eight-inch FAB4 in Hillsboro, Oregon.

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About MoSys

MoSys Incorporated, headquartered in San Jose, CA., is a semiconductor memory architecture company and manufacturer of ultra-high-performance MDRAMs and MCache. Founded in September 1991 to develop semiconductor memory technologies and supply ultra-high-performance DRAMs, MoSys is both a fabless DRAM company and a technology licensor. MoSys builds and sells products based on its technologies as well as licenses the technology and products to leading DRAM suppliers around the world. MoSys' other manufacturing partners include: Taiwan Semiconductor Manufacturing Company (TSMC); Oki Electric Industry Co. Ltd. of Tokyo, Japan; and Siemens AG of Munich and Berlin, Germany, the later two of which also have rights to market and distribute MDRAMs worldwide. Products based on MDRAMs have won *Byte Magazine's* "Best of COMDEX" awards and *PC World Magazine's* "Best Buy" honors. Customers for the MDRAM include STB Systems (Dallas, TX), Hercules Computer Graphics (Fremont, CA) and Video Logic (UK) among other companies.

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* Source: PC Magazine, September 24, 1996

MDRAM, Multibank and MoSys are registered trademarks and MCache is a trademark of MoSys, Inc.

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